

Programmable Video Signal Generator

# VG-870B/871B/873/874

**Instruction Manual** 

Ver.2.70



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## VG-870B/871B/ 873/874

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2011.6

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## BEFORE OPERATING THE GENERATOR

## Introduction

Thank you very much for purchasing this model VG-870B/871B video signal generator.

This manual contains details on the operation procedures to be followed when the VG-870B/871B is used, the checkpoints and precautions to be observed, and so on. Improper handling may result in malfunctioning so before using the VG-870B/871B, please read through these instructions to ensure that you will operate the generator correctly.

After reading through the manual, keep it in a safe place for future reference.

## Safety precautions

## **AWARNING**

## Concerning the generator

- Do not subject the generator to impact or throw it. Doing so may cause the generator to malfunction, explode or generate abnormally high levels of heat, possibly resulting in a fire.
- Do not use the generator where there is a danger of ignition or explosions.
- Do not place the generator inside a microwave oven or other heating kitchen appliance or inside a high pressure vessel. Doing so may heat up the generator to abnormally high levels, cause smoking, running the risk of the generator's catching fire and/or damaging the circuit components.
- This generator contains some high-voltage parts. If you touch them, you may receive an electric shock and burn yourself so do not attempt to disassemble, repair or remodel the generator.
- If there is a thunderstorm while the generator is being used outdoors, immediately turn off its power, disconnect the power cable from the main unit, and move the generator to a safe place.

#### Concerning the power cord

- Always take hold of the molded part of the plug when disconnecting the power cord.
- Do not use force to bend the power cord or bunch it up for use. Doing so may cause a fire.
- Do not place heavy objects on top of the power cord. Doing so may damage the cord, causing a fire or electrical shock.

#### Concerning foreign matter

■ Do not spill liquids inside the generator or drop inflammable objects or metal parts into it. Operating the generator under these conditions may cause a fire, electric shocks and/or malfunctioning.

## **A** CAUTION

## Concerning the generator

■ When connecting the generator to a display unit, use the FG cable provided to connect the frame ground (FG) terminal on the generator to the frame ground terminal on the display unit. If these terminals are not connected together, the generator may fail. Take special care when connecting the generator to a display unit which is under development.



- When disconnecting the VG-870B/871B from the display unit, first disconnect the connecting cables, and then disconnect the FG cable.
- When the generator's power is to be turned ON or OFF, be absolutely sure to use the POWER switch on the front panel. Turning the power on and off by plugging in and unplugging the AC power cable may damage the PC card.
- Do not start using the generator straight away: instead, turn on the power of the VG-870B/871B and allow it to warm up for about 10 to 15 minutes before use so as to ensure that the VG-870B/871B will operate stably.
- It is forbidden to remove the video units from the generator main unit.
- A CompactFlash (CF) card slot is provided on the front panel. The LED at the side of the slot flashes while the data on the CF card is being accessed. Under no circumstances must the card be ejected while this LED is flashing. Otherwise, malfunctioning may result.
- Never unscrew and open the FC card slot (for the moving image module) cover on the side panel of the main unit while the main unit power is turned on. Malfunctioning may result if the cover or the screw should drop into the chassis of the main unit.

## **Concerning impact**

- This is a precision instrument and, as such, subjecting it to impact may cause malfunctioning. Take special care when moving the generator.
- Do not drop the generator.

#### Concerning installation

■ Install the generator in a stable location. Do not stand it on either of its side panels. Doing so may cause the generator's temperature to rise due to heat generation, possibly resulting in malfunctioning.

## When trouble or malfunctioning has occurred

■ In the unlikely event that trouble or malfunctioning should occur, disconnect the generator's power cable, and contact your dealer or an ASTRODESIGN sales representative.

## What is packed with the generator

The generator comes with the following items.

Be absolutely sure to use only the genuine accessories which are supplied with this generator since the use of any non-designated items may cause malfunctioning.

#### Standard accessories

- VG-870B/871B main unit
- CD with VG-870B/871B instruction manual (what you are now reading): 1 disc
- CompactFlash (CF) card: 1 pc
- CompactFlash (CF) card case: 1 pc
- SP-8870 software installation CD (for Windows): 1 pc
- SP-8870 instruction manual: PDF version (packed with the SP-8870 software installation CD)
- Power cable: 1 pc \*
- FG cable (1.5 meters long): 1 pc \*1
- \*1: These cables are designed to be used exclusively with the VG-870B/871B.

#### Optional accessories

• RB-1870:

Remote control box used exclusively \*2 with the VG-870B/871B

• RB-1871:

Simplified remote control box used exclusively \*2 with the VG-870B/871B

This remote control box is used exclusively for executing program data, timing data, pattern data and other operations so it cannot be used for setting operations.

\*2: These remote control boxes are not compatible with the existing VG series other than VG-870/871/870A/871A.

# **CONCERNING THE VG-870B/871B/873/874**

## 1.1 General description

The VG-870B/871B/873/874 video signal generator supports applications in every field of display test and measuring.

It features a high level of expandability which is achieved by the installing video output interface units.

## 1.2 Features

## 16-bit high-speed imaging engine

This generator features a maximum 16-bit × RGB high-gradation imaging engine. It even draws full HD images in an instant.

## ■ Wide dot clock frequency range

The VG-870B/873 support dot clock frequencies up to 340 MHz. The VG-871B/874 support a maximum dot clock frequency of 250 MHz for analog outputs and a maximum dot clock frequency of 340 MHz for digital outputs.

## ■ Windows-compatible editing and registration software (SP-8870) provided as standard accessory

This software can be used to edit and register the program data and exercise control over the signals output from the PC connected to the RS-232C/LAN/USB connector.

#### Full variety of sample data incorporated inside

A total of a thousand types of timing data and a thousand types of pattern data are registered inside the VG-870B/871B as sample data. They are categorized by standard, application and other factors, and it is possible for the data required to be selected easily.

#### Registration of program data on PC cards

A total of a thousand program data can be registered on a PC card. PC screens or natural images can also be registered. On a PC equipped with a PC card slot, the data can be copied using Explorer provided with Windows 98SE, Windows 2000 or Windows XP.

#### Creation of user option patterns

In addition to the existing basic patterns (including character, crosshatch, color bar and gray scale) and optional patterns, a function that allows users to create their own optional patterns has been added. This function makes it possible to create the optional patterns which are useful for developing and evaluating the next-generation displays.

#### ■ Selection and installation of up to three video units possible

In line with the operating environment, users can select up to three kinds of video units from the six kinds of output units available. In addition, a multiple number of video units of the same kind can be installed, and Astrodesign also provides units which are customized to the needs of the users.

#### Output of uncompressed movies

10-bit uncompressed movies can be output from the output unit which the user has selected.

 When units are to be added or replaced, please contact ASTRODESIGN.

## 1.3 Data configuration

The data output by the VG-870B/871B is managed by the program data.

The program data consists of the pattern data which is used to set the data relating to the output images and the timing data which is used to set the data relating to all other output timing data and output conditions.

The table below gives a breakdown of the data.

Block		Description
Timing data Program Name		Program name
	Timing	Timing
	Output	Output condition
	AUDIO	Audio output
Pattern data	Pattern	Pattern
	Action	Pattern action

A number of types of program data, optional patterns and user character patterns are contained as sample data inside the VG-870B/871B.

	Number of data
Timing data	1000 (Timing # 1001 to 2000)
Pattern data	1000 (Pattern # 1001 to 2000)
Optional patterns	200 (1 to 200)
User character patterns	16 (F0H to FFH)

The various data can be registered in the <u>internal memory (approx. 100 MB)</u> of the VG-870B/871B/873/874 or on CF cards.

	Number of data		
Program data	1000 (Program #	1 to	1000)
User option patterns	200 (1 to 200)		
Images (image data)	200 (1 to 200)		
			epends on the image data size, and card capacity.
User character patterns	16 (E0H to EFH)		
Number of characters in program names	20 characters		
Number of groups	99 (1 to 99)		For further details on groups, refer to
Number of group data	98 (1 to 98)	"2.4 Groups."	"2.4 Groups."
Number of characters in group names	20 characters		



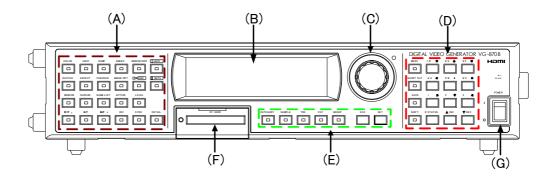
When a CF card has been inserted, the data registered on that card becomes valid, and the data registered in the internal memory becomes invalid.

In the case of image data, both the data on a CF card and the data in the internal memory can be made valid. \*

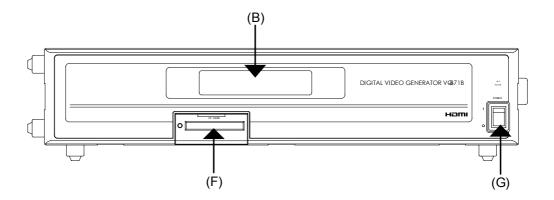
\* For further details, refer to "9.1.11 Image - priority settings."

## 1.4 Panel parts and their functions

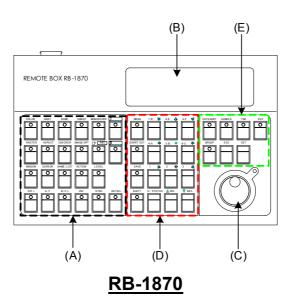
## 1.4.1 VG-870B/873 front panel

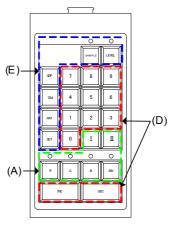


## 1.4.2 VG-871B/874 front panel



## 1.4.3 RB-1870/RB-1871 panel





RB-1871

Some restrictions apply to operating the RB-1871. The operable items are described below.

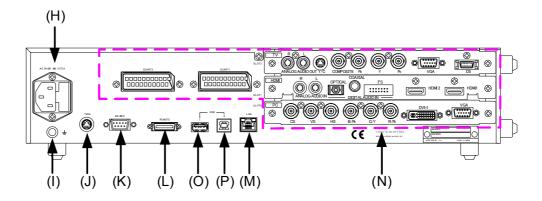
- Selecting and executing programs
- Execution of grouped programs (but group editing is not possible)
- ON/OFF operations of CUSTOM (I, II) keys (default = I: HDCP, II: MUTE)
- Adjustment of levels (digital video levels only)
- For further details on the keys, refer to "1.4.4 Names of the keys and their functions."

## 1.4.4 Names of the keys and their functions

			[
(A)	Pattern keys	COLOR DX ABC etc.	Used to display and edit the patterns.
	Action key	ACTION	Used when setting the scroll, flicker and other functions.
	Level key	LEVEL	Used to set the digital video levels, analog video levels and audio levels.
	RGB channel on/off	etc.	Used to set R, G and B on or off.
	INV key	INV	Used to invert the black and white of the video levels.
	SYNC key	SYNC	Used to set the sync on or off.
	Detail key	DETAIL	Used to perform the detailed settings of the pattern data, timing data, etc.
	HDCP key (custom key)	I.HDCP	Used to set HDCP on or off. (HDCP is a system for protecting content used by HDMI and DVI.)
	MUTE key (custom key)	II MUTE	* When setting HDMI AV-MUTE This key functions as the HDMI AV-MUTE On/Off setting. (Refer to "9.1.17 CUSTOM Key1, 2, RB-1871 CUSTOM Key".)
(B)	Menu operation screens	Program Edit Program Edit Program Edit State Coffetti State Coffetti State Coffetti State Coffetti State Coffetti State	The menu screens are used to set and check the items displayed on the fluorescent display tube.
(C)	Rotary switch		This is turned clockwise or counterclockwise to select the setting items or parameters, change the level settings, etc.
(D)	Number keys	0/STATUS 9/F 為	Used to input numerical values, select the menus, etc.
	INC/DEC	∆ INC  DEC	Used to select the setting items or parameters, change the level settings, change the program numbers, etc.
	Menu	MENU	Used to display the menu screens. When it is pressed while a menu screen is already displayed, the initial screen is restored.
			When the menu key indicator is lighted     It is no longer possible to use any of the other keys.
	Short-cut key	SHORT CUT	Used to move to a user-registered menu screen using minimal key operations.
	Save key	SAVE	Used to save the data which has been set.
	Shift key	SHIFT	Used to input letters of the alphabet with the number keys.

(E)	Category key	CATEGORY	Used to select the internal sample data by category.
	Sample key	SAMPLE	Used when the internal sample data is used.
			* When the sample key indicator is off
			The data stored on CF cards or stored in the internal memory can be used.
	Timing key	TIM	Used to display changeable lists when only the output timing data is to be changed.
	Pattern key	PAT	Used to display changeable lists when only the output pattern data is to be changed.
	Group key	GROUP	Used to display user-registered groups, etc. and create groups.
	Escape key	ESC	This key can be used in the following situations
			When canceling parameter selections or numerical value settings
			When returning the displayed menu screen to the previous hierarchical level
	Set key	SET	Used to enter the setting items and parameters which have been set.
(F)	CF card slot		Used for inserting a CF card or accessing the memory on a CF card.
(G)	Power switch		Used to turn the power of the VG-870B/VG-871B on and off.

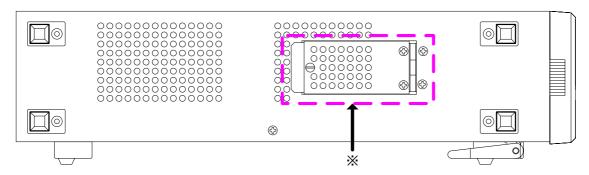
## 1.4.5 VG-870B/871B/873/874 rear panel



## 1.4.6 Names of connectors and their applications

(H)	AC power socket		Connect the power cable here.
			Any voltage from 100 V to 240 V is supported.
(I)	Frame ground	_ ±	Connect this frame ground terminal to the frame ground terminal of the unit which is connected to the VG-870B/871B.
(J)	TRIG connector		This is the trigger input/output connector.
(K)	RS-232C connector	RS - 232C	This is used to connect a personal computer using an RS-232C cable.
(L)	Remote connector	REMOTE S	This is used to connect the dedicated remote control box (RB-1870 or RB-1871) to operate the generator by remote control.
(M)	LAN port	LAN	This port is used for connection to a LAN using the Ethernet cable.
(N)	Units		These connectors enable up to three interface units (VM18XX series) to be installed.
(O)	USB (1)	1	This connector supports a regular USB mouse.      When the cursor is displayed     Using the USB mouse, the pointer on the monitor can be moved.
(P)	USB (2)	2	This connector is used to connect the generator with a PC to enable the VG-870B/871B to be operated using the SP-8870 software, etc. Refer to the instruction manual of the SP-8870 software for further details.

## 1.4.7 VG-870B/871B/873/874 side panel



\* Note

Open or close this cover when inserting or removing a CF card for the moving image module. Do not open the cover while the power is on.

## 1.4.8 Tools used to operate the VG-870B/871B

The table below lists the operation tools of this generator and the restrictions on the operation of each of these tools.

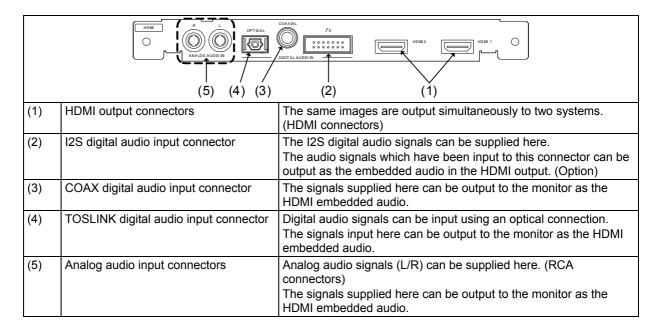
Operation tool	Restriction on operation	Remarks
VG-870B/873 front panel	These enable all the generator functions to be operated.	The controls can be used only by the VG-870B/873 main unit.
RB-1870	These enable all the generator functions to be operated.	This remote control box makes it possible to perform the same operations as the ones which are performed on the front panel of the VG-870B/873.
RB-1871	Programs can be read only.	This is a simplified remote control box which is intended for use on production lines.
SP-8870	These enable all the generator functions to be operated.	This software program is intended for performing operations and editing using a PC.

## 1.5 VG-870B/871B/873/874 video units

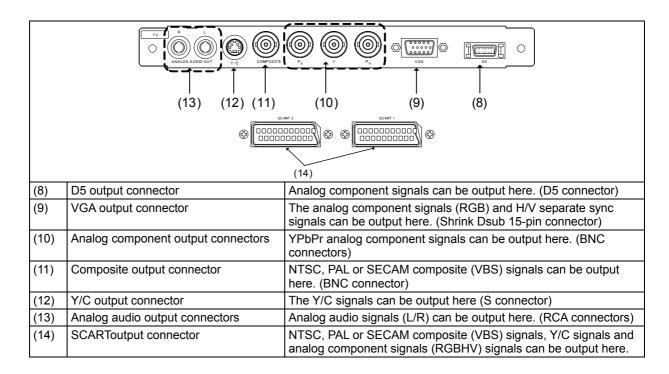
The VG-870B/871B/873/874 is constructed to allow video interface units to be installed. Video interface signals are output from these units. Up to three units can be installed.

\* When one or more video units are to be replaced, please contact ASTRODESIGN.

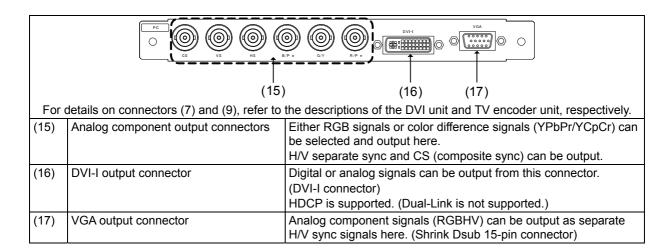
## 1.5.1 HDMI unit (VM-1817)



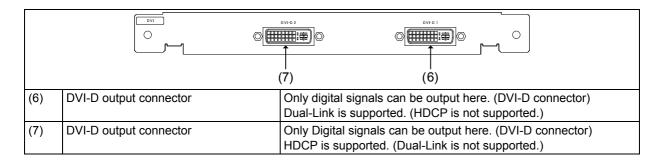
## 1.5.2 TV encoder unit



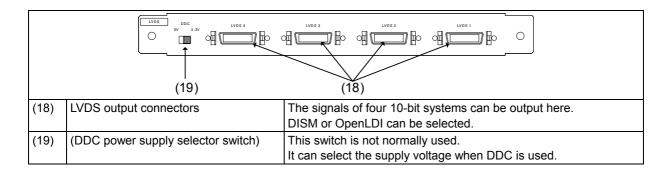
## 1.5.3 PC analog unit



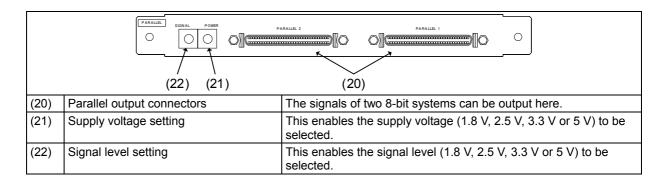
#### 1.5.4 **DVI** unit



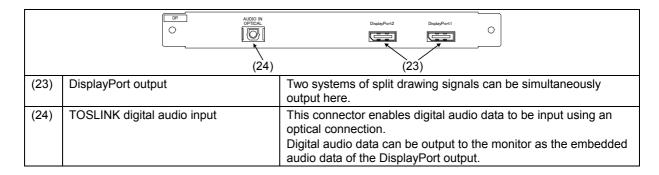
### 1.5.5 LVDS unit



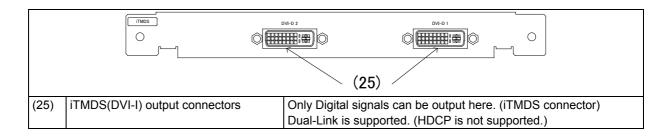
### 1.5.6 Parallel unit



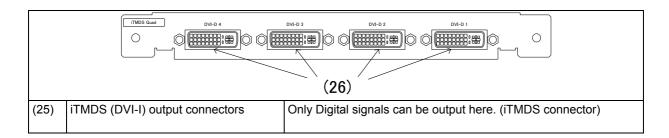
## 1.5.7 DisplayPort unit



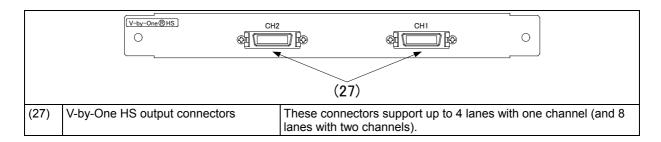
## 1.5.8 4K2K (iTMDS) unit



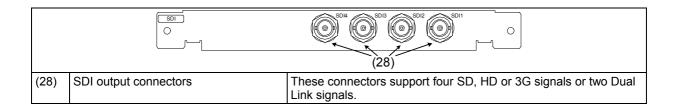
## 1.5.9 4K2K (iTMDS Quad) unit



## 1.5.10 V-by-One HS unit



## 1.5.11 SDI unit



## 1.5.12 HDMI 1.4 (ARC-compatible) unit

	(32) (31	) (30) (29)
(29)	HDMI output connectors	The same images and sound can be output simultaneously from these two connectors. (HDMI connectors)
(30)	I2S digital audio input connector	The I2S digital audio signals can be supplied here.  The audio signals which have been input to this connector can be output as the embedded audio in the HDMI output. (Option)
(31)	COAX digital audio input connector	The signals supplied here can be output to the monitor as the HDMI embedded audio.
(32)	COAX digital audio output connector	The sound input using the ARC function of HDMI can be output in its original form from this connector.

## 1.5.13 HDMI(300MHz supported) Unit for VG-873 / 874

	(37) (3	15
(33)	HDMI Output	The same images and sound can be output simultaneously from these two connectors. (HDMI connectors)
(34)	LAN	When using HEC function, the Ethernet that is input from VG is thoroughly output.
(35)	I2S digital audio input connector	The I2S digital audio signals can be supplied here.  The audio signals which have been input to this connector can be output as the embedded audio in the HDMI output. (Option)
(36)	COAX digital audio input connector	The signals supplied here can be output to the monitor as the HDMI embedded audio.
(37)	COAX digital audio output connector	The sound input using the ARC function of HDMI can be output in its original form from this connector.



Do not attempt to remove the video units from the main unit due to the risk of damaging them. When units are to be added or replaced, please contact ASTRODESIGN.

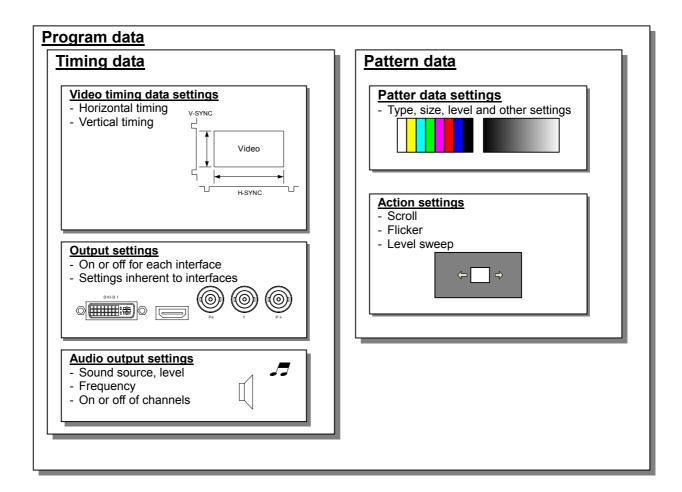
## **OPERATION PROCEDURES**

## 2.1 Flow of basic operations

## 2.1.1 Settings required for displays

The **timing data** and **pattern data** must be set in order for the test patterns to be displayed from the generator. The following items are set for these data.

In the case of this generator, the **timing data** and **pattern data** are collectively referred to as the "**program data**."

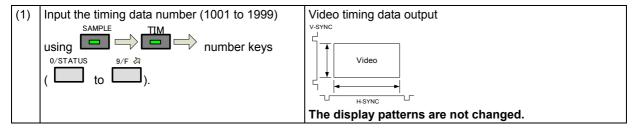


## 2.1.2 Selecting the timing data

There are two ways to select the timing data.

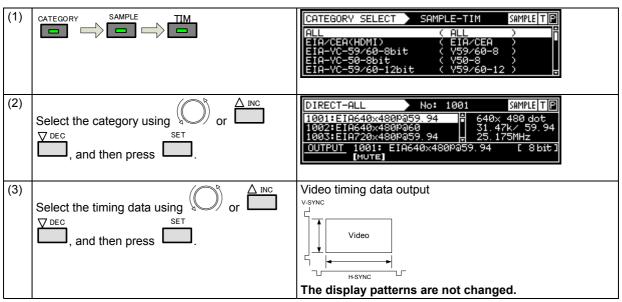
- 1) Input the timing data numbers directly.
- 2) Select the timing data from the categories.

## 1) Input the timing data numbers directly.



### 2) Select the timing data from the categories.

The timing data of the internal sample data is classified by category such as EIA or VESA (PC). Select the desired timing data from the category which contains it.

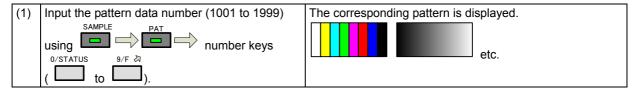


## 2.1.3 Selecting the pattern data

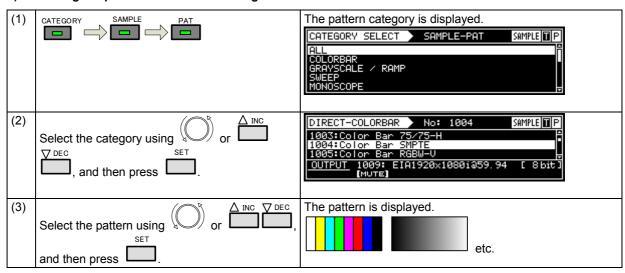
There are four ways to select the pattern data.

- 1) Input the pattern data numbers directly.
- 2) Select the pattern data from the categories.
- 3) Select the pattern data using the pattern keys.
- 4) Select the patterns for each program data.

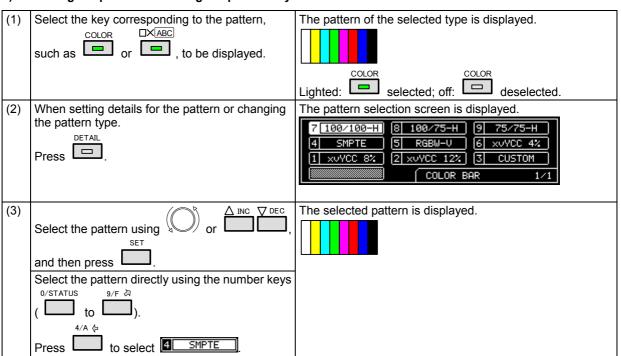
#### 1) Inputting the pattern data numbers directly



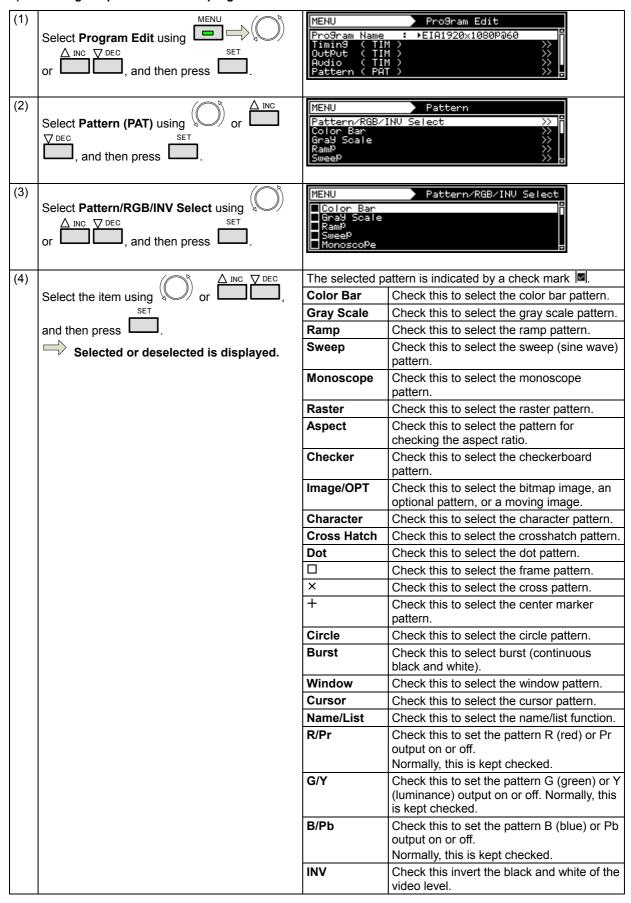
#### 2) Selecting the pattern data from the categories



#### 3) Selecting the pattern data using the pattern keys



#### 4) Selecting the patterns for each program data

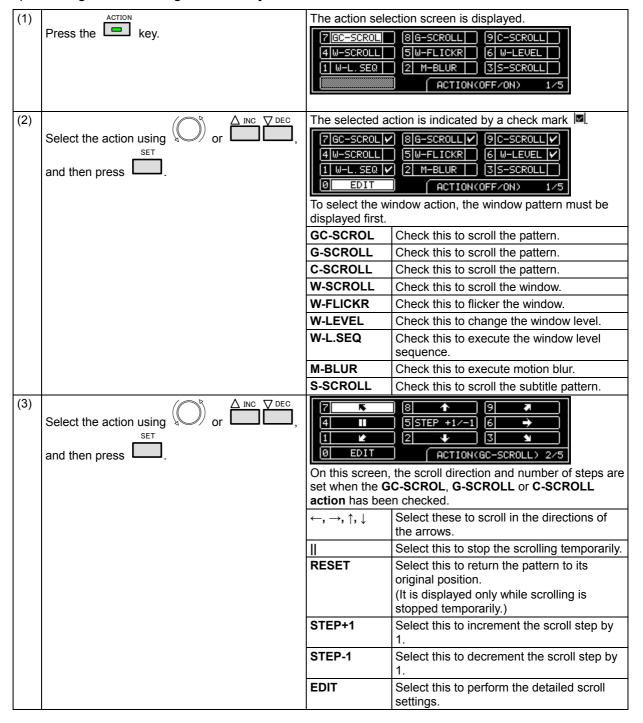


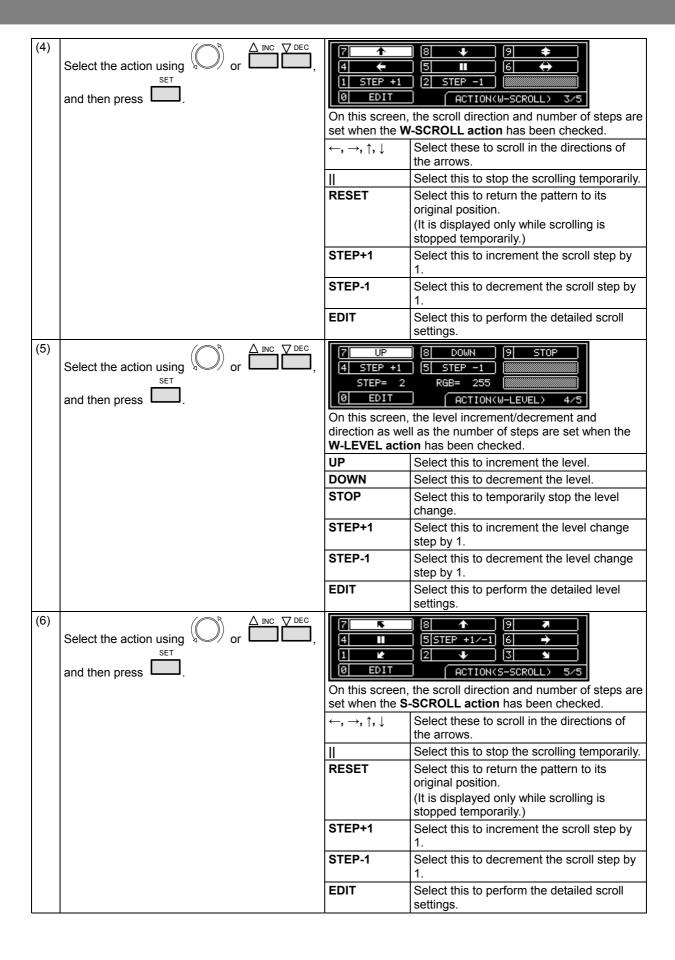
### 2.1.4 Selecting the actions

There are two ways to select actions.

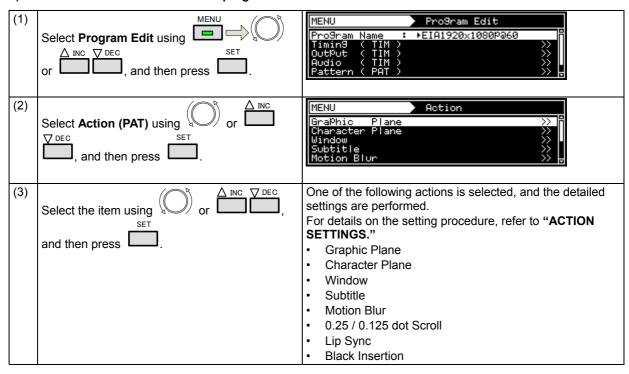
- 1) Select the action using the action key.
- 2) Select and set the action for each program data.

#### 1) Selecting the action using the action key





#### 2) Select and set the action for each program data.



# 2.2 Saving the program data

Upon completion of program editing, save the data.

If the power is turned off without saving the data, the status before the changes were made will be restored.

(1)	SAVE	Pro9ram Data SAVE  No. > Media : 1 > Internal  Pro9ram Name: ►EIA1920x1080i359.94  Pattern Name: Color Bar SMPTE    EXECUTE <		
(2)	Select the item using $\circ$ or $\circ$ inc $\circ$ dec $\circ$ and then press $\circ$ .	No.	The program number is set here. (0001 to 1000)	
		Media	The internal memory or CF card is selected here.	
	and then press ——.	Program Name	Any name (containing up to 20 characters) can be allocated as the program name.	
		Pattern Name	Any name (containing up to 20 characters) can be allocated as the pattern name.	
(3)	Select EXECUTE < using or Inc	The program data is saved.		
	, and then press			



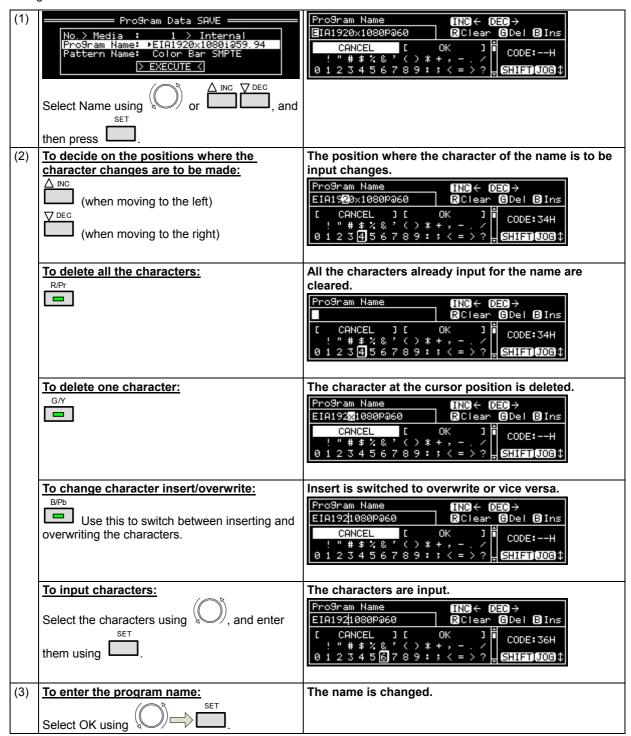
If a CF card has been inserted, the data registered on the card will be enabled and the data registered in the internal memory will be disabled.

For the image data, it is possible to enable both the data registered on the CF card and the data in the internal memory.

\* For further details, refer to "9.1.11 Image - priority settings."

# 2.3 Setting the names

When timing or pattern data is edited and the edited data is to be saved, the name used for the program can be changed.

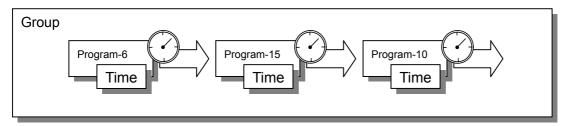


# 2.4 Groups

Registering programs as "**groups**" is useful when specific programs are to be combined and used repeatedly.

Examples include times on a TV set inspection process when specific timing and pattern data are combined for repeated use.

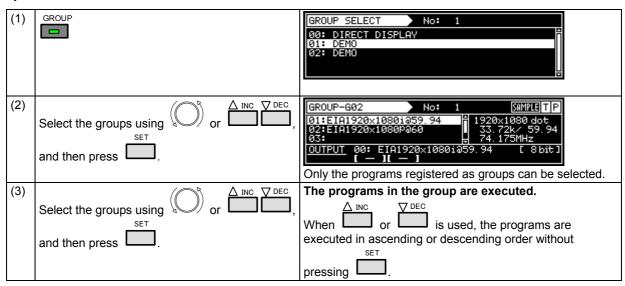
It is possible to set not only the timing and pattern data execution sequence but the execution time of each program as well.



Up to 98 programs can be registered in a group. Up to 99 groups can be registered.

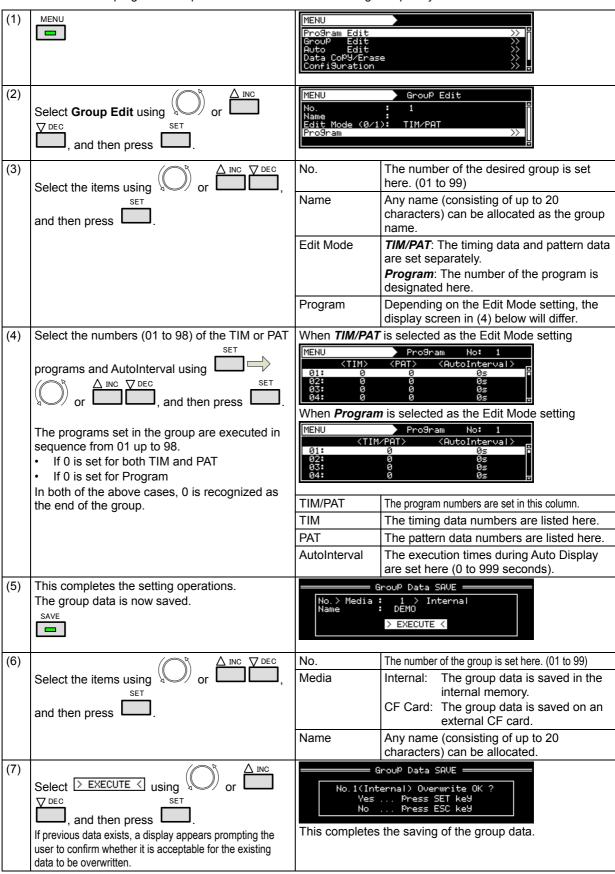
# 2.4.1 Executing groups

The combinations of programs and patterns which are used with a high frequency and which have been registered by the user can be executed.



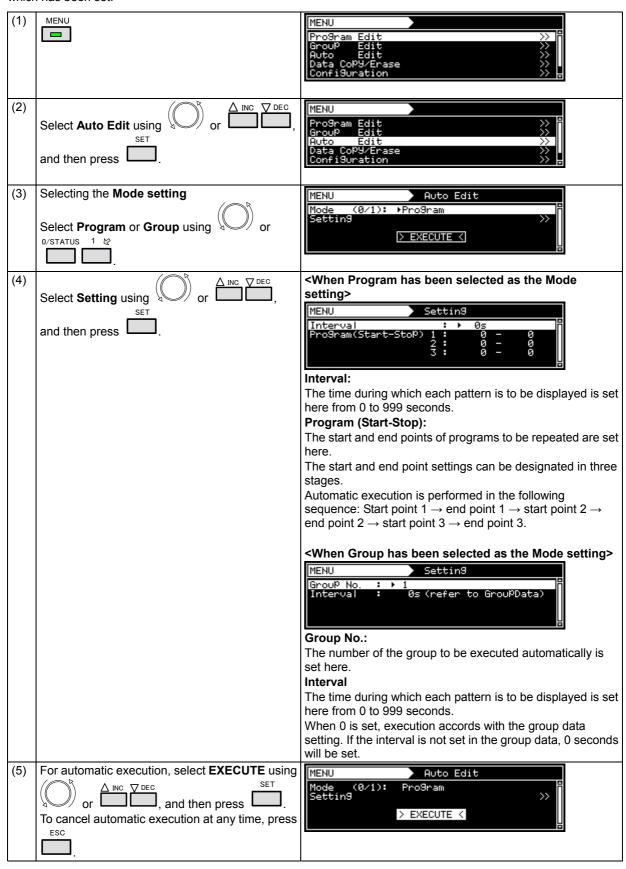
# 2.4.2 Setting and saving groups

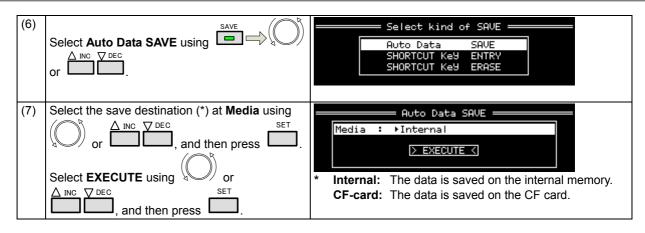
The combinations of programs and patterns which are used with a high frequency can be saved.



# 2.5 Automatic execution

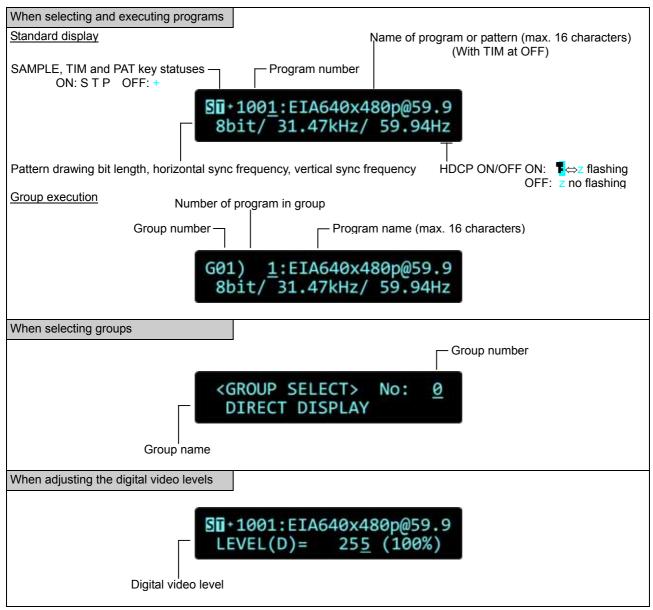
The data in the selected groups and program numbers can be automatically output in accordance with the delay time which has been set.





# 2.6 Displays appearing on the VG-871B / 874 fluorescent display tube

The following information appears on the VG-871B/874 fluorescent display tube.

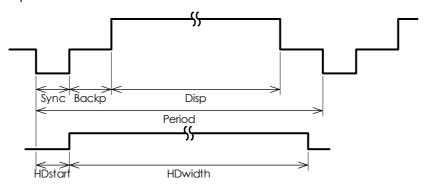


# **TIMING DATA SETTINGS**

# 3.1 Horizontal timing data editing

### 3.1.1 Horizontal timing data

When making changes with the horizontal timing data, the parameters which can be set and the names of the parameters are indicated below.



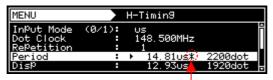
### 3.1.2 Restrictions on the horizontal timing parameters

The table below shows the restrictions on the parameters which can be changed with the horizontal timing data.

Setting item	Setting range	Parameter fixing function
Dot Clock	Depending on the number of bits which are output, the dot clock frequency is subject to some restrictions. For details, refer to "11.1.1 Common specifications."	
Priod	Time display: 0.00 to 999.999 µs Dot display: 128 to 8192 dots	μs setting fixed using SHIFT+2 dot setting fixed using SHIFT+3
Disp	Time display: 0.00 to 999.999 µs Dot display: 48 to 4096 dot	μs setting fixed using SHIFT+0 dot setting fixed using SHIFT+1
Backp, Sync	Time display: 0.00 to 999.999 µs Dot display: 0 to 8192 dot	
HDstart, HDwidth	Time display: 0.00 to 999.999 µs Dot display: 0 to 8190 dot	

\* When a parameter is fixed, it is accompanied by an asterisk (\*). Even when values other than ones for parameters with an asterisk have been changed, the values of the parameters with the asterisks remain fixed

Example: When a us setting has been fixed using SHIFT+2 for Period



An asterisk is displayed here when the value is fixed.



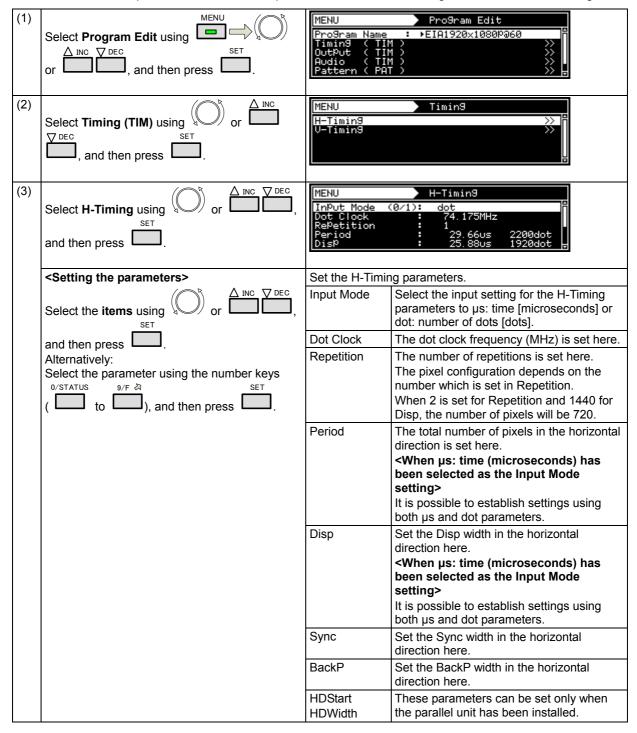
When items are set in microseconds (µs), restrictions apply to these settings depending on the dot clock frequency and other timing data used for drawing.

The values for the blanking and frontp items are calculated automatically on the basis of the data presented above.

Item	Calculation formula	Setting range
Blanking	Blanking = Period - Disp	Time display: 0.00 to 999.999 µs Dot display: 40 to 8192 dots
Frontp	Frontp = Period - Disp - Sync - Backp	Time display: 0.00 to 999.999 µs Dot display: 0 to 8192 dots

#### 3.1.3 Horizontal timing data setting procedure

Described below is the procedure used to set the parameters which can be changed with the horizontal timing data

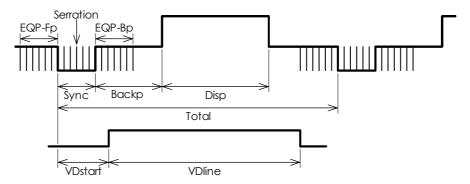


# 3.2 Vertical timing data editing

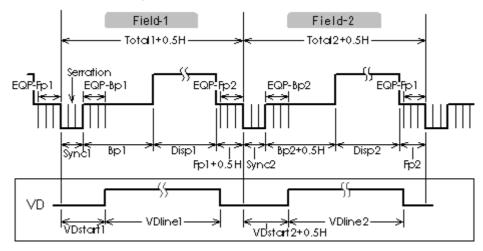
The vertical timing setting locations and names of the settings are indicated below.

#### 3.2.1 Vertical timing data

[For progressive scanning]



#### [For interlaced scanning]



# 3.2.2 Restrictions on the vertical timing parameters

The table below shows the restrictions on the parameters which can be changed with the vertical timing data.

#### <For progressive scanning>

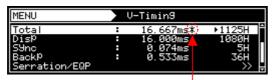
Setting item	Setting range	Parameter fixing function
Total	Time display: 0.00 to 999.999 ms Dot display: 8 to 8192 H	ms setting fixed using SHIFT+2 H setting fixed using SHIFT+3
D'	. ,	<u> </u>
Disp	Time display: 0.00 to 999.999 ms	ms setting fixed using SHIFT+0
	Dot display: 2 to 4096 H	H setting fixed using SHIFT+1
Sync	Time display: 0.00 to 999.999 ms	
	Dot display: 1 to 99 H	
Backp	Time display: 0.00 to 999.999 ms	
	Dot display: 1 to 8192 H	
VDstart , VDline	Time display: 0.00 to 999.999 ms	
	Dot display: 0 to 8190 H	

#### <For interlaced scanning>

Setting item		Setting range	Parameter fixing function
Field-1	Total1	Time display: 0.00 to 999.999 ms	Fixed to ms setting using SHIFT+2
		Dot display: 4.0 to 4096.0 H	Fixed to H setting using SHIFT+3
		(in 0.5H increments)	
	Disp1	Time display: 0.00 to 999.999 ms	Fixed to ms setting using SHIFT+0
		Dot display: 1 to 2048 H	Fixed to H setting using SHIFT+1
	Sync1	Time display: 0.00 to 999.999 ms	
		Dot display: 1.0 to 99.0 H	
		(in 0.5H increments)	
	Backp1	Time display: 0.00 to 999.999 ms	
		Dot display: 0.0 to 4096.0 H	
	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	(in 0.5H increments)	
	VDstart1		
		Dot display: 0.0 to 4095.0 H (in 0.5H increments)	
	VDline1		
	VDIIIle	Time display: 0.00 to 999.999 ms Dot display: 0.0 to 4095.0 H	
		(in 0.5H increments)	
Field-2	Total2	(iii e.e.i iiieieiiieiie)	
11010.2	Disp2		
	Sync2		
Backp2		Same as <b>Field-1</b>	Same as Field-1
	VDstart2		
	VDline2		

\* When a parameter is fixed, it is accompanied by an asterisk (\*). Even when values other than ones for parameters with an asterisk have been changed, the values of the parameters with the asterisks remain fixed.

Example: When an ms setting has been fixed using SHIFT+2 for Total



An asterisk is displayed here when the value is fixed.



When the time display (ms) is set for the items, restrictions apply to these settings depending on the H-period and other timing data used for drawing.

The values for the blanking and frontp items are calculated automatically on the basis of the data presented above.

#### <For progressive scanning>

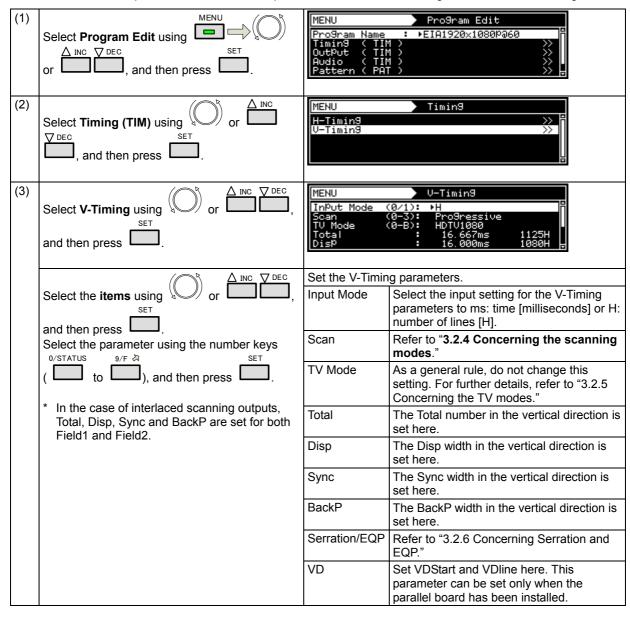
Item	Calculation formula	Setting range
Blanking	Blanking = Total - Disp	Time display: 0.00 to 999.999 ms Dot display: 2 to 8192 H
Frontp	Frontp = Total - Disp - Sync - Backp	Time display: 0.00 to 999.999 ms Dot display: 0 to 8192 H

#### <For interlaced scanning>

Item	Calculation formula	Setting range
Frontp1 (Frontp2)		Time display: 0.00 to 999.999 ms Dot display: 0.0 to 4096.0 H
Blanking1 (Blanking2)	, , , , ,	Time display: 0.00 to 999.999 ms Dot display: 2.0 to 4096.0 H

# 3.2.3 Vertical timing data setting procedure

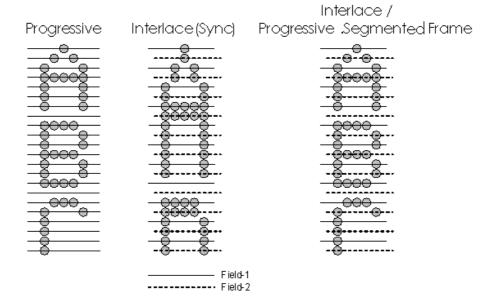
Described below is the procedure used to set the parameters which can be changed with the vertical timing data.



# 3.2.4 Concerning the scanning modes

The table below lists the V-Timing scan settings as well as the operations for the scanning methods supported by the settings, imaging methods and action settings.

Scan mode	System	Pixel imaging	Scroll and other actions
Progressive	Progressive scanning	Different pixels are drawn on each line.	Operation is performed for each frame.
Interlace	Interlaced scanning	Different pixels are drawn in the first field and second field.	Operation is performed for each field.
Prog.Segmented Frame	Interlaced scanning	Different pixels are drawn in the first field and second field.	Operation is performed for each frame (2 fields).
Interlace (Sync)	Interlaced scanning	The same image is repeatedly drawn in the first field and second field.	Operation is performed for each field.



# 3.2.5 Concerning the TV modes

This parameter indicates the output of the TV standard signals (NTSC, NTSC-M, NTSC-443, PAL, PAL-M, PAL-60, PAL-N, PAL-Nc, SECAM, HDTV1080 or HDTV 720).

Even when this parameter is changed, the timing data and other data will not be edited. For this reason, when it is changed, it will no longer be possible for the images to be drawn correctly on the monitor.

\* When editing the sample timing data using a TV Mode setting which is not 'Other,' select 'Other' as the TV mode setting. In this case, however, the tri-level sync signal will not be output.

# 3.2.6 Concerning Serration and EQP

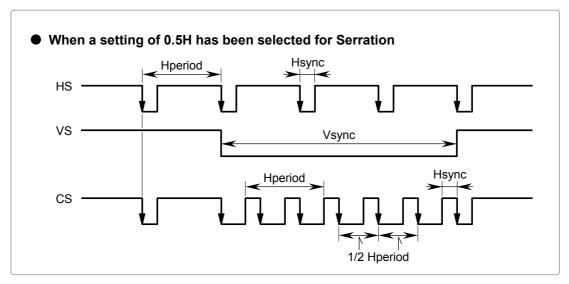
Serration and EQP can be selected on the V-Timing [MENU] screen, and various parameters can be set. The correspondences between the settings and operations are described using the table below.



#### Serration and EQP setting procedure

Setting item	Key	LCD display	Description
Serration	0	OFF	Serrated pulses are not inserted.
	1	0.5H	Serrated pulses are inserted in increments of 0.5H.
2 1H Serrated pulses are inserted in		1H	Serrated pulses are inserted in increments of 1H.
	3	EXOR	HS and VS EXORs are inserted as serrated pulses.
EQP 0 OFF Equalizing pulses are not inserted integrated periods.		Equalizing pulses are not inserted into the EQPfp and EQPbp periods.	
	1	ON	Equalizing pulses are inserted into the EQPfp and EQPbp periods.

Shown below as an example is the phase relationship when a setting of 0.5H has been selected for Serration.





- The serration and EQP item settings are not reflected in the composite, Y/C and SCART signals.
- In the case of HDTV timing data, they are set to OFF when EXOR is selected as the serration setting item.

# 3.2.7 Concerning EQP-Fp and EQP-Bp

Equalizing pulses (EQP-Fp and EQP-Bp) can be selected on the V-Timing [MENU] screen, and various parameters can be set. The correspondences between the settings and operations are described using the table below.



EQP-Fp/EQP-Bp setting procedure

#### <For progressive scanning>

Setting item	Details of setting
EQPfp	This sets the equalizing pulse inside the front porch. Setting range: 0.000 to 999.999 [ms], 0 to 99 [H]
EQPbp	This sets the equalizing pulse inside the back porch. Setting range: 0.000 to 999.999 [ms], 0 to 99 [H]

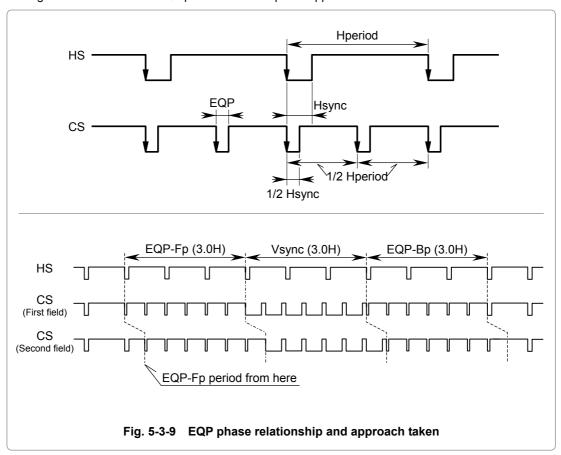
#### <For interlaced scanning>

Setting item	Details of setting	
EQP-Fp1	This sets the equalizing pulse inside the front porch.	
(EQP-Fp2)	QP-Fp2) Setting range: 0.000 to 999.999 [ms], 0.0 to 99.0 [H] (in 0.5H increments)	
EQP-Bp1	This sets the equalizing pulse inside the back porch.	
(EQP-Bp2)	Setting range: 0.000 to 999.999 [ms], 0.0 to 99.0 [H] (in 0.5H increments)	

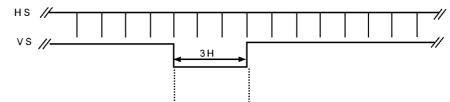


• Set EQP-Fp within the range of [(EQP-Fp +1H) ≤ Hfrontp] for tri-level sync signal outputs in the interlaced scanning mode.

The figure below shows the EQP phase relationship and approach taken.

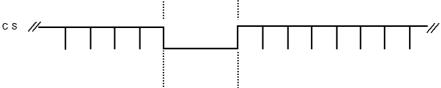


Example: Four examples of the EQP-Fp, EQP-Bp, EQP and Serration settings are shown below.



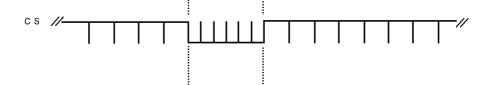
# <Setting example 1>

Setting item	Setting
EQP-Fp	0H
EQP-Bp	0H
EQP	OFF
Serration	OFF



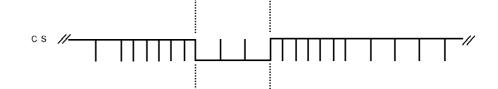
#### <Setting example 2>

Setting item	Setting
EQP-Fp	0H
EQP-Bp	0H
EQP	OFF
Serration	0.5H



#### <Setting example 3>

Setting item	Setting
EQP-Fp	3H
EQP-Bp	3H
EQP	ON
Serration	1H



#### <Setting example 4>

Setting item	Setting
EQP-Fp	3H
EQP-Bp	0H
EQP	OFF
Serration	OFF





# **INTERFACE SETTINGS**

# 4.1 Output settings

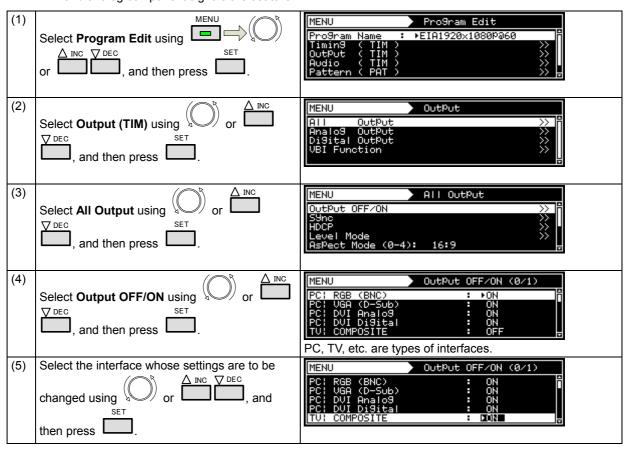
The following items are set as settings common to multiple video and audio output interfaces.

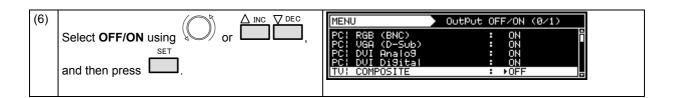
- · Output interface on/off setting
- · Sync signal on/off and polarity setting
- · Level mode setting
- · Aspect ratio setting
- · Pattern drawing bit length (gray scale) setting
- · RGB/YPbPr selection and color difference coefficient setting
- · Analog level setting
- · Digital level setting
- · Audio sweep setting
- Audio level setting (temporary settings)

### 4.1.1 Setting the output interfaces to ON or OFF

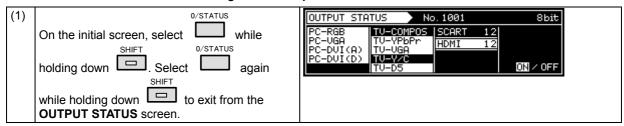
"Output" (ON) or "not output" (OFF) can be selected for each output interface whether video or audio interface. It is set to ON for the internal sample timing data unless the ratings or specifications of the generator prevent this.

Example: In the case of EIA 1920 × 1080i@59.94, the COMPOSITE and Y/C signals are set to OFF, but the HDMI and analog component signals are set to ON.



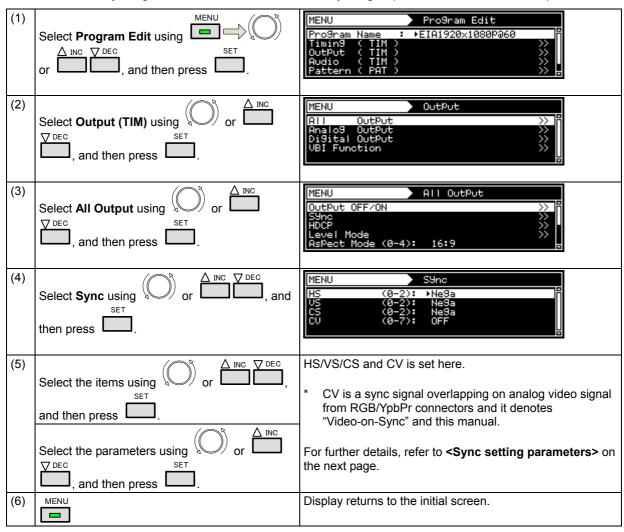


#### <How to check the interfaces whose signals are output>



# 4.1.2 Setting the sync signals to ON or OFF and setting the sync signal polarities

In this section, the sync signals are set to ON or OFF and the sync signal polarities are set for each output connector.



# <Sync setting parameters>

HS	Us	Used to set the HS connector output.					
	0	Off	No output				
	1	Nega	The signal is output with a negative polarity.				
	2	Posi	The signal is output with a positive polarity.				
vs	Used to set the VS connector output.						
	0	Off	No output				
	1	Nega	The signal is output with a negative polarity.				
	2	Posi	The signal is output with a positive polarity.				
cs	Us	ed to s	et the CS connector output.				
	0	Off	No output				
	1	Nega	he signal is output with a negative polarity.				
	2	Posi	The signal is output with a positive polarity.				
CV Used to set whether to superimpose Video-on-Sync onto the analog component signals.							
	0	Off	Off Video-on-Sync is not superimposed.				
	1 R Video-on-Sync is superimposed onto the R analog component signal.						
	2	2 <b>G</b> Video-on-Sync is superimposed onto the G analog component signal.					
	3	3 RG Video-on-Sync is superimposed onto the RG analog component signal.					
	4	В	Video-on-Sync is superimposed onto the B analog component signal.				
	5	RB	Video-on-Sync is superimposed onto the RB analog component signal.				
	6	GB	Video-on-Sync is superimposed onto the GB analog component signal.				
	7	RGB	Video-on-Sync is superimposed onto the RGB analog component signal.				



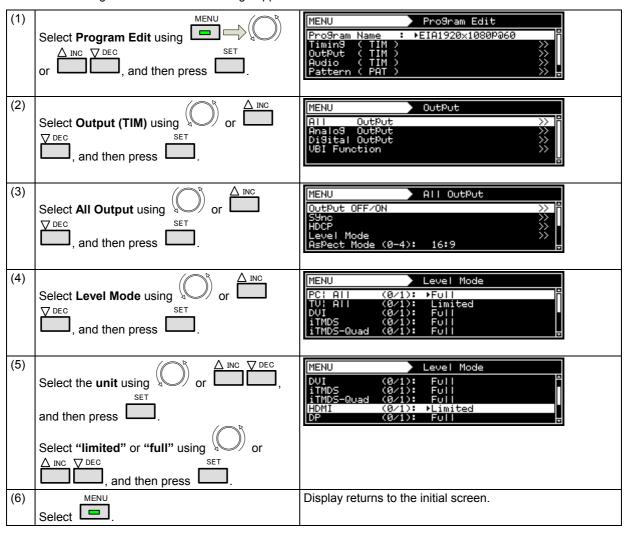
 If the CS signal is a tri-level sync (HDTV timing) signals, its polarity cannot be changed.

# 4.1.3 Setting the level mode

Images can be output in the "limited" range of the HDMI standard. The output image range can be set for each unit.

The same level setting can also be established using an interface which is not HDMI (such as DVI, LVDS, parallel or analog).

\* With the analog interface, the gray scale of the video parts will change, but neither the pedestal level nor peak level will change from when the "full" range applies.



#### Video range when "full" is selected

	8 BIT	10BIT	12BIT	16BIT	
R/G/B/Y/Cb/Cr	0-255	0-1023	0-4095	0-65535	

#### Video range when "Limited" is selected

	8BIT	10BIT	12BIT	16BIT
R/G/B/Y	16-235	64-940	256-3760	4096-60160
Cb/Cr	16-240	64-960	256-3840	4096-61440

# 4.1.4 Setting the aspect ratio

In this section, the aspect ratio of the video signals is set.

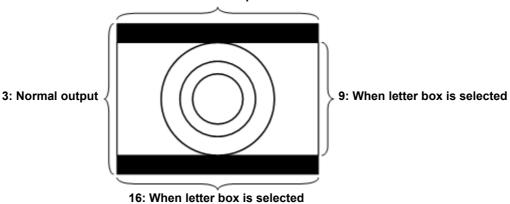
(1)	Select Program Edit using  MENU  SEL  SET	Pi Ti O	ENU ro9ram Name : imin9 ( TIM ) utPut ( TIM )	Program Edit  ►EIA1920×1080Pa60  >> >>
	or, and then press		Jdio (TIM) attern(PAT)	» <u>.</u>
(2)	Select <b>Output (TIM)</b> using or	A A	ENU    OutPut nalo9 OutPut  Sita  OutPut  SI Function	OutPut  >>>   >>   >>   >>   >>   >>   >>   >
(3)	Select All Output using or	O. S. HI	ENU JtPut OFF/ON Hnc OCP evel Mode sPect Mode (0-4	All OutPut  >>> >>> >>> >>> >>> >>> >>> >>> >>>
(4)	Select <b>Aspect Mode</b> using or	SX HI L¢	ENU Jnc OCP evel Mode BPect Mode (0-4 ser AsPect	All OutPut
(5)	<inputting parameters="" the=""></inputting>	Se	t the aspect ratio	0.
	<u>∆ inc</u>	0	4:3	The aspect ratio is set to 4:3.
	Select the parameters using or or	1	4:3 Letter Box	The aspect ratio is set to 4:3 letter box.
	▼ DEC SET	2	16:9	The aspect ratio is set to 16:9.
	Alternatively:	3	Resolution	The aspect ratio is set to the same ratio as the screen resolution.
	Select the parameters using the number keys  O/STATUS  9/F & SET  SET	4	User	The aspect ratio of the user's choice is set.
	( to ), and then press .	*	The 4:3 letter b timing signals.	ox setting takes effect only with SDTV
(6)	If <b>User</b> was set in step (5), users can set the aspect ratio of their choice.  Select <b>UserAspect</b> using or OF	S\ HI Le	ENU Unc OCP evel Mode sPect Mode (0-4 ser AsPect	All OutPut  >>>
	, and then press .			

(7)	<inputting parameters="" the=""></inputting>	Se	et the aspect ratio.
	NC NC	Н	
	Select the parameters using or under or		Setting range: 0 to 255
	<u> </u>	٧	The aspect ratio is set in the vertical direction.
	and then press .		Setting range: 0 to 255
	Select the numerical value using or		
	$\triangle$ INC $\bigcirc$ DEC , and then press .		
	Alternatively:		
	Select the parameters using the number keys		
	0/STATUS 9/F ऄ SET		
	( to ), and then press .		

\* Although images are normally output with the 4:3 aspect ratio, the images which are output when 4:3 letter box has been selected will be in the 16:9 aspect ratio. For this reason, the top and bottom of the images are filled in with black and output.

When 4:3 letter box has been selected as the aspect ratio, the images output will appear as shown below.

#### 4: Normal output



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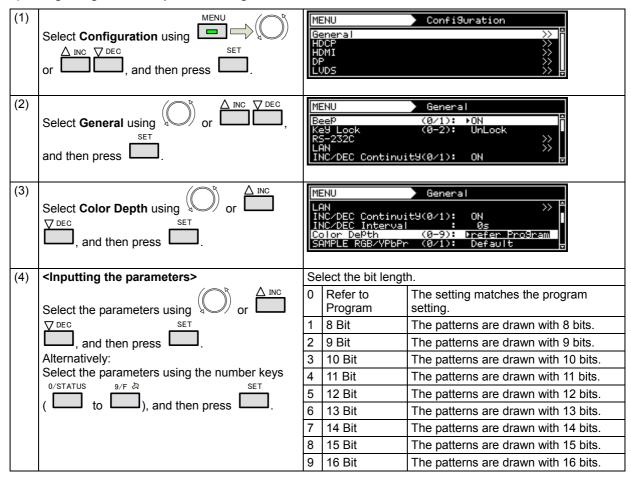
### 4.1.5 Setting the bit length (gray scale) for pattern drawing

The bit length (gray scale) applying when drawing test patterns can be set.

It can either be set either separately for each program or it can be fixed irrespective of the programs.

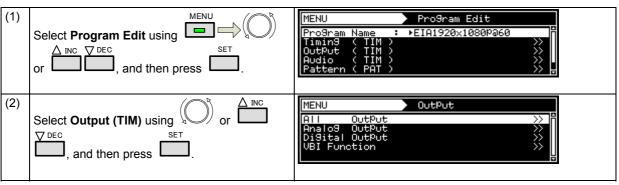
- a) The same specific bit length is designated.
- b) The bit length is set for each program.

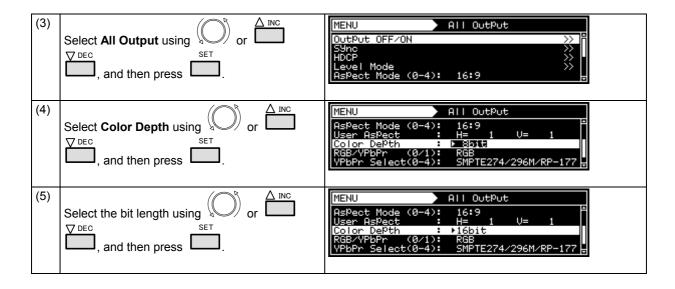
#### a) Designating the same specific bit length



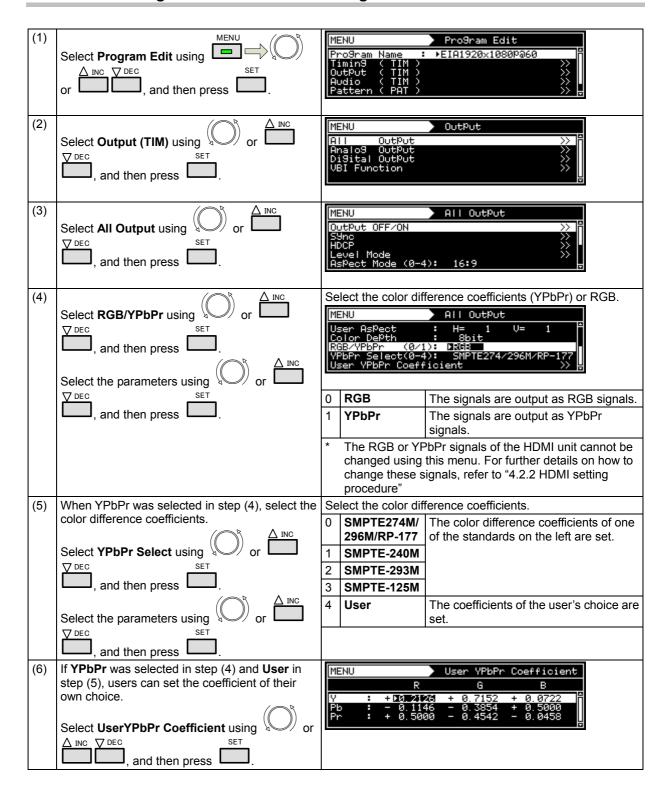
#### b) Setting the bit length for each program

This setting takes effect when "Refer to Program" has been selected for the setting in (3) of "Designating the same specific bit length" in a) above.





### 4.1.6 Selecting RGB or YPbPr and setting the color difference coefficients



(7)	Select the parameters using or Select the parameters using or , and then press .  Select the numerical value using or AINC OF SET OF SE	<ul> <li>Caution for setting the coefficients</li> <li>The Y line must total 1.0000.</li> <li>The sum of the coefficient for Pb and Pr respectively must be 0.</li> </ul>
(8)	Select —.	Display returns to the initial screen.

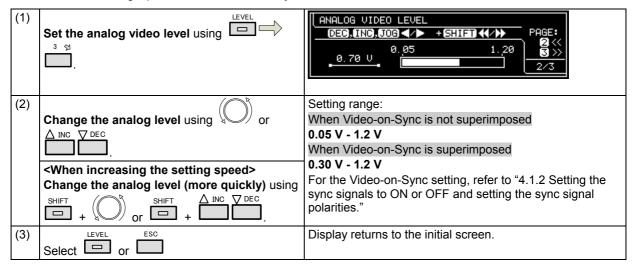
# 4.1.7 Setting the analog level

In this section, the video level of the analog component signals is set.

The video signal gray scale remains unchanged, and only the level is changed.

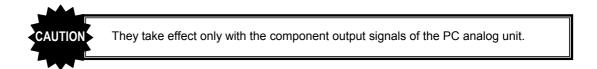
There are two ways to set the analog input level: One uses the LEVEL key screen, and the other is from Program Edit.

<How to set the analog input level on the LEVEL key screen>



<How to set the analog input level from Program Edit>

Refer to section "4.7.2 Setting the analog output connectors" (Video parameters).



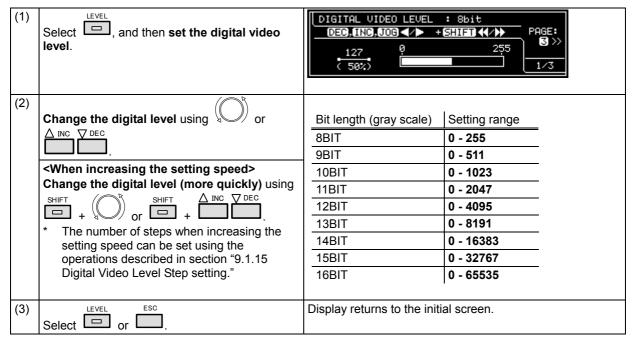
### 4.1.8 Setting the digital level

In this section, the digital level of the video signals is set.

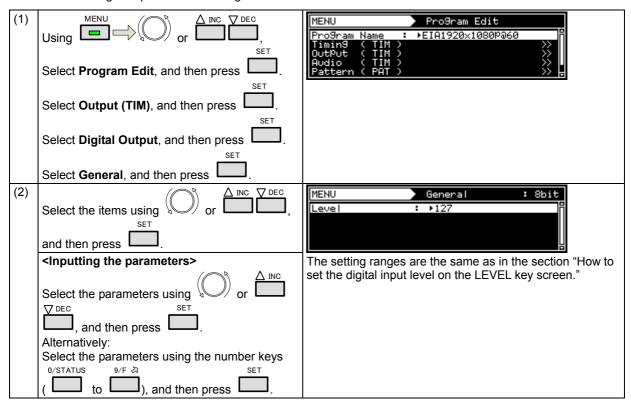
For further details on setting the gray scale, refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

There are two ways to set the digital input level: One uses the LEVEL key screen, and the other is from Program Edit.

<How to set the digital input level on the LEVEL key screen>

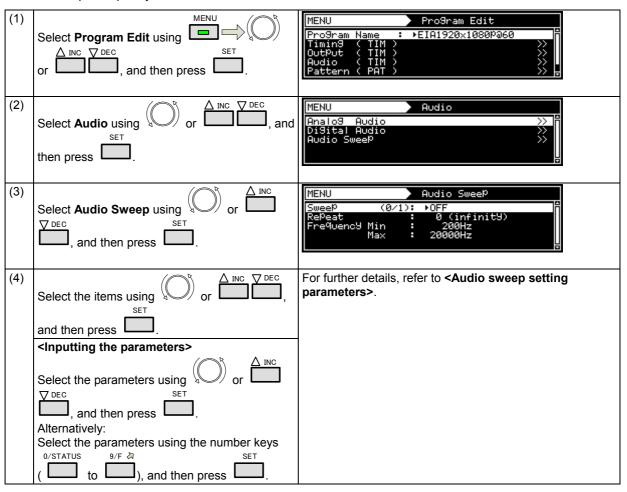


<How to set the digital input level from Program Edit>



# 4.1.9 Audio sweep settings

The audio output frequency can be raised or lowered at the set interval.



#### < Audio sweep setting parameters>

(1)	Sweep (0/1)	Used to enable or disable the sweep function.		
		0	OFF	Disable
		1	ON	Enable
(2)	Repeat (0-15)	Used to set the number of repeats.		
		0	Infinity	Repeated indefinitely.
		1-15		Repeated for the set number of times only.
(3)	Frequency Min	Used to set the minimum frequency.		
		Setting range: 200 Hz to 20000 Hz		
(4)	Frequency Max	Used to set the maximum frequency.		
		Setting range: 200 Hz to 20000 Hz		

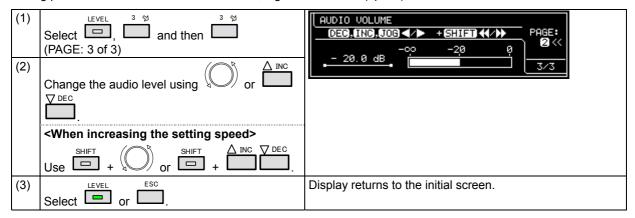


When using the function with HDMI, select Internal PCM as the Digital Audio
 Source setting.

# 4.1.10 Setting the audio level (temporary settings)

In this section, the audio output level is set.

The level which was set in "4.15.2 Analog audio signals" for analog audio or which was set using <Internal PCM setting parameters> in "4.2.5 Embedded audio, high bit rate audio (option)" for HDMI is 0 dB.





- The values set here are not saved as program data.
- When they are used with HDMI, select Internal PCM as the Digital Audio > Source setting.

# 4.1.11 Digital level setting of 3D pattern

Digital level of 3D pattern is set by each left and right picture.

Refer to "4.1.5 Big length for pattern drawing" about level setting.

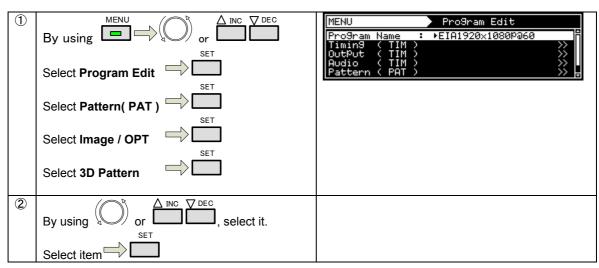
There are 2 setting method; LEVEL key screen and Program Edit.

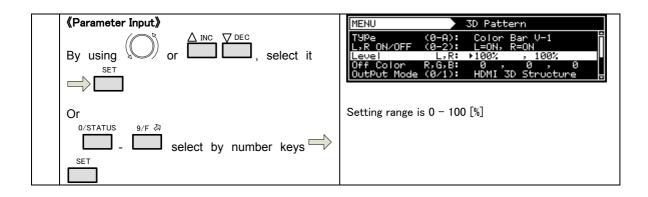
### 《 LEVEL key screen 》

When you output 3D program, by pressing LEVEL key, it automatically shows "3D LEFT VIDEO LEVEL"

1	"3D LEFT VIDEO LEVEL" Press "3" to selt RIGHT level.	3D LEFT VIDEO 0:0,110,500 255 (100%)	
2	By using or ☐ DEC,	BIT length	Setting range
	change video level	8BIT	0 - 255
		9BIT	0 - 511
	《For speed up the change》	10BIT	0 - 1023
	( C )	11BIT	0 - 2047
	By using + Or SHIFT A INC V DEC	12BIT	0 - 4095
	By using + or + -	13BIT	0 - 8191
		14BIT	0 - 16383
	step changes (speed up)	15BIT	0 - 32767
	* The changing step can be set in "9.1.15	16BIT	0 - 65535
	Digital Video Level Step".		
3	2 4 3 公	Switch LEFT / RI	GHT.
4	LEVEL	Via DIGITAL LE	VEL SETTING screen, it goes
	Select 🗀	back to iniaila disp	olay.
5	Select Esc	To initial display	

### 《 Setting by Program Edit 》

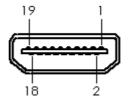




# 4.2 HDMI

# 4.2.1 Connectors and pin assignments

### ■ HDMI

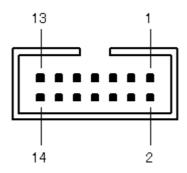


Pin no.	Signal
1	TMDS DATA2+
2	TMDS DATA2 SHIELD
3	TMDS DATA2-
4	TMDS DATA1+
5	TMDS DATA1 SHIELD
6	TMDS DATA1-
7	TMDS DATA0+
8	TMDS DATA0 SHIELD
9	TMDS DATA0-
10	TMDS CLK+
11	TMDS CLK SHIELD
12	TMDS CLK-
13	CEC
14	RESERVE / HEAC+
15	DDC CLK
16	DDC DATA
17	GROUND (for +5 V)
18	+5 V (DDC power supply *1)
19	HOT PLUG DETECT / HEAC-
Shell	FG

<sup>\*1:</sup> Restrictions apply to the supply current of the DDC power supply. Refer to "12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply."

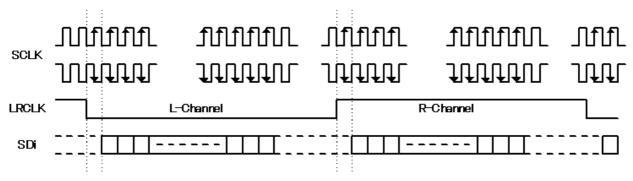
# ■ I2S (Option)

Connector: 7614-5002PL (made by 3M)



Pin no.	Signal	Description
1	MCLK IN	Input a clock signal with a frequency of 24.576 MHz or 22.5792 MHz.
2	GND	
3	SCLK IN	Input the I2S SCLK signal.
4	GND	
5	LRCLK IN	Input the I2S LRCLK signal.
6	GND	
7	SD0 IN	Input the I2S SD0 signal.
8	GND	
9	SD1 IN	Input the I2S SD1 signal.
10	GND	
11	SD2 IN	Input the I2S SD2 signal.
12	GND	
13	SD3 IN	Input the I2S SD3 signal.
14	GND	

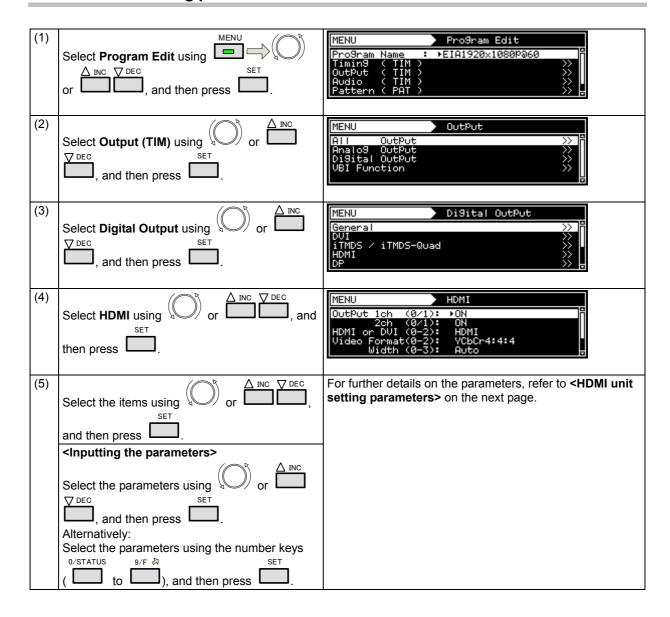
Input the signals at the following timing.



\* For the SCLK and LRCLK signals, input signals which are synchronized with MCLK.

The leading edge of SCLK can be set using "4.2.5 Embedded audio, high bit rate audio (option)."

# 4.2.2 HDMI setting procedure



# <HDMI unit setting parameters>

(4)	Output data (0/4)	Cot o	or off for ac-1	a shannal hara			
(1)	Output 1ch (0/1) Output 2ch (0/1)	Set on or off for each channel here. The same settings as the ones described in "4.1.1 Setting the output					
	Output zen (o/1)	interfa	ces to ON or C	DFF" can also be established.			
		0	Off	No signal output			
		1	On	Signal output			
(2)	HDMI or DVI (0-2)	An HDMI connection can be made to DVI by cable conversion. Set the operations at this time here.					
		0	HDMI	The full functions of HDMI can be used.			
		1 DVI		This setting differs from HDMI in the following ways.			
				Info Frame and Packet are not sent.			
				Audio is not supported.			
				Up to 8 bits are supported. Deep Color is not supported.			
		2	Auto	EDID of the connected monitor is checked, and the DVI and HDMI modes are set.			
(3)	Video Format (0-2)	The co	olor space of the	ne images output from HDMI is set here.			
		0	RGB	The images are output using RGB signals.			
		1	YCbCr4:2:2	The images are output using YCbCr4:2:2 signals.			
		2	YCbCr4:4:4	The images are output using YCbCr4:4:4 signals.			
(4)	Width (0-4)	The bit length of the images output from HDMI is set here. A setting independent of the bit length for pattern drawing can be selected or the same bit length can be selected automatically.					
		bit fille	length which hed with zeros.	hich the bit length for pattern drawing exceeds the has been set here is discarded. A deficient portion is			
		0	Auto	g the bit length (gray scale) for pattern drawing."  8, 10 or 12 bits are selected here automatically			
				depending on the bit length for pattern drawing.			
		1	8 bit	8-bit output			
		2	10 bit	10-bit output			
		3	12 bit	12-bit output			
		4	16 bit	16-bit output (only for VG-873/874)			
(5)	Audio Output (0/1)	The e	mbedded audi	o output is set here.			
			h bit rate audi	ed audio settings, refer to "4.2.5 Embedded audio, o (option)."			
		0	Off	No embedded audio output			
		1	On	Embedded audio output			
(6)	Audio N (0/1)	* Th	is setting is op	ter is set here. tional. It can be set only when the CTS license has When it has not been registered, it is fixed at Auto.			
		0	Auto	The appropriate value is set.			
		1	Manual 128×fs/A	The N value, calculated using the following formula, is set:			
				N = 128 × sampling frequency / <b>A</b> Where the setting range of <b>A</b> : 300 to 1500			
(7)	InfoFrame	Where the setting range of <b>A</b> : 300 to 1500  When sending InfoFrame automatically in line with the color space and other settings, refer to " <b>4.2.3 InfoFrame/Packet</b> "  When sending InfoFrame with the data of the user's choice, refer to " <b>4.2.3 InfoFrame/Packet</b> "					

### 4.2.3 InfoFrame/Packet

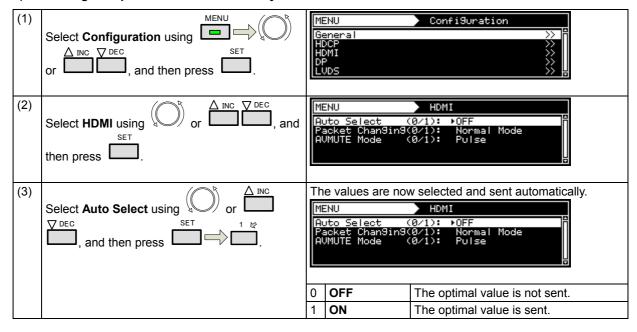
InfoFrame can send the values which are optimal for the video and audio output conditions.

In addition, it is possible to send InfoFrame using values differing from the output conditions to reproduce illegal operation conditions.

Use one of the following operations to send InfoFrame:

- a) Send the optimal values automatically.
- b) Set separate InfoFrame values, and send them.

#### a) Sending the optimal values automatically



# <List of automatically selected items>

- If program data has been saved when Auto Select is set to ON, the values which were set by automatic selection will be saved.
- A dash ("-") denotes that the value of the original setting is used.

Item	Setting/reference section	n						
AVI InfoFrame								
	AFD pattern (see "6.7 A now displayed	Setting other than the one given on the left						
Active Format Information	Valid				-			
Active Format Aspect	The setting accords with	the AF	D > <b>Type</b> s	etting.	<b>-</b>			
Top Bar	Value calculated from Al	FD, Tim	ing setting		-			
Bottom Bar								
Left Bar								
Right Bar		7						
RGB or YCbCr	-	he setting accords with the HDMI > <b>Video Fo</b>						
Picture Aspect	The setting accords with setting. (EIA/CEA-861 s			nfoFrar	ne > Video	Code		
Repetition	The setting accords with	the H-	Γiming > <b>R</b> e	epetiti	<b>on</b> setting.			
Audio InfoFrame								
	The setting accords with				ce setting.	-		
	Ext.ANALOG to L-PCM Int.L-PCM	Ext.AN	Ext.ANALOG to DSD		Int.DSD (Option)	Setting other than the one		
	Ext.I2S L-PCM (Option)					given on the left		
Sampling Frequency	-	44.1 kHz		The DSD File informatio n is used.	-			
Channel Count	The setting accords with by Digital Audio > <b>Output</b>			annels	set to ON	-		
	0		1	2 to 8	3			
	Refer StreamHe	ader	2ch	2 to 8	Bch			
ACP Packet								
	The setting accords with	the AC	P Packet >	ACP_				
	DVD-Audio				Setting oth one given o			
DVD-Audio_Type	1				0			
Copy_Permission	_				0 (Copy Fr	eely)		
Copy_Number	_				0 (1 copies	)		
Quality	-				0			
Transaction	-				0 (Not Pres	sent)		
ISRC Packet	•							
	A The setting accords w	ith the A	CP Packet	t > AC	-,			
	DVD-Audio				Setting oth one given o			
OFF/ON ISRC1	_				OFF			
ISRC2	The setting accords with ISRC_Cont setting.  O OFF	the ISF	RC Packet	>	OFF			

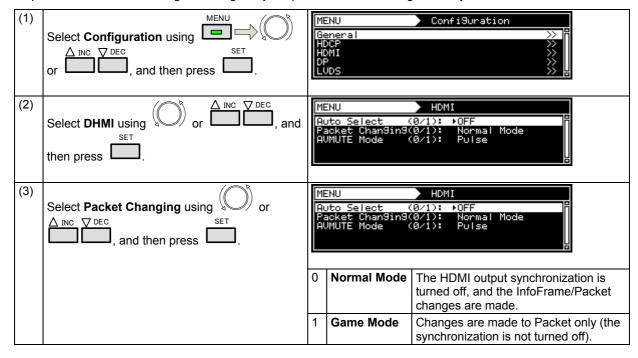
# b) Setting separate InfoFrame and Packet values and sending them

This setting can be performed when "off" has been selected for Auto Select in a) Sending the optimal values automatically.

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select <b>Output (TIM)</b> using or or or SET, and then press	MENU OutPut  All OutPut  Analog OutPut  Digital OutPut  VBI Function  OutPut  >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>
(3)	Select <b>Digital Output</b> using or or or or and then press	MENU Digital OutPut  General DVI STANDS-Quad HDMI DP STANDS OUTPUT  DESCRIPTION DIGITAL OUTPUT  DIGITAL OUTPUT  DIGITAL OUTPUT  STANDS OUTPUT  DIGITAL OUTPUT  STANDS OUTPUT  DIGITAL OUTPUT  STANDS OUTPUT  STANDS OUTPUT  DIGITAL OUTPUT  STANDS OUT
(4)	Select <b>HDMI</b> using or or or or and then press.	MENU HDMI  OutPut 1ch (0/1): ▶ON  2ch (0/1): ON  HDMI or DVI (0-2): HDMI  Video Format(0-2): YCbCr4:4:4  Width (0-3): Auto
(5)	Select the Info Frame and Packet to be set.  Select Info Frame/Packet using  or  or  or  A INC DEC, and then press	MENU InfoFrame/Packet  Transmission Mode
(6)	Select the parameters using or Select the parameters using or Select the parameters using or Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters using the number keys 0/STATUS 9/F ⋈ and then press Select the parameters 0/STATUS 9/F ⋈ and the paramet	Example: When AVI-Info Frame is selected    MENU

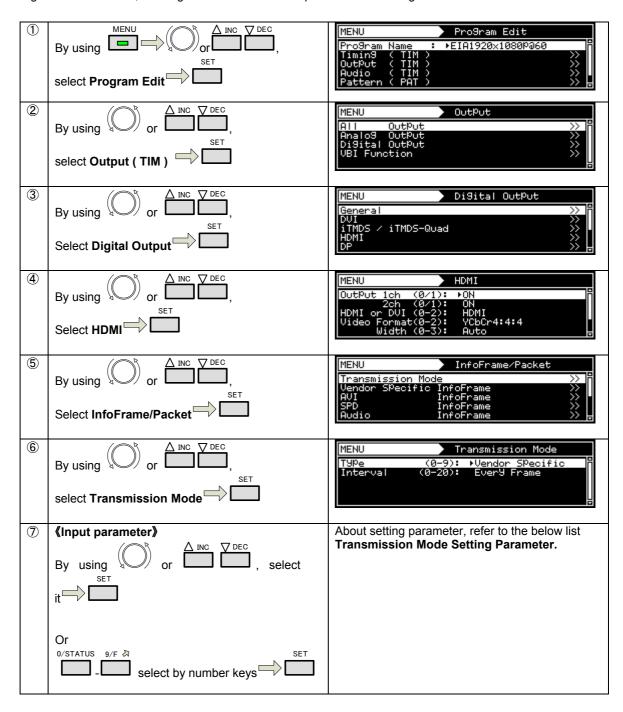
### <Setting the HDMI output when making changes to InfoFrame/Packet>

When making changes to InfoFrame/Packet, users can select either to turn off the synchronization of the HDMI output and establish the settings or change only the packets without turning off the synchronization.



### 《 Set interval of sending InfoFrame/Packet 》

Among InfoFrame/Packet, sending interval of one kind of packet can be changed.



## Transmission Mode setting parameter

(1)	Type(0-9)	Selec	t the packet to set this function.	
		0	Vendor Specific	
		1	AVI InfoFrame  Note: it is not possible to change it in HDMI(300MHz) uni	
			Note: it is not possible to change it in HDMI(300MHz) unit of	
			VG-873/874	
		2	SPD InfoFrame	
		3	Audio InfoFrame	
		4	MPEG InfoFrame	

		5	NTSC VBI InfoFran	NTSC VBI InfoFrame				
		6	ACP Packet					
		7	ISRC 1 Packet					
		8	ISRC 2 Packet					
		9	Gamut Metadata					
(2)	Interval(0-20)	Set th	ne interval of sending packets.					
		0	Every Frame	Send every frame				
		1	50ms	Send frame by the setting interval.				
		2	100ms					
		:	:					
		20	1000ms					



- This setting is active when the selected packet (in "Type")'s output setting is "ON".
- The setting value of "Intervale" is the interval of executing packet transmission that is sent from VG's CPU to HDMI transmitter. Therefore, since the actual packet transmission is executed by HDMI transmitter, it will appear 1 or 2 frame difference from the theoretical value.

### <InfoFrame and Packet setting parameters>

Listed below are the 9 InfoFrame and Packet setting parameters.

- Vendor Specific
- AVI InfoFrame
- SPD InfoFrame
- Audio InfoFrame
- MPEG InfoFrame
- NTSC VBI InfoFrame
- ACP Packet
- ISRC Packet
- Gamut Metadata Packet



Due to the limitations by the installed chips, the **maximum** number of packets which can be sent simultaneously is limited.

#### [HDMI Unit (VM-1817), HDMI1.4 Unit]

VG can send maximum **4 packets** at a time except AVI InfoFrame and Audio InfoFrame. The packet ON/OFF check is carried out in the following sequence.

- 1. ACP Packet
- 2. ISRC1 Packet
- 3. ISRC2 Packet
- 4. Gamut Meta Data Packet
- 5. SPD InfoFrame
- 6. MPEG InfoFrame
- 7. Vendor Specific InfoFrame
- 8. NTSC VBI InfoFrame

In the example given above, Vendor Specific InfoFrame and NTSC VBI InfoFrame are disabled.

ACP Packet : ON SPD InfoFrame : ON ISRC1 Packet : OFF MPEG InfoFrame : ON ISRC2 Packet : OFF Vendro Specific InfoFrame : ON Gamut Metadata Packet : ON NTSC VBI InfoFrame : ON

### [VM-1823]

VG can send maximum 7 **packets** at a time except AVI InfoFrame. However, the below packets can not be sent together.

- (1) ACP Packet and SPD InfoFrame
- (2) ISRC2 Packet and NTSC VBI InfoFrame

In case the output of combination is "ON", priority is shows below.

- (1) ACP Packet has priority to be sent.
- (2) ISRC2 Packet has priority to be sent.

# ■ Vendor Specific InfoFrame

The vendor specific information is stored in Vendor Specific InfoFrame, and sent.

(1)	OFF/ON	This setting determines whether Vendor Specific InfoFrame is to be sent.				
		0	OFF	The Vendor Specific InfoFrame is not sent.		
		1	ON	The Vendor Specific InfoFrame is sent.		
	pelow are the Vendor Specific se settings are not related to			ettinge		
			•			
(2)	Туре		is is the Vendor Specific I			
(0)	14	1	* * * * * * * * * * * * * * * * * * * *	only. It cannot be changed.		
(3)	Version	ın	•	InfoFrame version setting.		
(4)	IEEE D. ID O.I	7		d only. It cannot be changed.		
(4)	IEEE RegID Sel	it.		ng for the IEEE Registration ID and the items after		
		0	Other	Any IEEE Registration ID can be selected. The Payload is set after the IEEE Registration ID.		
		1	HDMI	The IEEE Registration ID is set to 000C03. After the IEEE Registration ID, the setting is established using the format that supports HDMI 1.4.		
1. IEEE	RegID Sel: Other					
1-(1)	IEEE Regist. ID	This is the IEEE Registration ID setting.				
		000000 - FFFFFF				
1-(2)	Payload Length	This is the Payload length setting.				
		0 -	24			
1-(3)	Payload 1-24	Th	is is the Payload data set	tting.		
		00	- FF			
2. IEEE	RegID Sel: HDMI					
2-(1)	IEEE Regist. ID	Th	is indicates the IEEE Reg	gistration ID. (It cannot be changed.)		
		00	0C03			
2-(2)	Video Format	This is the HDMI Video Format setting.				
		0 None		No additional HDMI video format is presented this packet.		
		1	Ext. Resolution	Extended resolution format present.		
		2	3D (Option)	3D format indication present.  * '3D' is an option. The Vendor Specific InfoFrame information is not sent unless the license has been registered. For further details, contact your dealer or an ASTRODESIGN sales representative.		
	eo Format: Ext. Resolution					
2-1-(1)	HDMI VIC	Th	is is the HDMI VIC setting	g.		
		0	4K×2K 29.97/30 Hz			
		1	4K×2K 25 Hz			
1		2	4K×2K 23.98/24 Hz			
1		3	4K×2K 24 Hz (SMPTE)			

2-2. Vid	eo Format: 3D (Option)							
2-2-(1)	3D Structure	thi	s is the 3D Structure sett	ing.				
		0	Frame Packing					
		1	Field Alternative *1					
		2	2 Line Alternative					
		3	3 Side-by-Side (Full)					
		4	L + depth					
		5	L + d + G + G-d (L + de	epth + graphics + graphics-depth)				
		6	Side-by-Side (Half)					
		7	Top & Bottom					
2-2-(2)	3D Ext Data	this is the 3D Ext Data setting.						
		0	Horizontal O/L,O/R	Horizontal sub-sampling				
		*2		Odd/Left picture, Odd/Right picture				
		1	Horizontal O/L,E/R	Horizontal sub-sampling				
		*2		Odd/Left picture, Even/Right picture				
		2	Horizontal E/L,O/R	Horizontal sub-sampling				
		*2		Even/Left picture, Odd/Right picture				
		3 * <b>2</b>	Horizontal E/L,E/R	Horizontal sub-sampling				
		4	Quincunx O/L,O/R	Even/Left picture, Even/Right picture  Quincunx matrix				
		4	Quilicultx O/L,O/K	Odd/Left picture, Odd/Right picture				
		5	Quincunx O/L,E/R	Quincunx matrix				
			Quillounx 0/2,2/10	Odd/Left picture, Even/Right picture				
		6	Quincunx E/L,O/R	Quincunx matrix				
			,	Even/Left picture, Odd/Right picture				
		7	Quincunx E/L,E/R	Quincunx matrix				
				Even/Left picture, Even/Right picture				
2-2-(3)	3DMeta Present		s is the 3D Meta present t) setting.	(whether the following 3D metadata is present or				
		0	0 (Not Present)	3D metadata not present				
		1	1	3D metadata present				
2-2-(4)	Metadata Type		1 -	lata type. (it cannot be changed.)				
( · /		0		Al (				
2-2-(5)	Metadata Length		s is the 3D Metadata Ler	nath settina.				
- (-)			- 21 *3	<u> </u>				
2-2-(6)	Metadata 1-21 *3		s is the 3D Metadata dat	a setting.				
( - )			- FF	Ŭ				



The output signals differ from the ones in the standards. The DE signal is high for three Vblank periods of the current Field Alternative.



\*2:

The 3D Ext Data values of 0, 1, 2 and 3 have all been made consistent with "Horizontal sub-sampling" in HDMI Ver.1.4a.
With this generator, the old expressions are used to differentiate the drawing

method used with optional pattern 101 and to provide compatibility.



\*3

If the setting value of 3D Struction is "Side-by-site (Half)", the maximum data of 3D Metadata is 20 bytes.

If you set "21" in Metadata Length, the last data (21th byte) is not sent. Please refer to the below list.

Packet Byte#	7	6	5	4	3	2	1	0		
PB 0		Checksum								
PB 1										
PB 2		24bit IEEE Registration Identifier (000C03h) (least significant byte first)								
PB 3										
PB 4	HD	MI_Video_For (010b(3D))	mat	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)		
PB 5		3D_St	ructure		3D_Metadata_ Present	Rsvd (0)	Rsvd (0)	Rsvd (0)		
PB 6	3D_	Metadata_1	Гуре		3D_Metadata_Length					
PB 7				3D_Me	tadata_1					
PB 8				3D_Me	tadata_2					
PB 9		3D_Metadata_3								
:		:								
PB 26		3D_Metadata_20								
PB 27				3D_Met	adata_21					

例1:3D\_Structureの設定が『Side-by-Side(Half)』以外の場合のパケットデータマッピング

Example 1: Packet data mapping except Side-by-Side(Half).

Packet Byte#	7	6	5	4	3	2	1	0		
PB 0	Checksum									
PB 1										
PB 2		24bit IEEE Registration Identifier(000C03h) (least significant byte first)								
PB 3			( ),	odoc olgriirio	ounc by co in c	,,				
PB 4	Н	MI_Video_For (010b(3D))	mat	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)		
PB 5		3D_St	ructure by–Side(Half))		3D_Metadata_ Present	Rsvd (0)	Rsvd (0)	Rsvd (0)		
PB 6			t_Data		Reserved (0)					
PB 7	3D.	_Metadata_1	Гуре		3D_Metadata_Length					
PB 8				3D_Me	etadata_1					
PB 9				3D_Me	etadata_2					
PB 10				3D_Me	etadata_3					
:	:									
PB 26		3D_Metadata_19								
PB 27				3D_Me	tadata_20					

例2:3D Structureの設定が『Side-bv-Side(Half)』の場合のパケットデータマッピング

Example 2: Packet data mapping of Side-by-Side(Half).

### AVI InfoFrame

"AVI InfoFrame" stands for Auxiliary Video Information InfoFrame. The information (including the color space and aspect ratio) of the transmission images is stored in it, and sent.

Note: HDMI 300MHz unit (for VG-873/874) always send AVI InfoFrame while HDMI is output.

(1)	OFF/ON	This setting determines whether the AVI InfoFrame is to be sent.				
		0	OFF	The AVI InfoFrame is not sent.		
				Note: it is not available in VG-873/874.		
		1	ON	The AVI InfoFrame is sent.		
Listed	below are the AVI InfoFrame	setti	ngs.			
* Th	ese settings are not related to	the	video and audio output s	ettings.		
(2)	Туре	Th	is is the AVI InfoFrame ty	pe setting.		
		2	* "Type" is displayed o	nly. It cannot be changed.		
(3)	Version	Th	is is the AVI InfoFrame ve	ersion setting.		
		1	Version 1			
		2	Version 2			
(4)	Scan Info		is sets the Scan Informat			
		(It	sets whether processing	is required for the transmitted images.)		
		0	No Data	No Data		
		1	Overscanned	Composed for an overscanned display.		
		2	Underscanned	Composed for an underscanned display.		
(5)	Bar Info	Th	is sets the Bar Info (valida	invalid for the Bar Information described later).		
		0	Data Not Valid	Bar Data not valid		
		1	Vertical Valid	Vert.Bar info valid		
		2	Horizontal Valid	Horiz.Bar info Valid		
		3	Vert. & Horiz. Valid	Vert. And Horiz. Bar Info valid		
(6)	ActiveF Info	This is the Active Format Information Present setting (valid/invalid for the Active Format Aspect Ratio described later).				
		0	No Data	No Data		
		1	Valid	Active Format Information Valid		
(7)	RGB or YCbCr	Th	is is the RGB or YCbCr (o	color space of transmitted images) setting.		
		0	RGB			
		1	YCbCr 4:2:2			
		2	YCbCr 4:4:4			
(8)	AvtiveF Aspect		is is the Active Format As ccluding Bar of letter box,	pect Ratio (aspect ratio of the video parts etc.)) setting.		
		0	Same Picture			
		1	4:3 (center)			
			16:9 (center)			
		3	14:9 (center)			
		4	Box 16:9 (top)			
		5	Box 14:9 (top)			
		6	Box > 16:9 (center)			
		7	4:3 (14:9 center)			
		8	16:9 (14:9 center)			
		9	16:9 (4:3 center)			
L	1		\ · · /			

(9)						
			letter box	k, etc.) setting.		
		0	No Data	3	No Data	
		1	4:3		4:3	
		2	16:9		16:9	
(10)	Scaling		ages hav	e been scaled) s		
		0	No Kno		No Known non-uniform Scaling	
		1	Horizor		Picture has been scaled horizontally	
		2	Vertical		Picture has been scaled vertically	
		3	Horiz. 8		Picture has been scaled horizontally and vertically	
(11)	Colorimetry				standard whose coefficients were used for ence signals) setting.	
		0	No Data	<b>a</b>	No Data	
		1	SMPTE ITU601	170M	SMPTE170M/ITU601	
		2	ITU709		ITU709	
		3	Extend	ed Valid	Extended Colorimetry Information Valid	
(12)	Video Code	Th	is is the '	Video Format Ide	entification Code setting.	
		0 -	59	For further deta CEA-861-D.	ails on the timings indicated by Code, refer to	
(13)	Repetition	1 -	10	This is the Pixe	el Repetition Factor setting.	
(14)	Top Bar	0 -	This is the Line bar size setting		Number of End of Top Bar setting (letter box top 1).	
(15)	Bottom Bar	0 -	65535	This is the Line bottom bar size	Number of Start of Bottom Bar setting (letter box e setting).	
(16)	Left Bar	0 -	65535	This is the Pixe bar size setting	el Number of End of Left Bar setting (pillar box left	
(17)	Right Bar	0 -	65535	This is the Pixe	el Number of Start of Right Bar setting (pillar box etting).	
(18)	RGB Quan.Range				on Range setting (quantization range when RGB	
		0	Default	•	<i>,</i>	
		1	Limited			
		2	Full Ra			
(19)	YCC Quan.Range	Th	is is the `		on Range setting (quantization range when YCC v).	
		0	Limited	•	77	
		1	<u> </u>			
(20)	Extended Colo.	Th		Extended Colorii	metry setting.	
		(This is referenced when Extended Valid has been set as the Colorimetry setting.)				
		<u> </u>	XvYCC	601		
		1	XvYCC	709		
		2	sYCC60	01		
		3	Adobe	/CC601		
		4	AdobeF	RGB		

(21)	IT content	This is the IT Content (whether the transmitted images are IT content)			J.				
		0		-					
		1							
(22) IT content Type			is is the IT Content	Type setting.					
		0	Graphics						
			Photo						
		2	Cinema	Cinema					
		3	Game						
(23)	Checksum	Th	This is the checksum setting.						
		0	Auto	The value is calculated automatically.					
		1	Manual	The desired value is set.					
			00h - 0FFh						
(24)	Length		0 - 15 [byte]	This is the AVI InfoFrame length setting.					
				(Packet Header and Checksum are not included.)					
				The default is 13 bytes.					
				Note: it is not available in VG-873/874.					
(25)	Data Byte 14, 15		00h - 0FFh	This is the data byte 14 and 15 value setting.					
				Note: it is not available in VG-873/874.					

<sup>\*1:</sup> The Checksum, Length and Data byte 14,15 settings are optional. They can be set only when the requisite license has been registered. When it has not been registered, the Checksum setting is fixed at Auto and the Length setting at 13 bytes.

# ■ SPD InfoFrame

"SPD InfoFrame" stands for Source Product Description InfoFrame. The information of the transmission device is stored in it, and sent.

(1)	OFF/ON	Th	This setting determines whether the SPD InfoFrame is to be sent.				
		0	OFF	The SPD InfoFrame is not sent.			
		1	ON	The SPD InfoFrame is sent.			
	below are the SPD InfoFrame		•				
	ese settings are not related to	_	•				
(2)	Туре		nis is the SPD InfoFrame	·· · · · · · · · · · · · · · · · · · ·			
		3		only. It cannot be changed.			
(3)	Version		is is the SPD InfoFrame				
		1	Version1	"Version" is displayed only. It cannot be changed.			
(4)	Vendor Name	Th	is is the Vendor Name (n	ame of the transmission device vendor) setting.			
		Ma	aximum 8 characters	For further details on the input method, refer to steps (2) and following in section "2.3 Setting the names"			
(5)	Product Description		nis the Product Description (c.)) setting.	n (name of the transmission device (model name,			
		Ma	aximum 16 characters	For further details on the input method, refer to steps (2) and following in section "2.3 Setting the names"			
(6)	Source Device		is is the Source Device Inting.	nformation (the type of transmission device)			
		0	Unknown				
		1	Digital STB				
		2	DVD Player				
		3	D-VHS				
		4	HDD Video recorder				
		5	DVC				
		6	DSC				
		7	Video CD				
		8	Game				
		9 A	<b>3</b>				
		В	Super Audio CD				
		С	HD DVD				
		D	PMP				
		ט	1				

# Audio InfoFrame

The transmission audio information is stored in the Audio InfoFrame, and sent.

(1)	OFF/ON	This setting determines whether the Audio InfoFrame is to be sent.					
(1)	OFF/ON	0	OFF The Audio InfoFrame is not sent.				
		1	OFF	The Audio InfoFrame is not sent.			
Liotodk	l below are the Audio InfoFram			The Audio infortaine is sent.			
	ese settings are not related to			ettings			
(2)	Type		is is the AVI Audio InfoFra	-			
(2)	1,700	-		only. It cannot be changed.			
(3)	Version	4	is is the Audio InfoFrame				
(0)	Toroion			d only. It cannot be changed.			
(4)	Coding Type		is is the Audio Coding Ty				
,		0	Refer StremHeader	Refer to Stream Header			
		1	IEC60958 PCM				
		2	AC-3				
		3	MPEG1 (Layers 1&2)				
		4	MP3 (MPEG1 Layer 3)				
		5	MPEG2 (multi ch.)				
		6	AAC				
		7	DTS				
		8	ATRAC				
		9	One Bit Audio				
		Α	Dolby Digital +				
		В	DTS-HD				
		С	MLP				
		D	DST				
		Е	WMA Pro				
		F	Refer Extension				
(5)	Coding Ext Type	Th	is is the Audio Coding Ex	t Type setting.			
		0	HE-AAC				
		1	HE-AACv2				
		2	MPEG Surround				
(6)	Channel Count	Th	is is the Audio Channel C	<u> </u>			
		0	Refer StreamHeader	Refer to Stream Header			
		1	2 ch				
		$\downarrow$	↓				
		7	8 ch				
(7)	Sampling Freq	Th	is is the Sampling Freque				
		0	Refer StreamHeader	Refer to Stream Header			
		1					
		2					
		3	48 kHz				
		4	88.2 kHz				
		5	96 kHz				
		6	176.4 kHz				
		7	192 kHz				

(8)	Sample Size	Th	is is t	he Samp	le Size s	sett	ina.					
(0)		0		Refer StreamHeader Refer to Stream Header								
		1	16 b				1 (0.0)	10 01100		<del>,</del>		
		2	20 b									
		3	24 b									
(9)	Speaker Placement				nel/Snea	kei	r Alloc	ation set	tina			
(3)	Opeaker i lacement	<u> </u>	This is the Channel/Speaker Allocation setting.  8ch 7ch 6ch 5ch 4ch 3ch 2ch 1ch									
		0		OCIT	7 011	0,	CII	3011	-	3011	FR	FL
		1			-	-		-	_	LFE	FR	FL
		2			-	+-		-	FC	LFE	FR	FL
		3			-	Ē		-  -	FC	LFE	FR	FL
		4				Ι_		RC	-	_	FR	FL
		5			-	-		RC		LFE	FR	FL
		6			_	+-		RC	FC	_	FR	FL
		7			-	+-		RC	FC	LFE	FR	FL
		8			-	4	.R	RL	10	LIL	FR	FL
		9			-	_	R.	RL	-	LFE	FR	FL
		10			-	_	R.	RL	FC	-	FR	FL
		11			-	_	R.	RL	FC	LFE	FR	FL
		12			RC	_	R.	RL	10	LIL	FR	FL
		13			RC	_	R.	RL	-	LFE	FR	FL
		14			RC	_	R.	RL	FC	_	FR	FL
		15			RC	_	R.	RL	FC	LFE	FR	FL
		16		RRC	RLC	_	R.	RL	-	_	FR	FL
		17		RRC	RLC	_	R.	RL	_	LFE	FR	FL
		18		RRC	RLC	_	R.	RL	FC	-	FR	FL
		19		RRC	RLC	_	R.	RL	FC	LFE	FR	FL
		20		FRC	FLC	-	.1 \	-	-	-	FR	FL
		21		FRC	FLC	-		-	-	LFE	FR	FL
		22		FRC	FLC	+		-	FC		FR	FL
		23		FRC	FLC	-		_	FC	LFE	FR	FL
		24		FRC	FLC	-		RC	-		FR	FL
		25		FRC	FLC	-		RC	_	LFE	FR	FL
		26		FRC	FLC	-		RC	FC		FR	FL
		27		FRC	FLC	-		RC	FC	LFE	FR	FL
		28		FRC	FLC	R	R	RL	-		FR	FL
		29		FRC	FLC	_	R.	RL	_	LFE	FR	FL
		30		FRC	FLC	_	R.	RL	FC		FR	FL
		31		FRC	FLC	_	R.	RL	FC	LFE	FR	FL
(10)	Level Shift Value			he Level		_			1	<u> </u>	1	<u> 1 · –                                    </u>
()			15		decibel				ere.			
(11)	Down-mix			he Down		_	•					
( )		0		nitted / I					o inform	ation abo	ut anv a	ssertion
					.50		of this	3			at arry a	,551 (1011
		1	Prol	nibited			Prohi	bited				

(12)	LEF PB Level	Th	is is the LEF Playback Level setting.
		0 Undnown	
		1	0 dB Playback
		2	+10 dB Playback

### **■** MPEG InfoFrame

If the original source of the data prior to its conversion to HDMI is MPEG data, its information is stored in MPEG InfoFrame, and sent.

(1)	OFF/ON	Th	s setting determines whether the MPEG InfoFrame is to be sent.				
		0	OFF	The MPEG InfoFrame is not sent.			
		1	ON	The MPEG InfoFrame is sent.			
	below are the MPEG InfoFran		•				
* Th	ese settings are not related to	the	video and audio output s	ettings.			
(2)	Туре	Th	is is the MPEG InfoFrame	e type setting.			
		5	<ul><li>* "Type" is displayed o</li></ul>	nly. It cannot be changed.			
(3)	Version	Th	is is the MPEG InfoFrame	e version setting.			
		1	* "Version" is displayed	d only. It cannot be changed.			
(4)	Bit Rate	0 -	4294 M 967 k 295 Hz This is the MPEG bit rate setting.				
(5)	Field Repeat	Th	is is the Field Repeat set	ting.			
		0	New Field(picture)				
		1	Repeated Field				
(6)	Frame	Th	is is the MPEG Frame setting.				
		0	Unknown(No Data)				
		1	I Picture				
		2	B Picture	B Picture			
		3	P Picture				

### ■ NTSC VBI InfoFrame

The vertical blanking interval (VBI) information is stored in NTSC VBI InfoFrame, and sent.

(1)	OFF/ON	Thi	This setting determines whether the NTSC VBI InfoFrame is to be sent.				
		0 <b>OFF</b>		٦	The NTSC VBI InfoFrame is not sent.		
		1	ON	٦	The NTSC VBI InfoFrame is sent.		
Listed	below are the NTSC VBI InfoF	ran	ne setting	S.			
* Th	ese settings are not related to	the	video an	d audio output set	ttings.		
(2)	Туре	This is the NTSC VBI InfoFrame type setting.					
		6	* "Тур	e" is displayed on	ly. It cannot be changed.		
(3)	Version	Thi	s is the I	NTSC VBI InfoFra	me version setting.		
		1 * "Version" is displayed only. It cannot be changed.					
(4)	PES Length	0 - 27		This sets the PES length.			
(5)	PES 1-5/6-10/11-15/16-20/ 21-25/26-27	+ +		This sets the PE	S data.		

# ACP Packet

"ACP Packet" stands for Audio Content Protection Packet. The copyright protection information added to DVD-Audio and Super Audio CD contents is stored in it, and sent.

(1)	OFF/ON	Th	This setting determines whether the ACP Packet is to be sent.					
		0	OFF	The ACP Packet is not sent.				
		1	ON	The ACP Packet is ser	nt.			
	below are the ACP Packet	_						
* Th	ese settings are not related							
(2)	ACP_Type	Th	is is the ACP Type settir	ng.				
		0	Generic Audio					
		1	IEC60958 Audio					
		2	DVD-Audio					
		3	Super Audio CD					
(3)	DVD-Audio Type	-		pe_Dependent_Generation				
		0		1 when "DVD-Audio" has	s been selected as the			
		1	ACP_Type setting.					
(4)	CopyPermission	Au D\	dio_Copy_permission (t ′D-Audio content) is set	he information concernin here.	g the permission to copy			
		0	Copy Freely					
		1	(reserved)					
		2	2 Specify CopyNumber					
		3	3 No More Copies					
(5)	Copy_Number	Au	Audio_copy_number (the number of times DVD-Audio content may be copied) is set here.					
		0	1 copies					
		1	2 copies					
		2	4 copies					
		3	6 copies					
		4	8 copies					
		5	10 copies					
		6	3 copies					
		7	<b>Copy OneGeneration</b>					
(6)	Quality	Au he		in which DVD-Audio cont	ent is to be copied) is set			
			No. of channels	Sampling frequency	Bit width			
		0	2 channels or less	Lower than 48 kHz	16 bits or less			
		1	2 channels or less	No restrictions	No restrictions			
		2	No restrictions	No restrictions	No restrictions			
		3	No restrictions	Lower than 48 kHz	16 bits or less			
(7)	Transaction		dio_Transaction (whether the DVD-Audio data) is:		ccess control is contained			
		0	Not Present	not present				
		1	(reserved)	Reserved for copyright	: management system use			

(8)	Count_A		he number of times the Super Audio CD contents can be copied by ed secure recorder) is set here.				
		0	Prohibited				
		1 - 254	Allowed from 1 to 254 times				
		255	No restrictions				
(9)	Count_S	Count_S (to a secure re	he number of times the Super Audio CD contents can be copied by ecorder) is set here.				
		0	Prohibited				
		1 - 254	Allowed from 1 to 254 times				
		255	No restrictions				
(10)	Count_U	Count_U (t by an unlis	he number of times the Super Audio CD contents can be copied ted recorder) is set here.				
		0	Prohibited				
		1 - 254	Allowed from 1 to 254 times				
		255	No restrictions				
(11)	CCI_Flags_Q_A		_Q_A (the quality in which Super Audio content is to be copied by ed secure recorder) is set here.				
		0 CD Qua	ality				
		1 Unlimit	ted DSD Quality				
(12)	CCI_Flags_Q_S		_Q_S (the quality in which Super Audio content is to be copied by ecorder) is set here.				
		0 CD Qua	ality				
		1 Unlimit	ted DSD Quality				
(13)	CCI_Flags_Q_U		_Q_U (the quality in which Super Audio content is to be copied by recorder) is set here.				
		0 CD Qua	ality				
		1 Unlimit	ted DSD Quality				
(14)	CCI_Flags_Move_A		_Move_A (whether copying of Super Audio content by individual an approved secure recorder is allowed) is set here.				
		0 Not All	0 Not Allowed				
		1 Allowe	d				
(15)	CCI_Flags_Move_S	CCI_Flags_Move_S (whether copying of Super Audio content by individual track onto a secure recorder is allowed) is set here.					
		0 Not All	owed				
		1 Allowe	d				
(16)	CCI_Flags_Move_U		_Move_U (whether copying of Super Audio content by individual an unlisted recorder is allowed) is set here.				
		0 Not All	owed				
		1 Allowe					

# ■ ISRC Packet

"ISRC Packet" stands for International Standard Recording Code Packet. The sound source identification codes and other information are stored in it, and sent.

(1)	OFF/ON ISRC1	WI	nether to send the ISRC1	Packet is set here.				
		0	OFF	The ISRC1 Packet is not sent.				
		1	ON	The ISRC1 Packet is sent.				
(2)	OFF/ON ISRC2	WI	Whether to send the ISRC2 Packet is set here.					
		0	0 OFF The ISRC2 Packet is not sent.					
		1	1 ON The ISRC2 Packet is sent.					
	below are the ISRC Packet s		_					
	ese settings are not related to							
(3)	ISRC_Cont	<u> </u>	is is the ISRC Continued	setting.				
		0	ISRC2 is not sent.					
		1	ISRC2 is sent.					
(4)	ISRC_Valid		is is the ISRC Valid settir	•				
				ner data has been set to the ISRC_Status in the he UPC_EAN_ISRC_XX field is valid.)				
		0	Invalid					
		1	Valid					
(5)	ISRC_Status	Th	is is the ISRC_Status set	tting.				
		(IS	RC_Status indicates the	position on the current track.)				
		0	Starting					
		1	1 Intermediate					
		2	Ending					
(6)	Validity Info		is is the Validity informati					
		<u> </u>		ISRC and UPC/EAN data is valid or invalid.)				
		_	Vo Validity					
		1	ISRC					
		2	UPC/EAN					
		3	UPC/EAN and ISRC					
(7)	Catalogue Code			(UPC/EAN #1 - 13) setting.				
		Number consisting of 13 digits						
(8)	Country Code	This is the Country Code (ISRC #1 - 2) setting.						
		Character string consisting of 2 letters						
(9)	First Owner Code	This is the First Owner Code (ISRC #3 - 5) setting.						
		Character string consisting of 3 alphanumerics						
(10)	Year of Rec. Code	This is the Year-of-recording code (ISRC #6 - 7) setting.						
			mber consisting of 2 d	<u> </u>				
(11)	Recording-item Code			/ Recording-item code (ISRC #8 -12) setting.				
		Nι	Number consisting of 5 digits					

# ■ Gamut Metadata Packet

If the transmission images have been sent by xvYCC, their color space information (range, etc.) is stored in the Gamut Metadata Packet, and sent.

(1)	OFF/ON	This setting determines whether the Gamut Metadata Packet is to be sent.						
		0			The Gamut Metadata Packet is not sent.			
		1 ON The Gamut Metadata Packet is sent.						
	isted below are the Gamut Metadata Packet settings.							
	These settings are not related to the video and audio output settings.							
(2)	Next-Field	This is the Next_Field setting.						
		(This indicates whether GBD (Gamut Boundary Description) sent in this						
		Gamut Metadata Packet is applicable to the next video field.)						
		<ul><li>Not applicable</li><li>Applicable</li></ul>						
(3)	No_Current_GBD	ļ.			D setting			
(3)	No_Guirent_GBB	This is the No_Current_GBD setting. (This indicates whether GBD sent in this Gamut Metadata Packet is valid or						
		•	alid.)	ico miotrior obi	2 contin the canat metadata i action is valid of			
		0	Invalid					
		1	Valid					
(4)	GBD_Profile	Th	is is the (	GBD_Profile set	ting.			
		0	P0					
		1	P1					
		2	<b>2</b> P2					
		<b>3</b> P3						
(5)	AffectedGamutSeqNum							
		(This indicates the number of GBD (Gamut boundary						
(0)	0	description) sent in this Gamut Metadata Packet.) <b>0 - 15</b> This is the Current Gamut Seq Num setting.						
(6)	Current_GamutSeqNum				ent_Gamut_Seq_Num setting. the number of the GBD that applies to the current			
		video field.)			the number of the GDD that applies to the current			
(7)	Packet_Seq	Th	is is the F	Packet Seq sett	ing.			
					mut Metadata Packet is in the Gamut Metadata			
			cket Seq					
			Interme	diate	Intermediate packet in sequence			
		1		First packet in sequence				
		2	Last		Last packet in sequence			
(2)	<u> </u>	_	3 Only		Only packet in sequence			
(8)	Format_Flag	This is the Format_Flag setting.						
		(This indicates the format of the GBD sent.)  0 <b>Vertices/Facets</b> Vertices/Facets description						
					·			
(9)	Colorprecision	1 Range Range description This is the GBD_Color_Precision setting.						
(9)	Colorbiecision	(This indicates the precision (bit width) of the vertex and range data in GBI						
		0 8 bit						
		1   10 bit						
		2 <b>12 bit</b>						
<u> </u>	L	L   12 VIL						

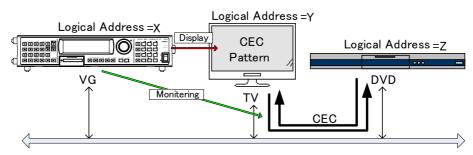
(10)	Со	lor_Space	This is the GBD_Color_Space setting.				
				■ When Vertices/facets (0) has been selected as the Format_Flag setting			
			0	ITU-R BT.709	ITU-R BT.709 (using RG	· · · · · · · · · · · · · · · · · · ·	
			1	xvYCC601	xvYCC601 (IEC61966-2	!-4-SD) (using YCbCr)	
			2	2 <b>xvYCC709</b> xvYCC709 (IEC61966-2-4		2-4-HD) (using YCbCr)	
			3	XYZ	XYZ		
			■ When Range (1) has been selected as the Format_Flag setting				
			0	0 Reserved Reserved			
			1	1 xvYCC601 RGB expression of xvYCC601 coordinates			
			2	2 xvYCC709 RGB expression of xvYCC709 coordinates			
			3	Reserved	Reserved		
(11) Number_Vertices			This is the Number_Vertices setting.  * This is displayed only when Vertices/facets (0) has been selected as the Format_Flag setting.				
			Colorprecision = 8 bit: 4 - 8 10 bit: 4 - 6 12 bit: 4 - 5				
(12)	Packed_GBD_Vertices_ Data		This is the Packed_GBD_Vertices_Data setting.  * This is displayed only when Vertices/facets (0) has been selected as the Format_Flag setting.				
		Data1	J	lorprecision =	The Y, Cb and Cr values of the colors (Data) are		
		Data2		oit: <b>0 - 255</b>	set here.		
		Data3	_	bit: 0 - 1023			
		Data4	12	bit: <b>0 - 4095</b>			
(13)	Packed_Range_Data		This is the Packed_Range_Data setting.  * This is displayed only when Range (1) has been selected as the Format_Flag setting.				
		Min_Red	Со	lorprecision =		The Range Data of the	
		Max_Red		oit: <b>-3.96875 - +3.96875</b>		colors (Red, Green and	
		Min_Green	10 bit: -3.9921875 - +3.9921875 Blue) are set here.				
	Max_Green Min_Blue Max_Blue		12 bit: <b>-3.998046875 - +3.998046875</b>				

### 4.2.4 CEC function

HDMI can send and receive the CEC commands, and display them on the screen. The CEC function has three operation modes.

#### a) Monitor mode (Monitor)

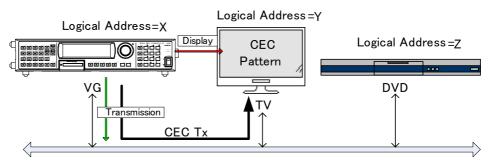
In this mode, the sending and receiving of the commands generated between the equipment connected to CEC are displayed on the screen.



**Monitor Mode** 

### b) Transmission mode (Transmission)

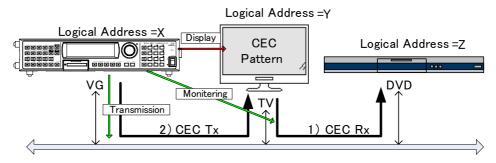
In this mode, the commands are sent from the generator to the designated logical address.



Transmission Mode

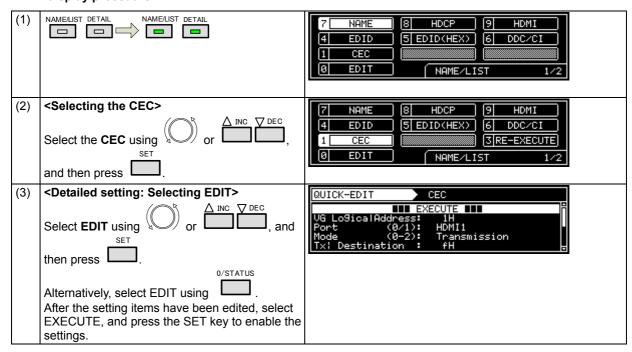
#### c) Response mode (Response)

In this mode, the commands are sent as responses when the designated commands have been transmitted.



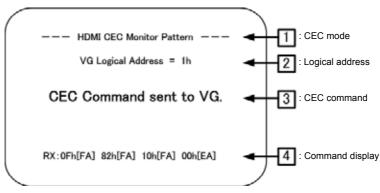
Response Mode

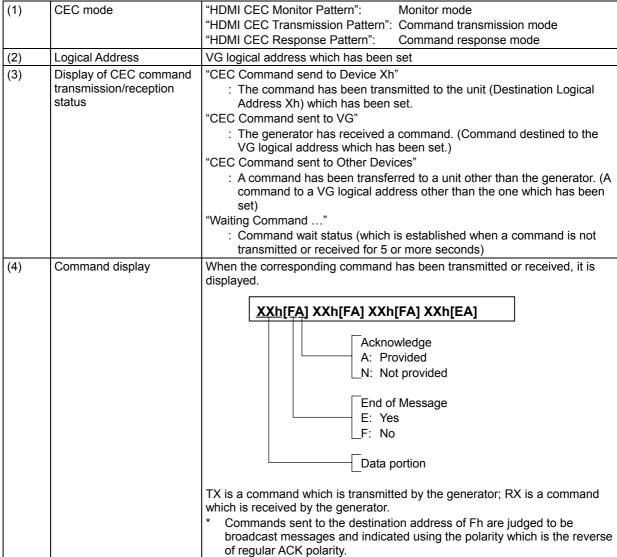
### <CEC display procedure>



### <Table of CEC setting items>

(1)	VC Laginal Address	Th	io oct	o the legical address	o of the VC generator (0 to E)	
(1)	VG Logical Address	This sets the logical address of the VG generator. (0 to F)				
(2)	Port (0-1)	This sets the port used for CEC execution.				
		0	O   HDMI1		CEC is executed using HDMI1.	
		1	HDN	112	CEC is executed using HDMI2.	
(3)	Mode (0-2)	This sets the operation mode.				
		0	Mon	itor	The CEC commands are monitored.	
		1	Tran	smission	The CEC commands set using items (4) to (7) are transmitted.	
		2	Res	oonse	When commands have been received under conditions (8) to (12), the CEC commands set using items (4) to (7) are transmitted.	
	re the CEC commands to be s					
The fol	lowing items are set when Tra	nsr	nissi	on or Response ha	s been selected as the <b>Mode setting</b> .	
(4)	Tx   Destination	-	This sets the address of the <b>destination</b> (transmission destination o CEC commands).			
(5)	Tx   Opcode	-	This sets the OPCode.			
(6)	Tx   Data Length	0 t	o 14	14 This sets the length of the Tx   data.		
(7)	<b>Tx</b>   Data [H] 1-6/7-12/13-14	-		This sets the CEC command data.		
Set her	Set here the CEC commands to be received from the VG-870B/871B.					
The fol	lowing items are set when Re	spo	nse l	nas been selected a	s the Mode setting.	
(8)	Rx   Initiator	0 t	οЕ	This sets the address of the initiator.		
(9)	Rx   Destination	0 t	o F	This sets the address of the <b>destination</b> .		
				This can also be set using a <b>logical address</b> other than the one set using item (1).		
(10)	Rx   Opcode	-		This sets the OPCode.		
(11)	Rx   Data Length	0 t	o 14	This sets the length of the <b>Rx</b>   data.		
(12)	<b>Rx</b>   Data [H] 1-6/7-12/13-14	-		This sets the CEC command data.		





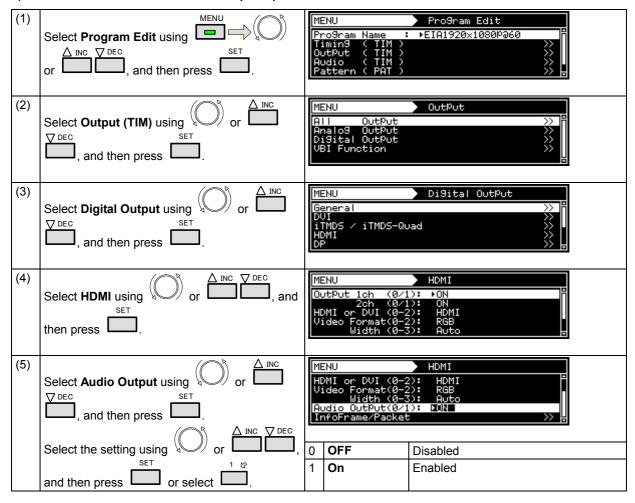
# 4.2.5 Embedded audio, high bit rate audio (option)

HDMI enables embedded audio and high bit rate audio to be output.

High bit rate audio is treated as an option. Contact your dealer or an ASTRODESIGN sales representative.

The operating procedure is as follows:

- a) Enable the embedded audio to be superimposed on HDMI.
- b) Set the sound source, frequency, level, etc.
- a) Enable the embedded audio to be superimposed on HDMI.



### b) Setting the sound source, frequency, level, etc.

For further details on the setting procedure, refer to "4.16 Digital audio."

### 4.2.6 EDID

For further details on the setting procedure, refer to "6.13.3 EDID."

### 4.2.7 HDCP

For further details on the setting procedure, refer to "8.1 HDCP settings."

# 4.2.8 DDC/CI

For further details on the setting procedure, refer to "6.13.4 DDC/CI."

# 4.2.9 LipSync

For further details on the setting procedure, refer to "7.9 LipSync."

## 4.2.10 VD-1673 (HDMI SWITCHER)

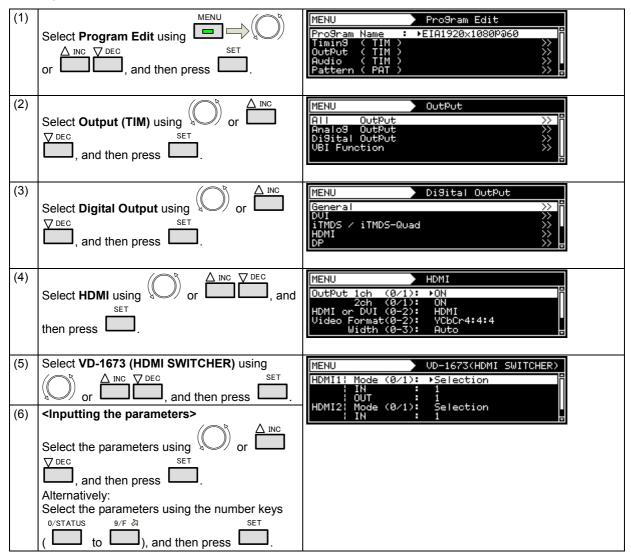
The VD-1673 is an HDMI switcher (with 2 inputs and 8 outputs) made by ASTRODESIGN.

The settings described in this section take effect only when the VD-1673 is connected to the generator and the "mode in which control is exercised by the VG series" has been selected.

\* Be absolutely sure to connect the VD-1673 before turning on the power of the generator.

For further details on the settings, refer to the operating instructions of the VD-1673.

### <Setting procedure>



### <Table of setting items>

The items are set for each of the HDMI1 and HDMI2 output channels.

(1)	Mode (0/1)	The output mode is selected here.				
		0 <b>Selection</b> Se		Selection mode		
		1 <b>Distribution</b> Distribution mode				
(2)	IN	The input port is selected here.				
		Setting range: 1 to 2				
(3)	OUT	The output port is selected here.				
		Se	Setting range: 1 to 8			

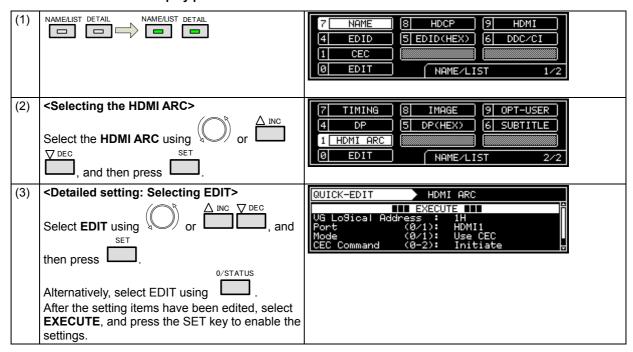
### 4.2.11 Audio Return Channel

On this screen, the Audio Return Channel function is executed, and the patterns of the related data are displayed.

The sound received is output from the COAX digital audio output connector.

\* This function is supported only by the HDMI 1.4a unit and HDMI 300MHz unit. It is not supported by the VM-1817 HDMI Unit.

### <Audio Return Channel display procedure>



#### <Table of Audio Return Channel setting items>

(1)	VG Logical Address	Th	This sets the logical address of the VG generator. (0 to F)				
(2) Port (0/1)			This sets the port used for Audio Return Channel execution.				
		0	HDMI1	Audio Return Channel is executed using HDMI1.			
		1	HDMI2	Audio Return Channel is executed using HDMI2.			
(3)	Mode (0/1)	Th	is sets the operation mod	de.			
			Use CEC	ARC start and end are controlled using the CEC commands.			
		1	Audio Monitor	The sound acquisition is started without using the CEC commands.			
(4)	CEC Command (0-2)		These set the operation to be performed when CEC (0) has been selected as the Mode setting.				
				Operation which accords with the ARC start and end requests from ARC TX is performed.			
		1	Initiate	ARC is started from ARC RX (VG). (The "Initiate ARC" command is sent.)			
		2	Terminate	ARC is ended from ARC RX (VG). (The "Terminate ARC" command is sent.)			
(5)	Follower: Mode (0/1)	This sets the send destination of the CEC commands.					
		0	Auto	The commands are sent to the adjoining device of the generator. *			
		1	Manual	A logical address is specified, and the commands are sent to this address.			
(6)	Follower: LogicalAddr	This sets the logical address where the commands are to be sent when Manual (1) has been selected as the Follower: Mode setting. (0-F)					



# <Concerning operations when Auto (0) has been selected as the Follower: Mode setting>

- In order to define the adjoining device of the generator, the connection location
  of the generator is checked by reading the physical address of the EDID
  connected to the generator. An error results if it has been determined that this
  physical address cannot be obtained (because the EDID of the connection
  destination cannot be read or because the EDID is not the HDMI EDID, for
  instance). In a case like this, EXECUTE must be selected again.
- In order to define the adjoining device of the generator, a CED command is sent from the generator. An error results if the response to this command is illegal and the generator cannot define the adjoining device. In a case like this, EXECUTE must be selected again.
- If, based on the response to the command sent, it has been determined that
  two or more devices adjoin the generator (because their physical addresses
  are identical, for instance), the adjoining device is identified with the lower or
  lowest logical address is identified to be the adjoining device.

#### <Concerning the CEC response commands>

A response is given to the following reception commands while the ARC pattern is selected.

When Auto (0) has been selected as the Follower: Mode setting

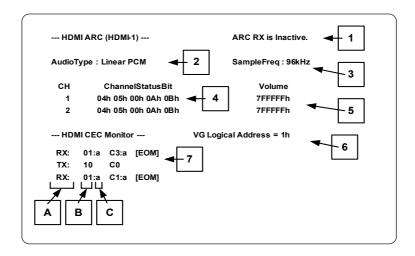
Reception com	mand	Response command		
Command	Send source	Command	Send destination	
Give Physical Address	All sources	Report Physical Address  * Only when it has been possible to obtain the physical address from the EDID  * The Device Type of the address set by VG Logical Address is used as the Device Type among the parameters. However, other Device types are used for the addresses listed below.  Ch: Reserved Dh: Reserved Eh: Video Processor Fh: No response	Broadcasts	
Request ARC Initiation	Adjoining device	Initiate ARC	Adjoining device	
Request ARC Termination	Adjoining device	Terminate ARC	Adjoining device	

When Manual (1) has been selected as the Follower: Mode setting

Reception com	mand	Response command		
Command	Send source	Command	Send destination	
Request ARC Initiation	Follower: LogicalAddr setting	Initiate ARC	Follower: LogicalAddr setting	
Request ARC Termination	Follower: LogicalAddr setting	Terminate ARC	Follower: LogicalAddr setting	



Commands common to the Manual and Auto Follower: Mode settings						
Reception command	Response command					
Give Device Vendor ID	Device Vendor ID					
Give System Audio Mode Status	System Audio Mode Status					
Request Short Audio Descriptor	Report Short Audio Descriptor					



On the Audio Return Channel screen, the Audio data is displayed in the top part of the screen and the CEC send/receive data is displayed in the bottom part of the screen.

Only the Audio data is displayed when **Audio Monitor (1) has been selected as the Mode setting.** 

#### <List of Audio data display area items>

(1)	Status display	The execution status of the generator's ARC function is displayed here.
	ARC RX is Inactive.	The ARC receive function has not been executed.
	ARC RX is Active.	The ARC receive function has been executed.
(2)	AudioType	The type of audio is displayed here.
(3)	SampleFreq	The sampling frequency is displayed here.
(4)	ChannelStatusBit	The channel statuses are displayed here.
(5)	Volume	The volume (peak) levels are displayed here. (Linear PCM only)

<sup>\*</sup> When **Use CEC (0) has been selected as the Mode setting**, the Audio data will not be displayed unless the CEC command has been communicated properly.

### <List of CEC data display area items>

(6)	VG Logical Address	The logical address of the generator is displayed here.
(7)	CEC send/receive data	The CEC command data sent from the generator or other devices is displayed here.
	(A) RX/TX	RX: These are the commands which have been received by the generator; TX: these are the commands which the generator has sent.
	(B) Data area	The block data is displayed here (00 to FF).
	(C) ACK area	The ACK data of the block is displayed here. (a: ACK present, n: ACK not present)  * Only the commands received are displayed.

The following information is displayed in [] for (7):

[E_BUS]	When an attempt was made to send a command from the generator, the bus was not released so the command would not be sent.
[E_ACK]	ACK was not present in the command sent from the generator.
[E_ARB]	When a command was sent from the generator, another command came into conflict, and the transmission was not completed.
[EOM]	EOM of the block has been set.
	* This is displayed only for commands which have been received.

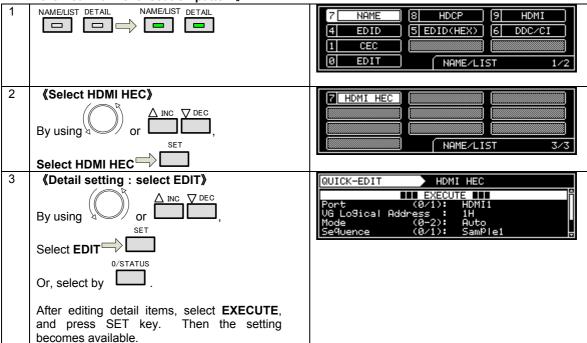
### 4.2.12 HDMI Ethernet Channel

HDMI Ethernet Channel function execution and pattern display

- \* This function is available only for the below condition.
  - VG-873 / 874 (HDMI 300MHz)
  - When IA-1542 (HEAC Adaptor) is connected to HDMI1.4a Video Signal Generator (VG-870B / 871B).
     IA-1542 can not be used with VM-1817(HDMI 1.3) or VG-873/874 signal generator.

The below operation is for VG-873/874 only. If you use IA-1542, please refer to IA-1542 Instruction Manual.

#### **《HDMI Ethernet Channel execution pattern》**



#### 《HDMI HECGeneral setting items》

(1)	Port(0/1)	Select a port to execute HEC test.		
		0	HDMI1	HDMI1 executes HEC.
		1	HDMI2	HDMI2 executes HEC.
(2)	VG Logical Address	Set CEC Logical Address of VG-873 / 874.		
(3)	Mode (0-2)	Set a mode of HEC test.		
		0	Auto Send CDC (Capability Discovery and Control)	
			message, ping commans automatically.	
		1	CDC Send CDC message.	
		2	Network	Send ping commands.

### **About Auto Mode**

This mode is supposed to use in production line testing.

By sending/receiving CDC message,HEC function search, control of connected devices and ping commands transmission automatically.

### 《Setting item》

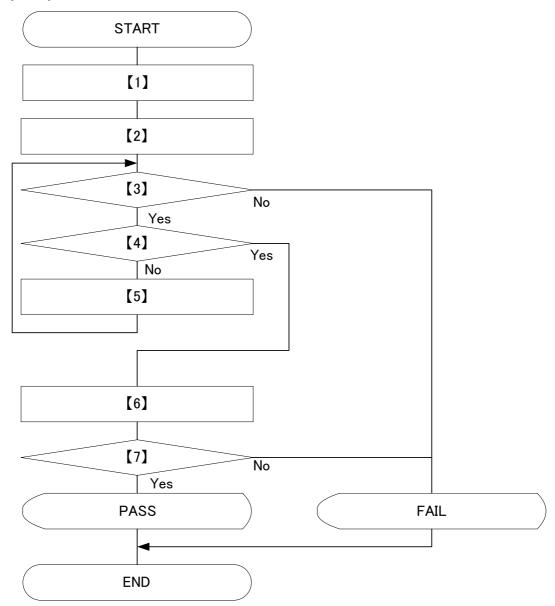
(1)	Sequence (0/1)	Select Test Sequence	
		0 Sample1	
		1 Sample2	
(2)	Target Logical Addr	Set CEC Logical Address of DUT (devide under test).	
(3)	Target IP Address	Set IP address of DUT.	

### 《Details of test sequence》

Sample1: VG-873 / 874 becomes Activator, and execute Activation.

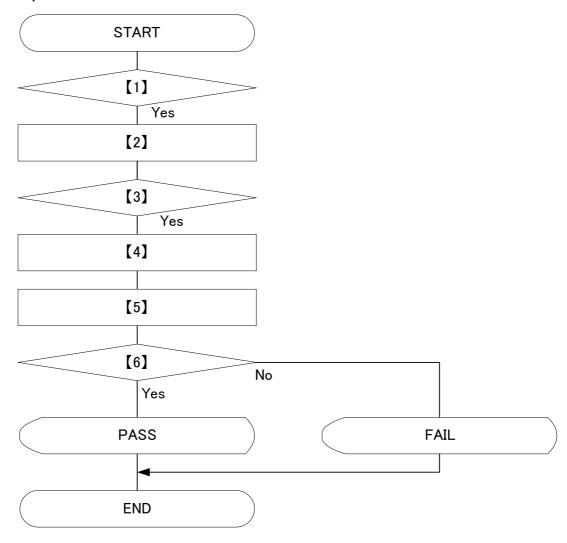
Sample2 : DUT becomes Activator and execute Activation to VG-873 / 874.

### Sample1 Operation flow



No.	Contents (Operation, condition to judge)	Details			
[1]	Send <cdc_hec_reportstate></cdc_hec_reportstate>	Physical Address of Target : 0xFFFF			
		HEC Functionality State : HEC Active			
[2]	Send <cdc_hec_inquirestate></cdc_hec_inquirestate>	Terminating device 1 : VM-1823			
		Terminating device 2 : DUT (device under test)			
[3]	Whether DUT responds				
	<cdc_hec_reportstate> or not.</cdc_hec_reportstate>				
[4]	Whether DUT is HEC Active or not.				
[5]	Send <cdc_hec_setstate></cdc_hec_setstate>	Terminating device 1 : VM-1823, Terminating device 2 : DUT			
		HEC Set State : Activate HEC			
[6]	Send Ping	Source: VM-1823 IP Address, Destination: Target IP Address			
[7]	Whethere there is reponse or not.				

### Sample2 Operation flow



No.	Contents (Operation, condition to judge)	Details
[1]	Whether DUT sends	
	<cdc_hec_inquirestate> or not.</cdc_hec_inquirestate>	
[2]	Send <cdc_hec_reportstate>.</cdc_hec_reportstate>	HEC Functionality State : HEC Inactive
[3]	Whether DUT sends	HEC Set State : Activate HEC
	<cdc_hec_setstate> or not.</cdc_hec_setstate>	
[4]	Send <cdc_hec_reportstate></cdc_hec_reportstate>	HEC Functionality State : HEC Active
[5]	Send Ping	Source: VM-1823 IP Address
		Destination : Target IP Address
[6]	Whethere there is reponse or not.	

### About CDC Mode

In this mode, parameter of CDC message is edit and sent.

PASS/ FAIL criteria after CDC message transmission is different from each message. Please refer to HDMI standard about the detail of each CDC message.

#### **《Setting item》**

etting	item/				
(1)	Target Logical Addr	Se	Set CEC Logical Address of DUT.		
		(T	his is used for result	criteria when executing CDC Msg = 0, 3, 6)	
(2)	CDC Msg (0-6)	Select CDC message to send			
		0	Inquire State	<cdc_hec_inquirestate></cdc_hec_inquirestate>	
		1	Report State	<cdc_hec_reportstate></cdc_hec_reportstate>	
		2 Set State Adj <cdc_hec_setstatea< th=""><th><cdc_hec_setstateadjacent></cdc_hec_setstateadjacent></th></cdc_hec_setstatea<>		<cdc_hec_setstateadjacent></cdc_hec_setstateadjacent>	
		3 Set State <cdc_hec_setstate></cdc_hec_setstate>		<cdc_hec_setstate></cdc_hec_setstate>	
		4	Req	<cdc_hec_requestdeactivation></cdc_hec_requestdeactivation>	
		Deactivation			
		5	Notify Alive	<cdc_hec_notifyalive></cdc_hec_notifyalive>	
		6	Discover	<cdc_hec_discover></cdc_hec_discover>	

### CDC Msg = 0 (Inquire State)

#### 《Setting item》

(1)	Dev Physical Addr1	Set Physical Address of the first Terminating Device.	
(2)	Dev Physical Addr2	Set Physical Address of the second Terminating Device.	

### 《Criteria of result judgement》

If the below condition is fulfilled, the test result becomes PASS.

• DUT (the device designated by Target Logical Addr) is included in the channel designated by the Dev Physical Addr1 and the Dev Physical Addr2.

and

There is a response of <CDC\_HEC\_ReportState> from the DUT>.

Note: if the setting value of **Target Logical Addr and** the setting value of **VG Logical Address** are same (DUT=VG-873/874), the result is FAIL.

### CDC Msg = 1 (Report State)

### 《Setting item》

(1)	Dev Physical Addr	Set	Physical address of	the destination device of the message.	
(2)	HEC Func State (0-3)	Set HEC Functionality State			
		0	Not Supported		
		1	Inactive		
		2	Active		
		3	Activation Field		
(3)	Host Func State (0-2)	Set	Host Functionality S	tate.	
		0	Not Supported		
		1	Inactive		
		2	2 Active		
(4)	ENC Func State (0-2)	Set	et ENC Functionality State.		
		0	Not Supported		
		1	Inactive		
		2	Active		
(5)	CDC Error Code (0-3)	Set	CDC Error Code.		
		0	No Error	"No Error"	
		1	Never Support "Initiator does not have the requested		
				Capability"	
		2	Can not	"Initiator is not capable to carry out the	
			respond	request in this state"	
		3	Other Error	" Other Error"	

### 《Criteria of result judgement》

After finish sending <CDC\_HEC\_ReportState>, judge it as PASS.

Note: the setting value of **Dev Physical Addr and** Physical Address of VG-873/874 are same (message destination = VG-873/874), the result is FAIL.

Or, if the device set by **Dev Physical Addr** does not exist, the result is FAIL.

### CDC Msg = 2 (Set State Adj)

#### 《Setting item when CDC Msg = 2 (Set State Adj) is selected》

(1)	Set Mode (0/1)	Sele	ect the setting metho	d of the destination device of message.
		0	Auto	Send message to the adjacent devices of
				VG-873/874.
				* If there are sevelra adjacent devices, the
				message is sent to the device that has
				smaller Logical Address.
		1	Manual	Message is sent to the device set in the
				below "Dev Physical Addr".
(2)	Dev Physical Addr	Set Physical Address of the destination device of message.		
		(This is available only when <b>Set Mode=1(Manual)</b> )		
(3)	HEC Set State (0/1)	Setting of the HEC Set State		
		0	Deactivate HEC	
		1	Activate HEC	

#### 《Criteria of result judgement》

When either of below 2 conditions are fulfilled, the result is PASS.

Condition 1 Set Mode = Auto

CDC\_HEC\_ReportState> is responded by the adjacent devices of VG-873/874.

Condition 2 Set Mode = Manual

<CDC\_HEC\_ReportState> is responded from the device designated by Dev Physical Addr.

Note: when setting by **Set Mode=Manual**, if the setting value of **Dev Physical Addr** and Physical address of VG-873/874 are same (message destination = VG-873/874), the result is FAIL.

Or, if the device set by Dev Physical Addr does not exist, the result is FAIL.

#### CDC Msg = 3 (Set State)

### **《Setting item》**

(1)	Dev Physical Addr1	Set Physical Address of the first Terminating Device.		
(2)	Dev Physical Addr2	Set Physical Address of the second Terminating Device.		
(3)	HEC Set State (0/1)	Setting of the HEC Set State		
		0 Deactivate HEC		
		1 Activate HEC		

#### 《Criteria of result judgement》

If the below condition is fulfilled, the test result becomes PASS.

DUT (the device designated by Target Logical Addr) is included in the channel designated by the
 Dev Physical Addr1 and the Dev Physical Addr2.

and

There is a response of <CDC HEC ReportState> from the DUT>.

#### CDC Msg = 4 (Req Deactivation)

#### 《Setting item》

(1)	(1) Act Physical Addr Set Physical Address of Activator.		
(2) Dev Physical Addr1 Set		Set Physical Address of the first Terminating Device.	
(3)	Dev Physical Addr2	Set Physical Address of the second Terminating Device.	

### 《Criteria of result judgement》

If the below condition is fulfilled, the test result becomes PASS.

• The device designated in the **Act Physical Addr** sends <CDC\_HEC\_SetState>[**Dev Physical Addr1**][**Dev Physical Addr2**][HEC Deactivate].

Note: if the setting value of **Act Physical Addr** and Physical Address of VG-873/874 are same, the result is FAIL.

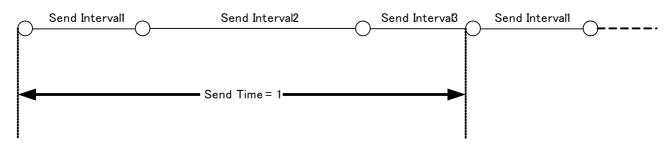
Or, if if the device set by Act Physical Addr does not exist, the result is FAIL.

#### CDC Msg = 5 (Notify Alive)

#### **《Setting item》**

(1)	Send Interval1	Set interval of sending <cdc_hec_notifyalive> (0-150sec)</cdc_hec_notifyalive>			
(2)	Send Interval2	Set interval of sending <cdc_hec_notifyalive> (0-150sec)</cdc_hec_notifyalive>			
(3)	Send Interval3	Set interval of sending <cdc_hec_notifyalive> (0-150sec)</cdc_hec_notifyalive>			
(4)	Send Time	Supposer 3 times of sending <cdc_hec_notifyalive> is</cdc_hec_notifyalive>			
		countedas one sequence, set how many sequence it is sent .			

### **《Transmission image》**



○ 〈CDC\_HEC\_NotifyAlive〉 送信

### 《Criteria of result judgement》

Under above condition, after <CDC\_HEC\_NotifyAlive> is sent, the result becomes PASS.

### CDC Msg = 6 (Discover)

### 《Criteria of result judgement》

If the below condition is fulfilled, the test result becomes PASS.

DUT (designated by Target Logical Addr) responds with <CDC\_HEC\_ReportState>.

Note: if the setting value of **Target Logical Addr** and **VG Logical Address** are same (DUT = VG-873/874), the resutl is FAIL.

### Mode = Network

In this mode, ping command (ICMP echo request) is sent to the DUT.

### **《Setting item》**

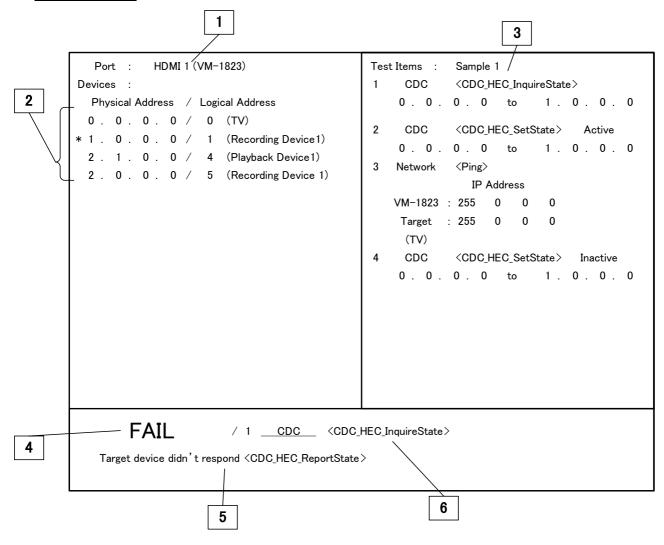
(1)	Target IP Address	Set IP address of DUT.
-----	-------------------	------------------------

### 《Criteria of result judgement》

If the below condition is fulfilled, the test result becomes PASS.

• The DUT (designated by **Target IP Address**) responds (ICMP echo reply).





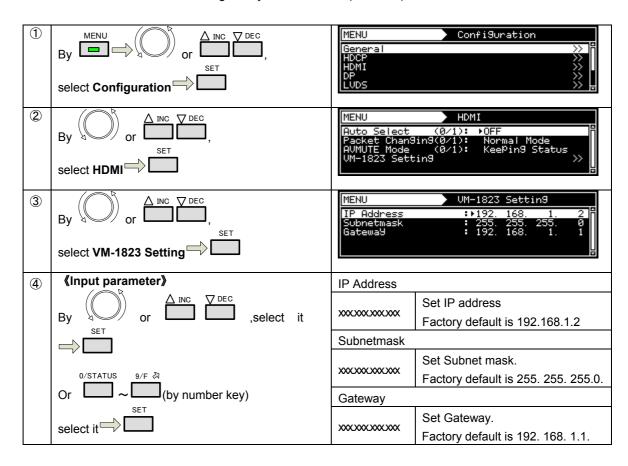
The test result is displayed as pattern as above picture.

### 《HEC test result items list》

1	Execution port	Display the port to execute the test.						
2	HDMI device information	Display the information (CEC Logical Address, Physical Address) of HDMI connected devices.  "*" indicates VG-873/874.						
3	Test contents	Display the test contents.  If theMode is Auto, information of each step of test sequence is displayed.						
4	Test result	"PASS" or "FAIL"						
Below information is displayed only when the result is FAIL.								
5	FAIL detail status	Details status of error.						
6	FAIL appeared step	When Mode is Auto, it displays in which step of sequence the "FAIL" statu appears.						

### **Network setting of HEC function**

Set IP address, Subnet mask and gateway of VG-873/874 (VM-1823).



### 4.3 **DVI**

DVI output connectors are provided on the PC analog unit and DVI unit.

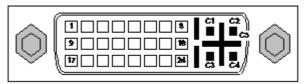
The specifications differ for each of the connectors so refer to the table below.

Unit/connector	Dual-Link	HDCP	Analog
PC analog unit	-	0	0
DVI unit DVI1	0	-	-
DVI unit DVI2	-	0	-

### 4.3.1 Connectors and pin assignments

Connector: DVI-I (74320-1004) made by Morex

Output: TMDS

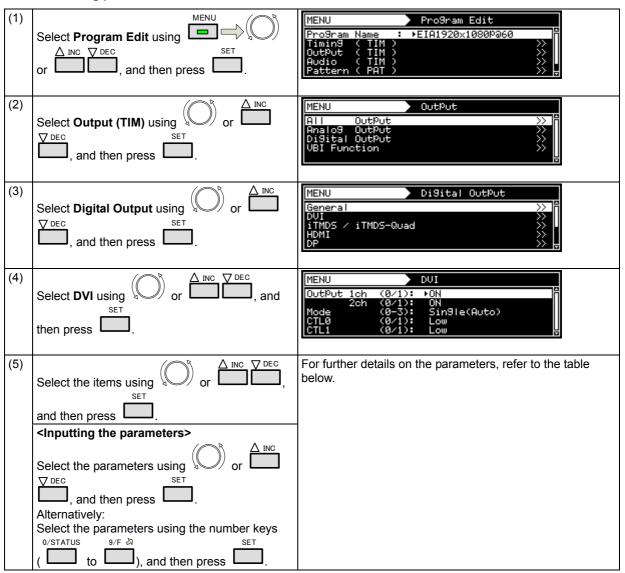


Pin no.	Signal	Pin no.	Signal	Pin no.	Signal
1	TMDS DATA2-	9	TMDS DATA1-	17	TMDS DATA0-
2	TMDS DATA2+	10	TMDS DATA1+	18	TMDS DATA0+
3	TMDS DATA2/4 G	11	TMDS DATA1/3 G	19	TMDS DATA0/5 G
4	TMDS DATA4-	12	TMDS DATA3-	20	TMDS DATA5-
5	TMDS DATA4+	13	TMDS DATA3+	21	TMDS DATA5+
6	DDC CLK	14	+5 V (DDC power supply *1)	22	TMDS CLK G
7	DDC DATA	15	Ground	23	TMDS CLK+
8	Analog Vsync	16	SENSE	24	TMDS CLK-
C1	Analog Red				
C2	Analog Green				
C3	Analog Blue				
C4	Analog Hsync				
C5	Analog Ground				

<sup>\*1:</sup> Restrictions apply to the supply current of the DDC power supply. Refer to "12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply."

### 4.3.2 DVI unit setting procedure

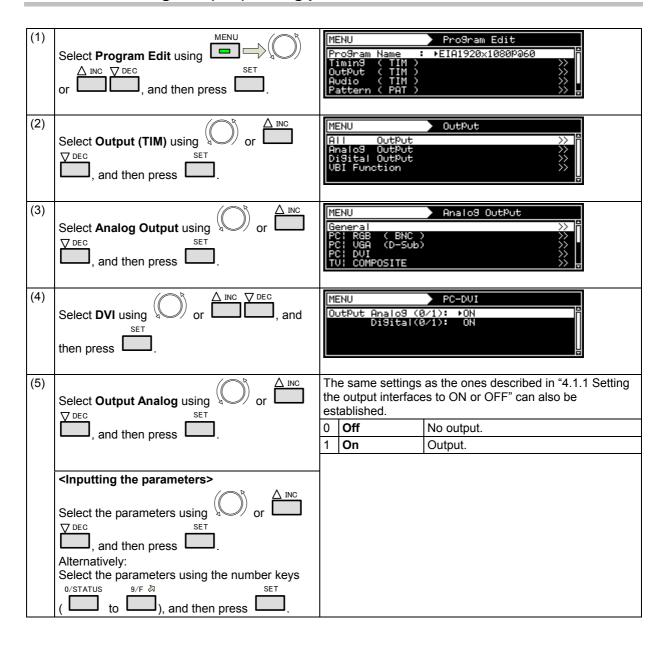
#### <DVI unit setting procedure>



### <DVI unit setting parameters>

(1)	Output 1ch (0/1)	This	This sets On or Off for each channel.		
	Output 2ch (0/1)		e same settings as the or rfaces to ON or OFF" ca	nes described in "4.1.1 Setting the output an also be established.	
		0 (	Off	No output.	
		1 (	On	Output.	
(2)	Mode (0/1)	setting which is independed selected. It is also possible by which the bit length for been set here is discarded "Single" can be selected to 165 MHz, and data can "Dual" can be selected w 330 MHz, and data can be from channel 2.		link format of the images to be output from DVI. A at of the bit length for pattern drawing can be to select the bit length automatically. The portion attern drawing exceeds the bit length which has A deficient portion is filled with zeros. When the dot clock frequency ranges from 25 MHz be output from output channels 1 and 2. The dot clock frequency ranges from 50 MHz to output from output channel 1. Data is not output (gray scale) for pattern drawing.  The data is output by Single Link from output	
			Single (o bits)	channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded.	
		1	Dual (8 bits)	The data is output by Dual Link from output channel 1. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded. Data is not output from channel 2.	
		2	Single (16 bits)	Up to 16 bits are output by Single Link using the two links of output channel 1. The portion by which the bit length for pattern drawing is deficient from the bit length which has been set here is filled with zeros.  The data is output by Single Link from output channels 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded.	
		3 Single (Auto)  The data is output by Single Link from out channels 1 and 2. Single (8 bits) or Single bits) is automatically selected depending bit length for pattern drawing.			
(3)	CTL0/CTL1	This	This is not normally used. Keep it at the low setting.		

### 4.3.3 PC analog unit (DVI) setting procedure



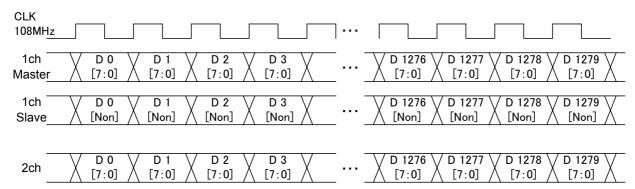
### 4.3.4 DVI data transfer systems

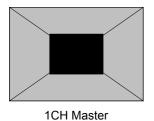
### <Specifications with 8-bit output>

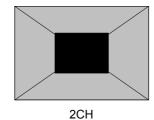
### [Single (8 bit)]

The same images are output to all channels 1 and 2. The output gray scale is 8 bits.

Given here as an example for explanatory purposes is a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz and the output gray scale is 8 bits.





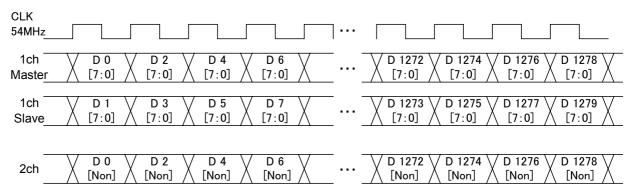


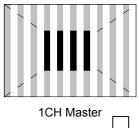
### [Dual (8 bit)]

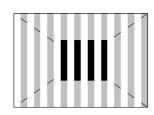
The images are output with channel 1 master and channel 1 slave making a pair. Eight bits are used for the output gray scale.

During dual output, the channel 2 output goes OFF.

Given here as an example for explanatory purposes is a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz and the output gray scale is 8 bits.

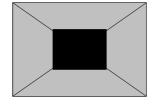






Master





### <Specifications with 16-bit output>

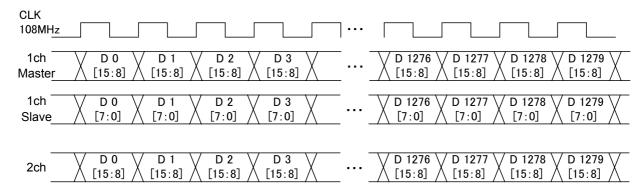
### [Single (16bit)]

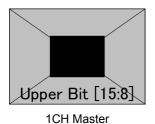
The 16-bit images are output with channel 1 master and channel 1 slave making a pair.

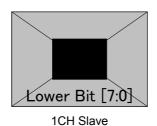
The higher 8 bits are output to the channel 1 master and the lower 8 bits to the channel 1 slave.

With channel 2, the higher 8 bits are output, and the lower bits are discarded.

Given here as an example for explanatory purposes is a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz and the output gray scale is 16 bits consisting of 8 bits for channel 1 and 8 bits for channel 2 channel.







Upper Bit [15:8]

2CH

### 4.3.5 Sync signal polarity setting

For further details on the setting procedure, refer to "4.1.2 Setting the sync signals to ON or OFF and setting the sync signal polarities." The same settings are established for both the DVI analog and digital signals.

### 4.3.6 EDID

For further details on the setting procedure, refer to "6.13.3 EDID."

Note: The DDC\_CLK frequency of DVI (channel 1) is fixed at 40 kHz. It does not exist in DDC\_CLK of Configuration. Also, the DDC\_CLK frequency of DVI (channel 2) changes in tandem with DDC\_CLK of Configuration.

### 4.3.7 HDCP

For further details on the setting procedure, refer to "8.1 HDCP settings."

### 4.3.8 DDC/CI

For further details on the setting procedure, refer to "6.13.4 DDC/CI."

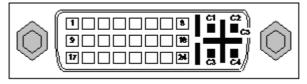
## 4.4 4K2K (iTMDS, iTMDS Quad)

The specifications differ for each of the connectors so refer to the table below.

Unit/connector	Dual-Link	HDCP	Analog
iTMDS unit 1CH	0	-	-
iTMDS unit 2CH	0	-	-
iTMDS Quad unit 1CH	-	-	-
iTMDS Quad unit 2CH	-	-	-
iTMDS Quad unit 3CH	-	-	-
iTMDS Quad unit 4CH	-	-	-

### 4.4.1 Connectors and pin assignments

Connector: DVI-I (74320-1004) made by Morex
 Output: iTMDS, TMDS (8-bit DVI compatible mode)



Pin no.	Signal	Pin no.	Signal	Pin no.	Signal
1	TMDS DATA2-	9	TMDS DATA1-	17	TMDS DATA0-
2	TMDS DATA2+	10	TMDS DATA1+	18	TMDS DATA0+
3	TMDS DATA2/4 G	11	TMDS DATA1/3 G	19	TMDS DATA0/5 G
4	TMDS DATA4-	12	TMDS DATA3-	20	TMDS DATA5-
5	TMDS DATA4+	13	TMDS DATA3+	21	TMDS DATA5+
6	DDC CLK	14	+5 V (DDC power supply *1)	22	TMDS CLK G
7	DDC DATA	15	Ground	23	TMDS CLK+
8	Analog Vsync	16	SENSE	24	TMDS CLK-
C1	Analog Red				
C2	Analog Green				
C3	Analog Blue				
C4	Analog Hsync				
C5	Analog Ground				

<sup>\*1:</sup> Restrictions apply to the supply current of the DDC power supply. Refer to "12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply."

### 4.4.2 4K2K (iTMDS) unit setting procedure

### <4K2K (iTMDS) unit setting procedure>

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU
(2)	Select <b>Output (TIM)</b> using or	MENU OutPut  AII OutPut >> C Analo9 OutPut >> C Di9ital OutPut >> C VBI Function >> C
(3)	Select <b>Digital Output</b> using or	MENU Digital OutPut  General DUI iTMDS / iTMDS-Quad HDMI DP
(4)	Select <b>iTMDS</b> using or $\triangle$ INC $\bigvee$ DEC or and then press .	MENU   iTMDS / iTMDS-Quad
(5)	Select the items using or	For further details on the parameters, refer to the table below.

### <iTMDS (4K×2K) unit setting parameters>

(1)	Output 1ch (0/1) Output 2ch (0/1) Output Ouad 1.2ch (0/1)	This sets On or Off for each channel.  The same settings as the ones described in "4.1.1 Setting the output interfaces to ON or OFF" can also be established.		
	Output Quad 1,2ch (0/1) Output Quad 3,4ch (0/1)	0	Off	No output.
		1	On	Output.
(2)	iTMDS or DVI (0/1)	Th	is sets the output signal t	format.
		0	DVI	The signals are output as DVI compatible signals.  Eight bits per link are output.
		1	iTMDS	The signals are output as iTMDS signals.
				Twelve bits per link are output.
(3)	Mode (0/6)	iTN ca po wh	MDS. A setting which is in be selected. It is also prition by which the bit length ich has been set here is When the dot clock freq Single Link can be select from output channels 1 distributed to and output When the dot clock freq Link can be selected, ar output channels 1 and 2 and the data of channel When the dot clock freq Quad Link can be select channels 1 and 2. With is combined and output When the dot clock freq selecting Octal Link and output by combining the data of board #2 output of board #1 channels 1, and 4 are combined and In the Quad Link or Oc operation which uses the performed.  With the VM-1824-A, the with Single Link outputs 1 and 2 of Single Link. Schannels 3 and 4 of Sin	link format of the images to be output from independent of the bit length for pattern drawing ossible to select the bit length automatically. The gith for pattern drawing exceeds the bit length discarded. A deficient portion is filled with zeros. Juency is in the range of 25 MHz to 165 MHz, cited, and the data can be distributed to and output and 2. With the VM-1824-A, the data can be to from channels 1, 2, 3 and 4.  Juency is in the range of 50 MHz to 330 MHz, Dual and the data can be distributed to and output from 2. With the VM-1824-A, the data of channels 1 and els 3 and 4 are combined and output by Dual Link. Juency is in the range of 296 MHz to 660 MHz, and the data can be output using output the VM-1824-A, the data of channels 1, 2, 3 and 4 by Quad Link.  Juency is in the range of 592 MHz to 1320 MHz, by a by using two output boards, the data can be data of board #1 output channels 1 and 2 and the channels 1 and 2. With the VM-1824-A, the data 2, 3 and 4 and data of board #2 channels 1, 2, 3 doutput by Octal Link.  Juency is in the range of 592 MHz to 1320 MHz, by a by using two output boards, the data can be data of board #1 output channels 1 and 2 and the channels 1 and 2. With the VM-1824-A, the data 2, 3 and 4 and data of board #2 channels 1, 2, 3 doutput by Octal Link.  Juency is in the range of 592 MHz to 1320 MHz, by a by using two output channels 1, 2, 3 doutput by Octal Link.  Juency is in the range of 592 MHz to 1320 MHz, by a by given by Octal Link the VM-1824-A, the data 2, 3 and 4 and data of board #2 channels 1, 2, 3 doutput by Octal Link.  Juency is in the range of 592 MHz to 1320 MHz, by a by using two output by Octal Link corresponds to channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded. (Max. 12 bits with the iTMDS format) The data is output by Dual Link from output channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded. (Max. 12 bits with the iTMDS format)
				length for pattern drawing exceeds 8 bits is discarded. (Max. 12 bits with the iTMDS format)
		2	Quad (8bit)	The data is output by Quad Link using output channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded. (Max. 12 bits with the iTMDS format) The <b>4K×2K screen splitting operation</b> which uses the frame memory on the board is performed. For details on the screen splitting method, refer to "Split" in the next section.

		2	Ootal (9hit)	Divising two output boards the details and the
		3	Octal (8bit)  Single (16bit)	By using two output boards, the data is output by Octal Link by combining the data of board #1 output channels 1 and 2 and the data of board #2 output channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded. (Max. 12 bits with the iTMDS format)  The 4K×2K screen splitting operation which uses the frame memory on the board is performed. For details on the screen splitting method, refer to "Split" in the next section.  Up to 16 bits can be output by Single Link using
				the two linksMaster and Slaveof one connector. The portion by which the bit length for pattern drawing is deficient from the bit length which has been set here is filled with zeros. The higher bits are output to the Master and the lower bits to the Slave.
		4	Dual (16bit)	Up to 16 bits can be output by Dual Link using two connectors. The portion by which the bit length for pattern drawing is deficient from the bit length which has been set here is filled with zeros.  The higher bits are output to output channel 1 and the lower bits to output channel 2.
		5	Single (Auto)	The data is output by Single Link from output channels 1 and 2. <b>Single (8 bits)</b> or <b>Single (16 bits)</b> is automatically selected depending on the bit length for pattern drawing.
		6	Dual (Auto)	The data is output by Single Link from output channels 1 and 2. <b>Single (8 bits)</b> or <b>Single (16 bits)</b> is automatically selected depending on the bit length for pattern drawing.
(4)	Split (0/8)	This selects the screen splitting method for the <b>4K×2K screen splitting operation</b> which uses the frame memory on the board or boards.  QuadLink		
		0	MODE0	Screen split-into-4 output in the form of a square divided into 4 equal parts
		1	MODE1	Screen vertically split-into-4 output
		2	MODE2	Screen horizontally split-into-2 output
		3	MODE3	Screen vertically split-into-2 output
		4	MODE0 (x4 Mode)	No screen splitting (Non Dividing Mode)
		5	MODE1 (x4 Mode)	Screen splitting (1) (Normal Mode)
		6	MODE2 (x4 Mode)	Screen splitting (2) (Cross Mode)
		7	MODE3 (x4 Mode)	Screen splitting (3) (Dividing Normal Mode)
		8	MODE4 (x4 Mode)	Screen splitting (4) (Dividing Cross Mode)
		OctalLink		
		1	MODE0	Screen split-into-4 output in the form of a square divided into 4 equal parts + screen vertically split-into-2 output
		2	MODE1	Screen vertically split-into-4 output + screen vertically split-into-2 output
		3	MODE2	Screen horizontally split-into-2 output + screen vertically split-into-2 output
		4	MODE3	Screen vertically split-into-2 output + screen vertically split-into-2 output
(5)	CTL0/CTL1	Th	is is not normally used. K	Geen it at the low setting.

### 4.4.3 iTMDS data transfer systems

### Specifications for Quad Link modes during 4K×2K mode output

During 4K×2K mode output, the data of four channels are combined using Dual Link equivalent to two channels to output one screen.

- \* As shown in the figure below, what is output as the data of channels 1 to 4 for the Master and Slave of Dual Link of VM-1824 is described in this section.
- \* Also described below in this section is the Single Link data of the VM-1824-A which is output as the data of channels 1 to 4.

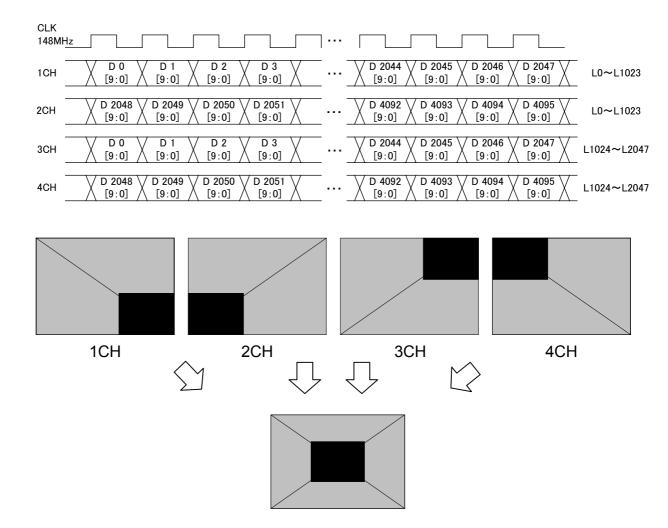
#### 1CH Link0 Link1 RGB 8-12Bit Dual [11:0] Link2 Link Master 1CH Connector 2CH Link3 Link4 RGB 8-12Bit Dual [11:0] Link5 Link Srave 3CH Link0 Link1 RGB 8-12Bit Dual [11:0] Link2 Link Master 2CH Connector 4CH Link3 Link4 RGB 8-12Bit Dual [11:0] Link5 Link Srave

[4K×2K 4-channel output]

# [1] MODE0 (Quad Link ) (screen split-into-4 output in the form of a square divided into 4 equal parts)

The screen is split into 4 equal parts using channels 1, 2, 3 and 4.

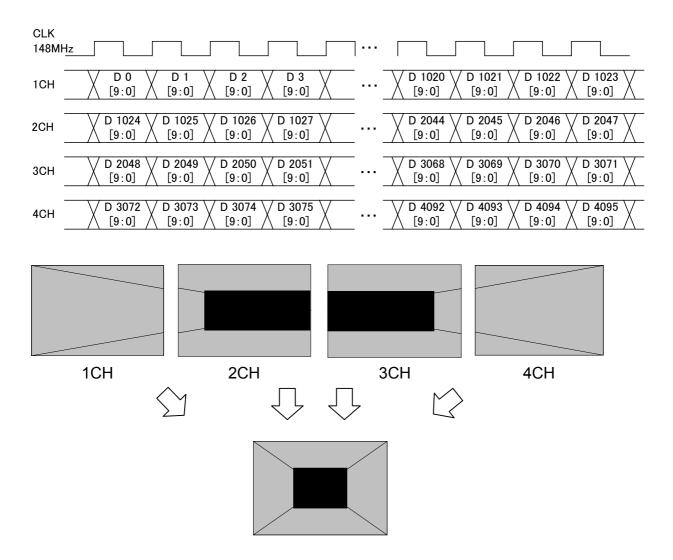
Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



### [2] MODE1 (QuadLink) (screen vertically split into 4)

The screen is split vertically into for parts and allocated from the left in the sequence of channel 1, channel 3, channel 2 and channel 4.

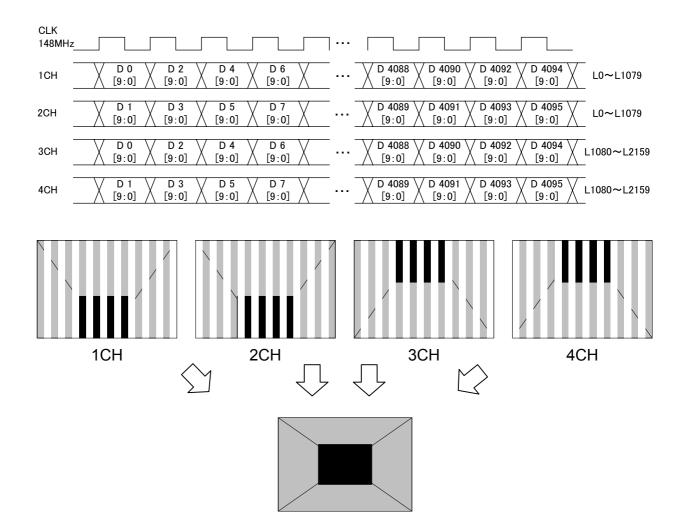
Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



### [3] MODE2 (Quad Link) (screen horizontally split into 2)

Using channels 1 and 2, the top half of the image is output in the even and odd numbers; similarly, using channels 3 and 4, the bottom half of the image is output in the even and odd numbers.

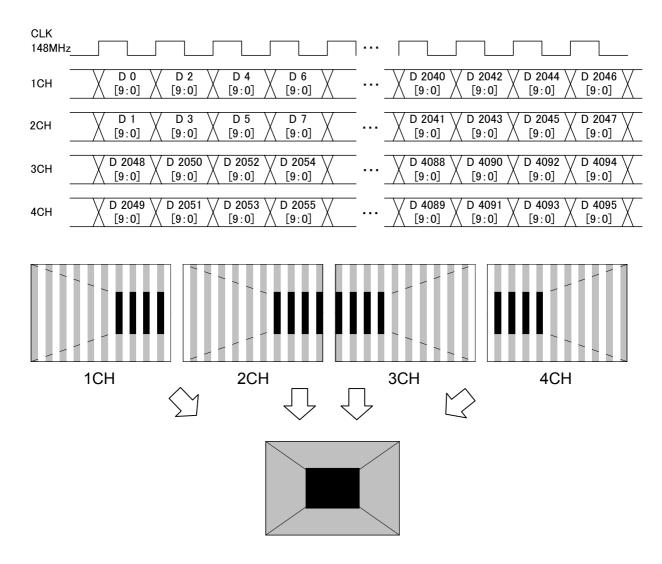
Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



### [4] MODE3 (Quad Link) (screen vertically split into 2)

Using channels 1 and 2, the left half of the image is output in the even and odd numbers; similarly, using channels 3 and 4, the right half of the image is output in the even and odd numbers.

Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



### Specifications for modes during ×4 mode (Full HD 240 Hz mode) output

The ×4 mode is an output mode designed for the inspection of flat panel devices which support the Full HD 240 Hz output. By connecting the output of the VG generator to the IA-1540 (iTMDS-LVDS converter box) and by halving the output in the converter box,

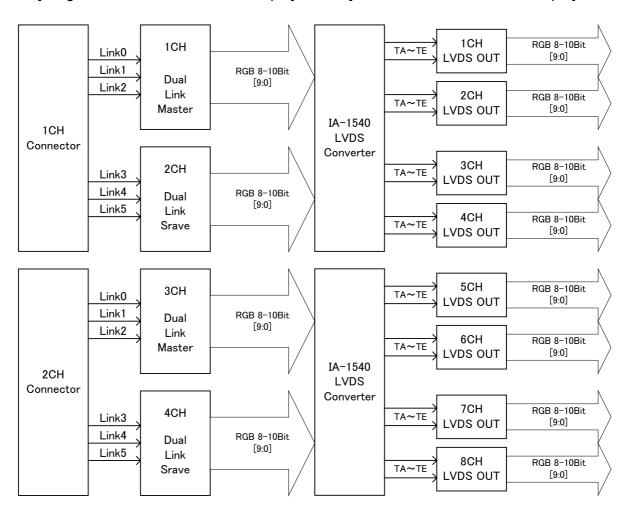
The explanation provided below concerns the signals of the four channels output from the VG generator. For an explanation concerning an 8-channel output from LVDS, refer to section "4.13.3 Data transfer systems (V-By-One HS)."

- \* As shown in the figure below, what is output as the data of channels 1 to 4 for the Master and Slave of Dual Link of VM-1824 is described in this section.
- Also described below in this section is the Single Link data of the VM-1824-A which is output as the data of channels 1 to 4.

### [Full HD 240 Hz output]

### [VG generator iTMDS 4-channel output]

### [IA converter box 8-channel output]



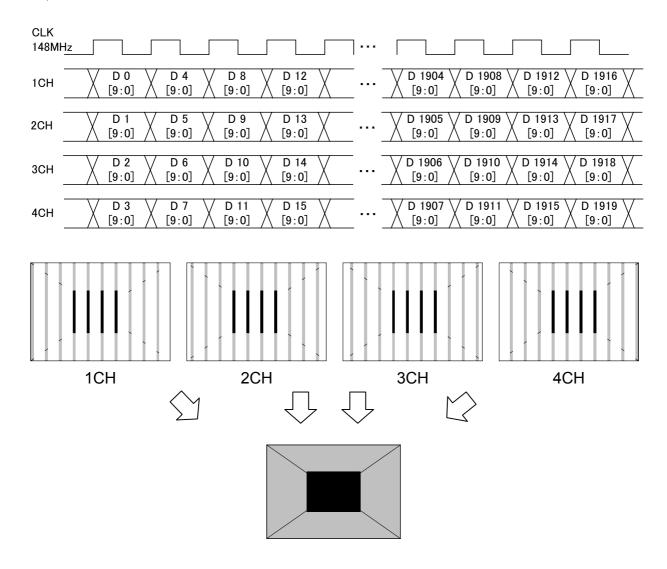
### [1] MODE0 ×4 Mode (Quad Link ) (Non Dividing Mode)

Using channels 1, 2, 3 and 4, the image is output under the pixel assignment given below without splitting the screen.

Given here as an example for explanatory purposes is a case where the resolution is  $1920 \times 1080$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.

The ×4 mode is an output mode designed for the inspection of flat panel devices which support the Full HD 240 Hz output. By connecting the output of the VG generator to the IA-1540 (iTMDS-LVDS converter box) and by halving the output in the converter box,

The explanation provided below concerns the signals of the four channels output from the VG generator. For an explanation concerning an 8-channel output from LVDS, refer to section "4.13.3 Data transfer systems (V-By-One HS)."



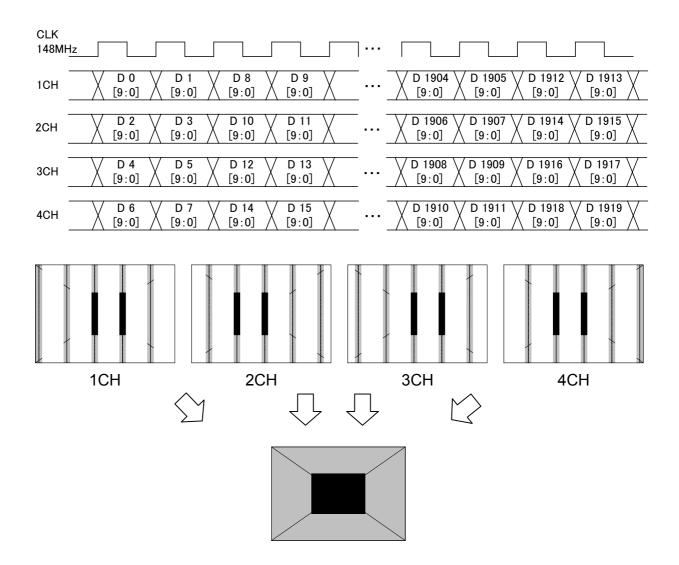
### [2] MODE1 ×4 Mode (Quad Link) (Normal Mode)

Using channels 1, 2, 3 and 4, the image is output under the pixel assignment given below without splitting the screen.

Given here as an example for explanatory purposes is a case where the resolution is  $1920 \times 1080$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.

The ×4 mode is an output mode designed for the inspection of flat panel devices which support the Full HD 240 Hz output. By connecting the output of the VG generator to the IA-1540 (iTMDS-LVDS converter box) and by halving the output in the converter box,

The explanation provided below concerns the signals of the four channels output from the VG generator. For an explanation concerning an 8-channel output from LVDS, refer to section "4.13.3 Data transfer systems (V-By-One HS)."



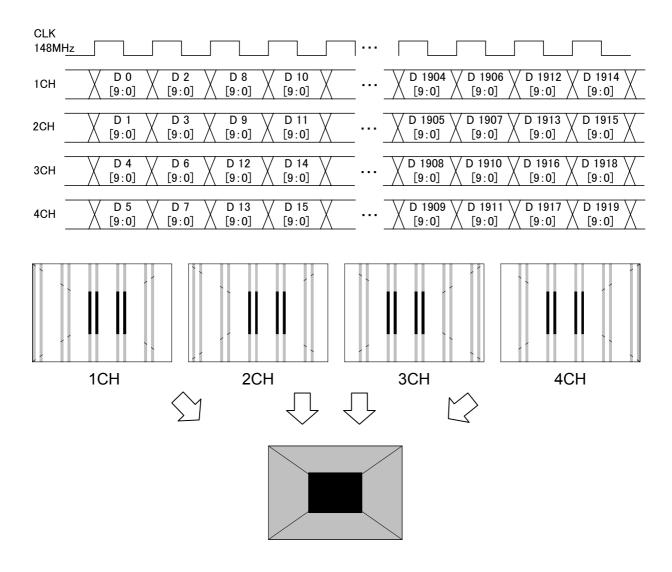
### [3] MODE2 ×4 Mode (Quad Link) (Cross Mode)

Using channels 1, 2, 3 and 4, the image is output under the pixel assignment given below without splitting the screen.

Given here as an example for explanatory purposes is a case where the resolution is  $1920 \times 1080$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.

The ×4 mode is an output mode designed for the inspection of flat panel devices which support the Full HD 240 Hz output. By connecting the output of the VG generator to the IA-1540 (iTMDS-LVDS converter box) and by halving the output in the converter box,

The explanation provided below concerns the signals of the four channels output from the VG generator. For an explanation concerning an 8-channel output from LVDS, refer to section "4.13.3 Data transfer systems (V-By-One HS)."



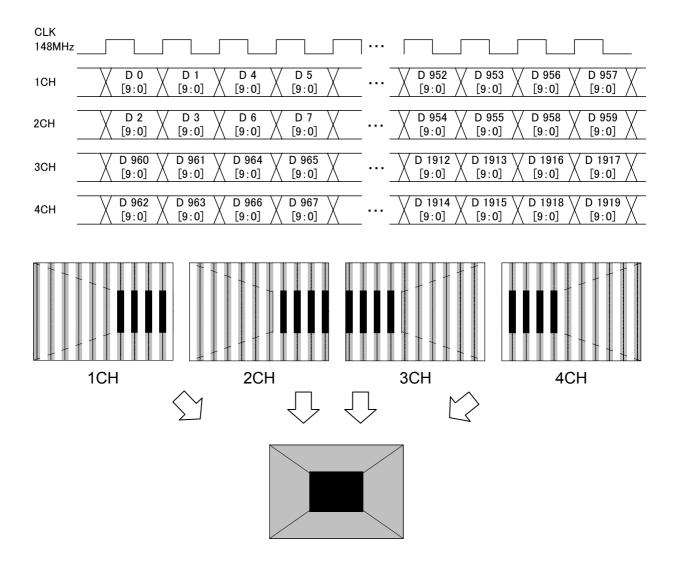
#### [4] MODE3 ×4 Mode (Quad Link) (Dividing Normal Mode)

Using channels 1 and 2, the right half of the image is output; similarly, using channels 3 and 4, the left half of the image is output. The pixel assignment is given below.

Given here as an example for explanatory purposes is a case where the resolution is  $1920 \times 1080$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.

The ×4 mode is an output mode designed for the inspection of flat panel devices which support the Full HD 240 Hz output. By connecting the output of the VG generator to the IA-1540 (iTMDS-LVDS converter box) and by halving the output in the converter box,

The explanation provided below concerns the signals of the four channels output from the VG generator. For an explanation concerning an 8-channel output from LVDS, refer to section "4.13.3 Data transfer systems (V-By-One HS)."



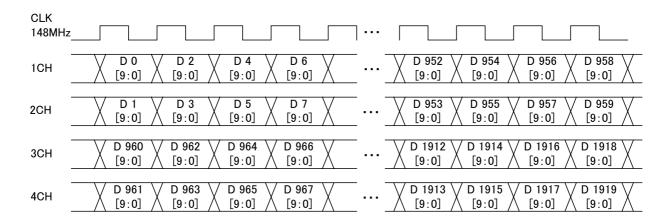
## [5] MODE4 ×4 Mode (Quad Link) (Dividing Cross Mode)

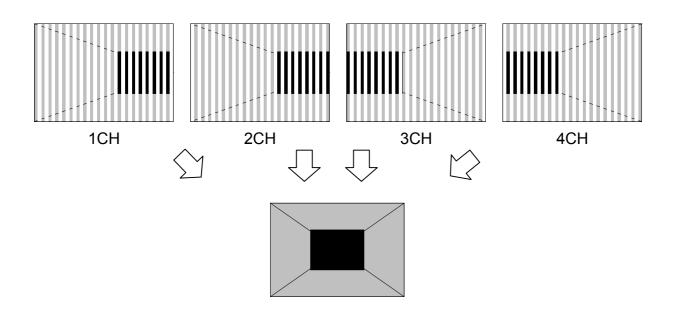
Using channels 1 and 2, the right half of the image is output; similarly, using channels 3 and 4, the left half of the image is output. The pixel assignment is given below.

Given here as an example for explanatory purposes is a case where the resolution is  $1920 \times 1080$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.

The ×4 mode is an output mode designed for the inspection of flat panel devices which support the Full HD 240 Hz output. By connecting the output of the VG generator to the IA-1540 (iTMDS-LVDS converter box) and by halving the output in the converter box,

The explanation provided below concerns the signals of the four channels output from the VG generator. For an explanation concerning an 8-channel output from LVDS, refer to section "4.13.3 Data transfer systems (V-By-One HS)."





# Specifications for Octal Link modes during 4K×2K 2-board mode (4K×2K 120 Hz mode) output

This mode is an output mode designed for 4K×2K 120 Hz inspections.

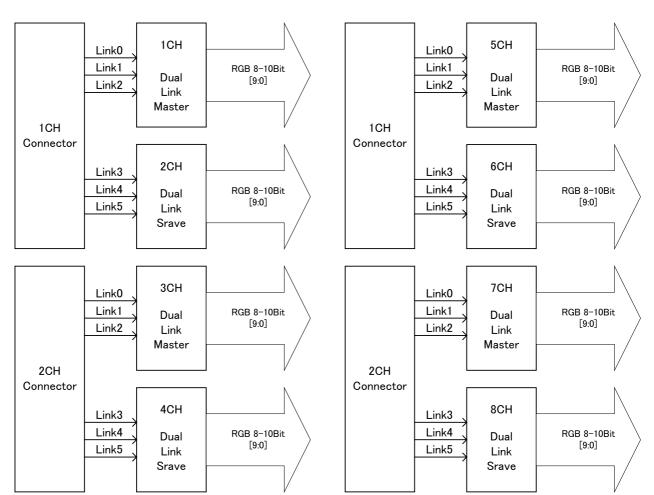
During the 4K×2K 2-board mode output, Dual Link equivalent to four channels is used through the use of two output boards, and one screen is produced by combining the data of eight channels.

- \* Described below in this section is a case where, in relation to the first VM-1824 board (lower level), the data is output in channels 1 to 4 for the Master and Slave of Dual Link.
- \* Described below in this section is a case where, in relation to the second VM-1824 board (upper level), the data is output in channels 5 to 8 for the Master and Slave of Dual Link.
- \* Described below in this section is a case where, in relation to the first VM-1824-A board (lower level), the Single Link data is output as channels 1 to 4.
- Described below in this section is a case where, in relation to the second VM-1824-A board (upper level), the Single Link data is output as channels 5 to 8.

#### [4K×2K 2-board 8-channel output]

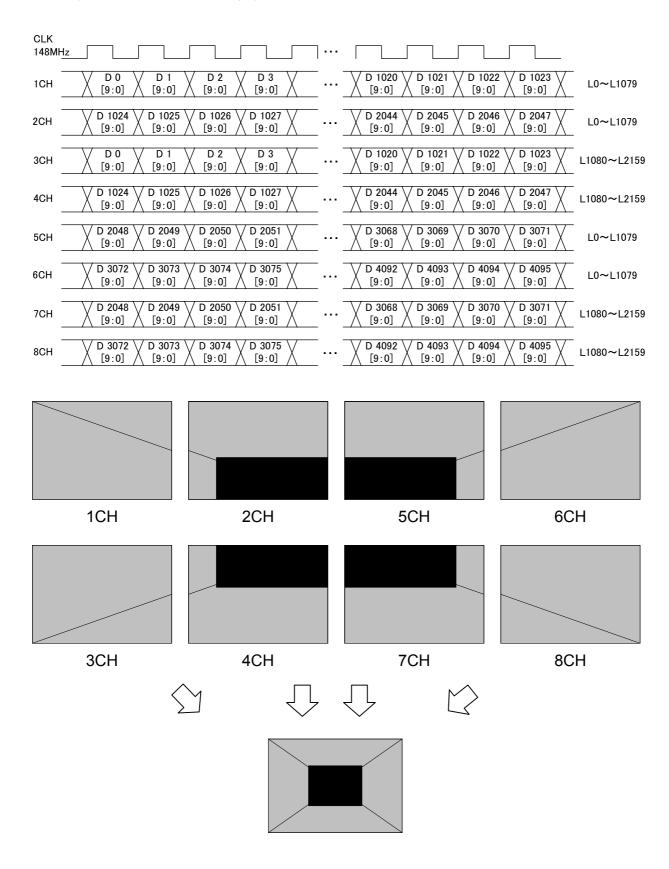
#### [1st board output (lower level)]

#### [2nd board output (upper level)]



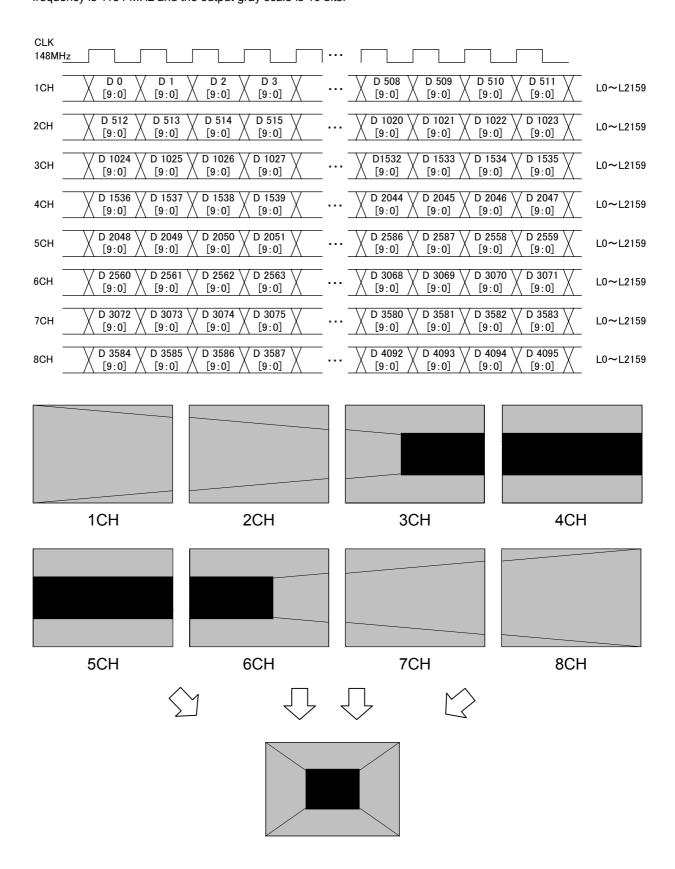
# [1] MODE0 (Octal Link) (screen split into 4 in the form of a square divided into 4 equal parts + screen split vertically into 2)

Channels 1 to 8 are used. The screen is split into 4 in the form of a square divided into 4 equal parts and output from each of the output boards, and then it is split vertically into two at each board, and output.



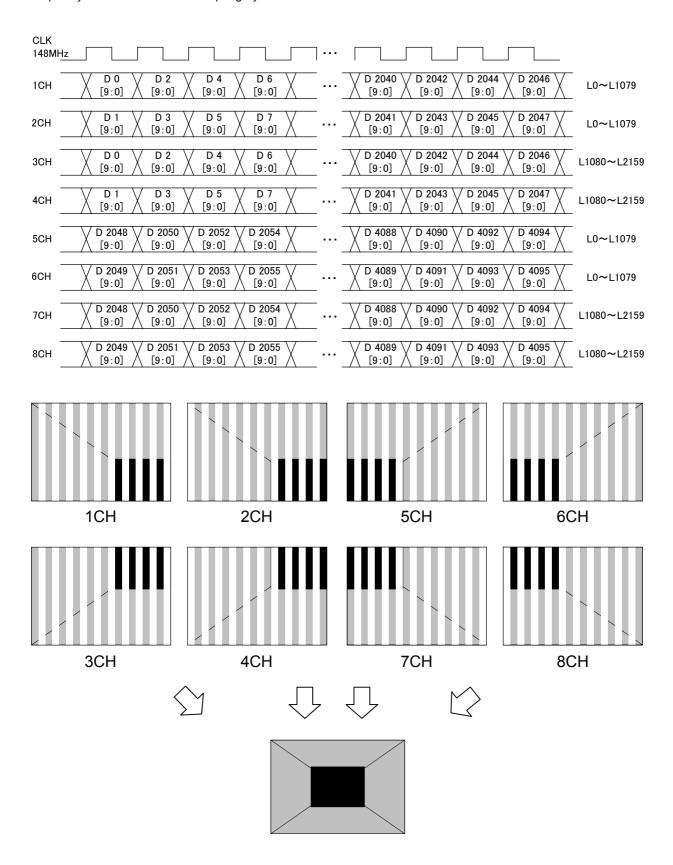
## [2] MODE1 (Octal Link) (screen split vertically into 4 + screen split vertically into 2)

Channels 1 to 8 are used. The screen is split vertically into 4 and output from each of the output boards, and then it is split vertically into two at each board and output.



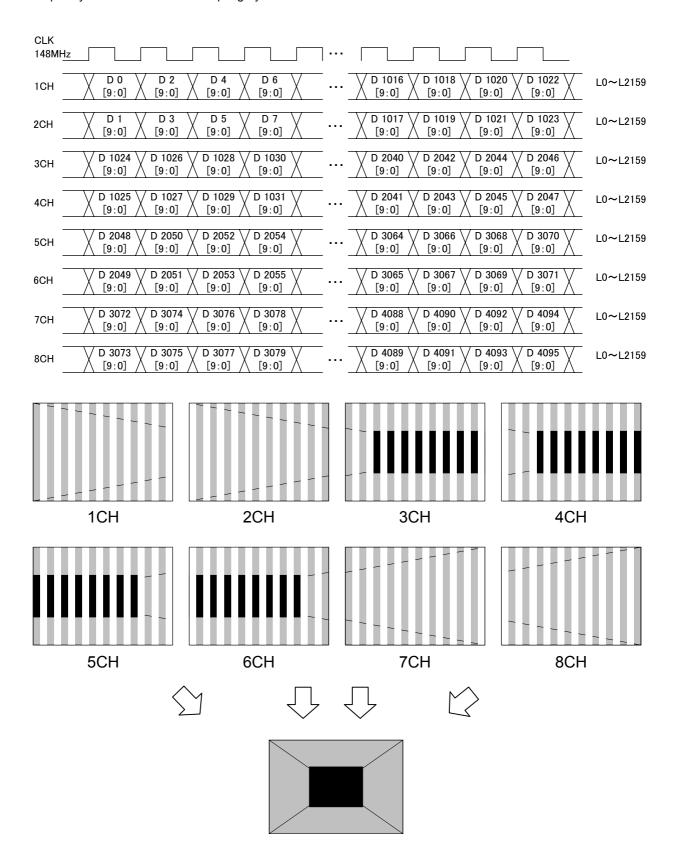
#### [3] MODE2 (Octal Link) (screen split horizontally into 2 + screen split vertically into 2)

Channels 1 to 8 are used. The screen is split horizontally into 2 and output from each of the output boards, and then it is split vertically into two at each board, and output.



#### [4] MODE3 (Octal Link) (screen split vertically into 2 + screen split vertically into 2)

Channels 1 to 8 are used. The screen is split vertically into 2 and output from each of the output boards, and then it is split vertically into two at each board, and output.



#### Specifications of bit assignment during 16-bit (8 + 8 bits) output

#### DVI multiple bit output

Sixteen bits are output by using two DVI channels and outputting the data using 8 + 8 bits.

The higher 8 bits are allocated to one channel and the lower 8 bits to the other channel.

If the setting involves 15 or fewer bits, "0" is entered for the lower bits which will not be used.

#### • iTMDS multiple bit output

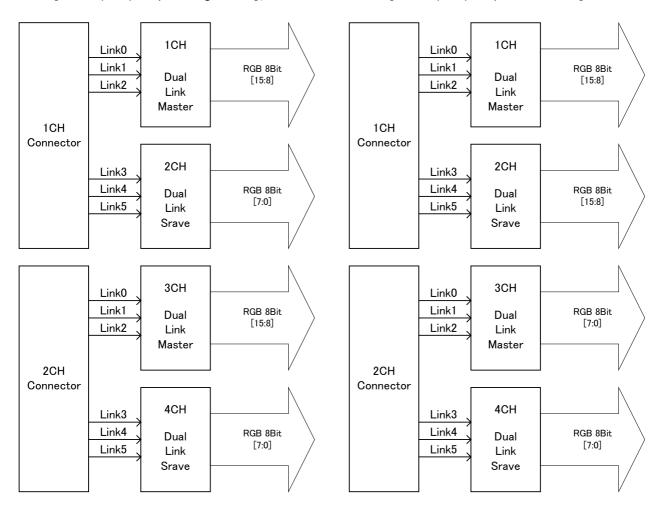
When outputting multiple bits using the iTMDS format, select iTMDS as the output setting.

When iTMDS has been selected, the multiple-bit output mode (10 bits or 12 bits) which uses one iTMDS channel is automatically tracked in tandem with the number of output bits regardless of which bit mode was selected.

- \* The 8 + 8 bit multi-channel output mode takes effect only when DVI has been selected as the output setting. If iTMDS has been selected, the multiple-bit output mode (10 bits or 12 bits) which uses one iTMDS channel is automatically tracked in tandem with the number of output bits regardless of which bit mode was selected.
- \* As shown in the figure below, what is output as the data of channels 1 to 4 for the Master and Slave of Dual Link of VM-1824 is described in this section.
- \* Also described below in this section is the Single Link data of the VM-1824-A which is output as the data of channels 1 to 4.

#### [16-bit (8+8) output Single Link]

#### [16-bit (8+8) output Dual Link]



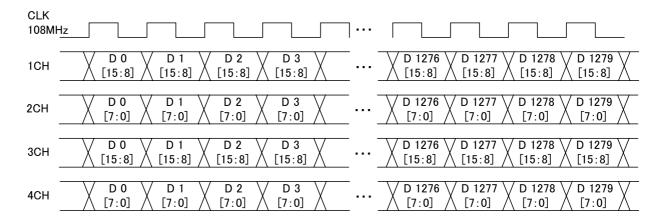
## [1] 16Bit (Single Link)

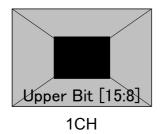
Sixteen-bit images are output using the channel 1 of the Master paired up with the channel 1 of the Slave.

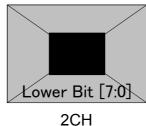
The higher 8 bits are output to channel 1 of the Master and the lower 8 bits to channel 1 of the Slave.

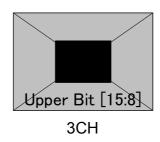
Also, the 16-bit images are output using channel 2 of the Master paired up with channel 2 of the Slave.

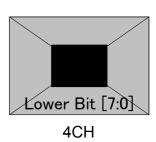
The higher 8 bits are output to channel 2 of the Master and the lower 8 bits to channel 2 of the Slave.











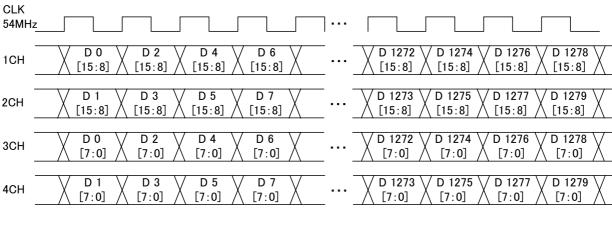
## [2] 16Bit (Dual Link)

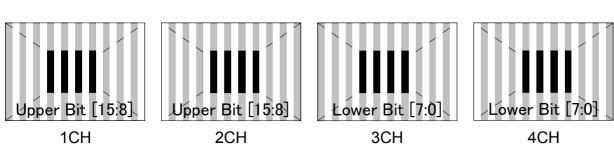
Dual Link images are output using channel 1 of the Master paired up with channel 1 of the Slave.

The higher 8 bits are output to channel 1.

Also, the Dual Link images are output using the 2-channel of the Master paired up with the 2-channel of the Slave.

The lower 8 bits are output to channel 2.





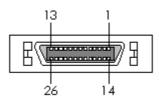
## 4.4.4 Sync signal polarity settings

For the detailed setting procedure, refer to "4.1.2 Setting the sync signals to ON or OFF and setting the sync signal polarities."

## 4.5 LVDS

## 4.5.1 Connectors and pin assignments

• Connector: MDR 10226-1210-VE made by 3M



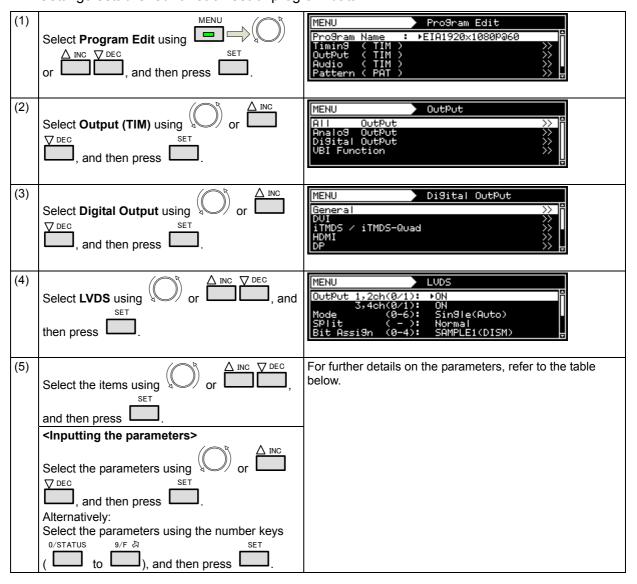
Pin no.	Signal	Pin no.	Signal
1	GND	14	TA-
2	TAG	15	TA+
3	Reserve (leave this unconnected)	16	GND
4	TB-	17	TBG
5	TB+	18	Reserve (leave this unconnected)
6	TC-	19	TCG
7	TC+	20	TE-
8	TEG	21	TE+
9	Reserve (leave this unconnected)	22	TCLK-
10	TCLKG	23	TCLK+
11	+5 V/+3.3 V (DDC power supply *1)	24	+5 V/+3.3 V (DDC power supply *1)
12	TD-	25	TDG
13	TD+	26	GND

<sup>\*1:</sup> Restrictions apply to the supply current of the DDC power supply. Refer to "12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply."

## 4.5.2 LVDS setting procedure

Some LVDS settings are established for each set of program data, and other LVDS settings are based on the device settings.

#### Settings established for each set of program data

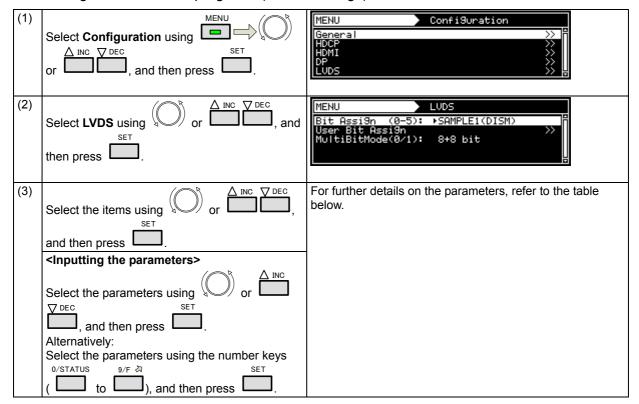


## <LVDS setting parameters selected for each set of program data>

(1)	Output 1,2ch (0/1)	This sets On or Off for each channel.							
(1)	Output 1,2ch (0/1) Output 3,4ch (0/1)	The same settings as the ones described in "4.1.1 Setting the output							
	- mep as 4) Toll (0/1)	inter	faces to ON or OFF" ca	an also be established.					
			Off	No output.					
		1 (	On	Output.					
(2)	Mode (0/1)	A se	This sets the bit length and link format of the images to be output from LVDS. A setting which is independent of the bit length for pattern drawing can be selected. It is also possible to select the bit length automatically. The portion						
		by w beer	hich the bit length for poset here is discarded.	attern drawing exceeds the bit length which has A deficient portion is filled with zeros.					
		to 34	40 MHz, and data can b	nen the dot clock frequency ranges from 80 MHz be output. en the dot clock frequency ranges from 40 MHz to					
			MHz, and data can be	•					
		to 13	35 MHz, and data can b						
				(gray scale) for pattern drawing"					
		0	Single (10 bits)	The data is output by Single Link from output channel 1. The portion by which the bit length for pattern drawing exceeds 10 bits is discarded. The same data as for output channel 1 is output from output channels 2, 3 and 4.					
		1	Dual (10 bits)	The data is output by Dual Link from output channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 10 bits is discarded. The same data as for output channels 1 and 2 is output from output channels					
				3 and 4.					
		2	Quad (10 bits)	The data is output by Quad Link from output channels 1, 2, 3 and 4. The portion by which the bit length for pattern drawing exceeds 10 bits is discarded.					
		3	Single (16 bits)	The data is output by Single Link from output channels 1 and 2. The portion by which the bit length for pattern drawing is deficient from 16 bits length is filled with zeros. The same data as for output channels 1 and 2 is output from output channels 3 and 4.					
		4	Dual (16 bits)	The data is output by Quad Link from output channels 1, 2, 3 and 4. The portion by which the bit length for pattern drawing is deficient from 16 bits length is filled with zeros.					
		(3) 5	Single (Auto)	The data is output by Single Link. Single (10 bits) or Single (16 bits) is automatically selected depending on the bit length for pattern drawing.					
		6	Dual (Auto)	The data is output by Dual Link. <b>Dual (10 bits)</b> or <b>Dual (16 bits)</b> is automatically selected depending on the bit length for pattern drawing.					
(3)	Split			output, and sets channels 1, 2, 3 and 4 as the					
		outp	ut channels. en a setting other than <b>I</b>	Normal is selected, all other outputs are shut					
		The		elected only when the <b>Single (10 bits) mode</b> has					
		0 1	Normal						
			setting below can be se	elected only when the Single (16 bits) mode has					
		0 1	Normal						
			setting below can be so	elected only when the <b>Single (Auto) mode</b> has					
		1							

			e setting below can be se en set.	elected only when the <b>Dual (10 bits) mode</b> has					
		0	Normal						
		1	2Split						
		The setting below can be selected only when the <b>Dual (16 bits) mode</b> has							
		been set.							
		0	Normal						
		1	2Split						
			•	elected only when the <b>Dual (Auto) mode</b> has					
		bee	en set.						
		0	Normal						
		1	2Split						
		The setting below can be selected only when the <b>Quad (10 bits) mode</b> has							
			en set.						
		0 Normal							
		1	2Split						
(4)	D'( A ! (O . I)	2	4Split						
(4)	Bit Assign (0-4)		is selects the bit assignm	ent.  when " <b>refer Program</b> " has been selected as					
			e Bit Assign setting among						
			r further details, refer to "						
			SAMPLE1 (DISM)	Based on the DISM standard.					
		1	SAMPLE2 (OLDI)	Based on the OpenLDI standard.					
		2	User1	These are set by the user.					
		3	User2	* The actual bit assignment is set using the					
			User3	SP-8870 software program which is provided					
(5)	CTI 4 4 0ab (0/4)	with the VG generator.  This is not normally used. Keep it at the low setting.							
(5)	CTL1 1,2ch (0/1)		•	· •					
(6)	CTL2 1,2ch (0/1)	This is not normally used. Keep it at the low setting.  This is not normally used. Keep it at the low setting.							
(7)	CTL1 3,4ch (0/1)								
(8)	CTL2 3,4ch (0/1)	Ιh	This is not normally used. Keep it at the low setting.						

## ■ Settings common to all programs (device settings)



#### <LVDS setting parameters in the device settings>

(1)	Bit Assign (0-5)	Th	is selects the bit assignm	ent.						
		Fo	For further details, refer to "4.5.4 Bit arrays."							
		0	SAMPLE1 (DISM)	Based on the DISM standard.						
		1	SAMPLE2 (OLDI)	Based on the OpenLDI standard.						
		2	User1	These are set by the user.						
		3	User2							
		4	User3							
		5	refer Program	The bit assignment accords with the program						
				settings.						
(2)	User Bit Assign	Th	is displays the bit assigni	ment of the user settings.						
		*		se the SP-8870 software program which is						
			provided with the VG ge	nerator.						
(3)	Multi Bit Mode (0/1)	Th	is selects the bit assignm	ent to be used when two output connectors are						
		us	ed.							
		Fo	For further details, refer to "4.5.4 Bit arrays."							
		0	8+8 bit							
		1 10+6 bit								

## 4.5.3 Data transfer system

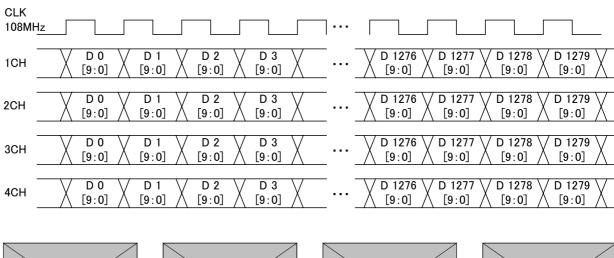
Settings (1) to (6) in the table below are available as the data train	nsfer system settings.

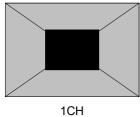
Item	Description	Output from other units
Setting (1)	What is drawn is output as is. (Same output for channels 1 to 4)	ON
Setting (2)	The data is output dot by dot to channels 1 and 2. (Same output for channels 3 and 4)	ON
Setting (3)	The data is output dot by dot to channels 1, 3, 2 and 4.	ON
Setting (4)	The left half of the screen is output to channel 1, and the right half of the screen is output to channel 2. (Same output for channels 3 and 4)	OFF
Setting (5)	One-fourth of the screen each is output to channels 1, 3, 2 and 4 in this order.	OFF
Setting (6)	The left half of the screen is output to channels 1 and 3, and the right half of the screen is output to channels 2 and 4.	OFF

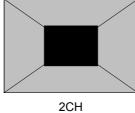
## <Specifications for outputs of 8 to 10 bits>

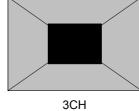
## Setting (1) [Single (10 bits)], [Normal]

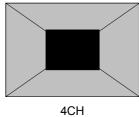
The same image is output to all four channels. The output gray scale is 8 to 10 bits.







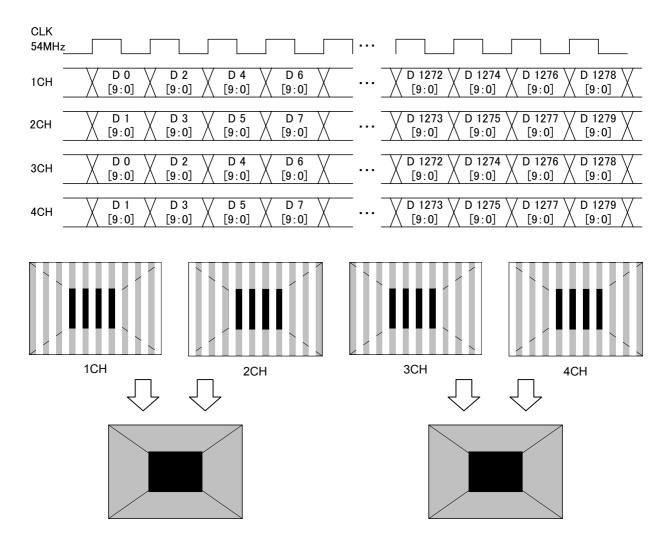




## Setting (2) [Dual (10 bits)], [Normal]

The images are output with channels 1 and 2 forming one set and channels 3 and 4 forming another set.

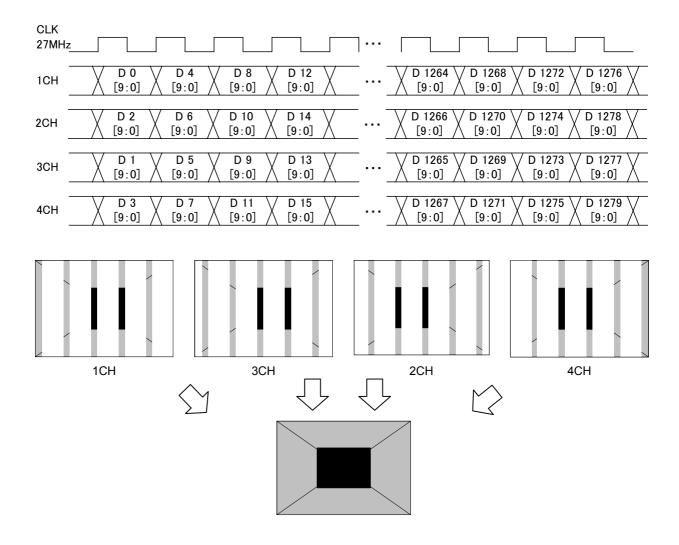
The output gray scale is 8 to 10 bits.



## Setting (3) [Quad (10 bits)], [Normal]

The images are output to channels 1, 2, 3 and 4 in this order.

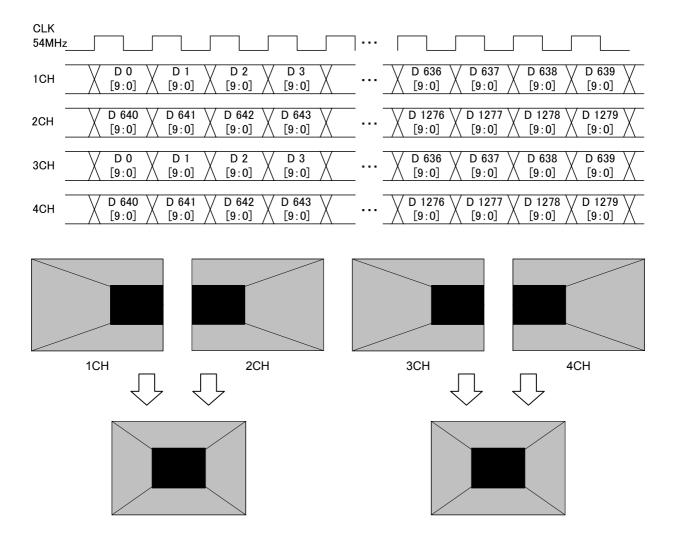
The output gray scale is 8 to 10 bits.



## Setting (4) [Dual (10 bits)], [2 split]

The images are output with channels 1 and 2 forming one set and channels 3 and 4 forming another set. If this is described with the channel 1 and 2 set used as an example, the left half of the image is allocated and output to channel 1, and the right half of the image is allocated and output to channel 2.

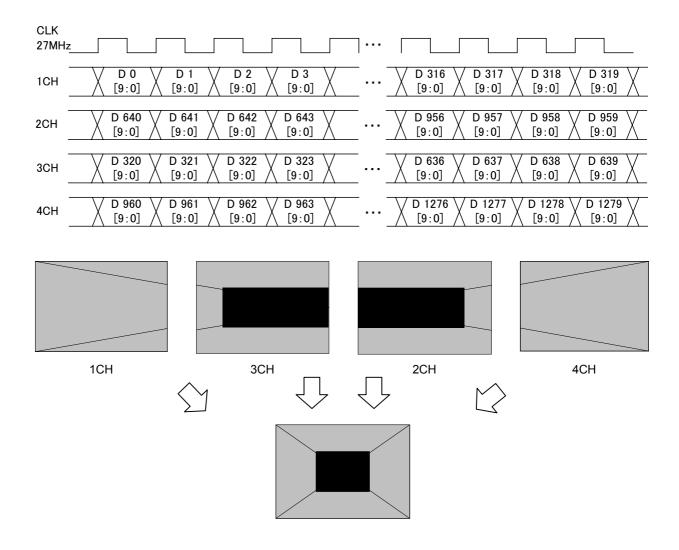
The output gray scale is 8 to 10 bits.



## Setting (5) [Quad (10 bits)], [4 split]

The images are split into four parts horizontally, and allocated from the left to channels 1, 3, 2 and 4 in this order.

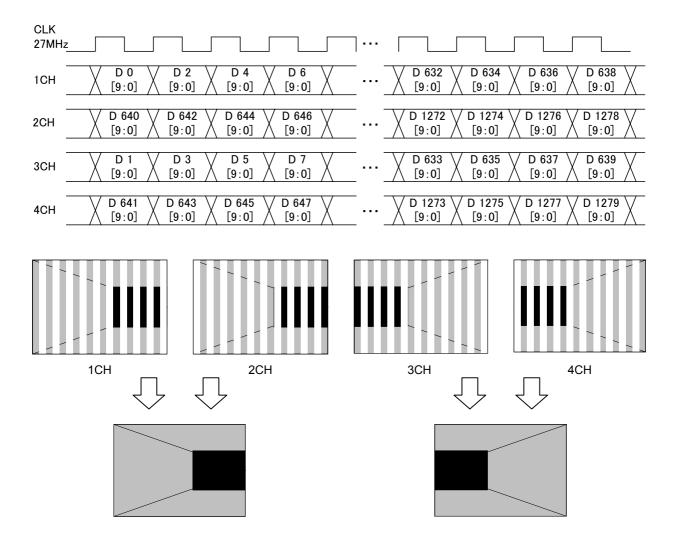
The output gray scale is 8 to 10 bits.



## Setting (6) [Quad (10 bits)], [2 split]

The images are output with channels 1 and 3 forming one set and channels 2 and 4 forming another set. The left half of the image is allocated and output to the channel 1 and 3 set, and the right half of the image is allocated to the channel 2 and 4 set.

The output gray scale is 8 to 10 bits.

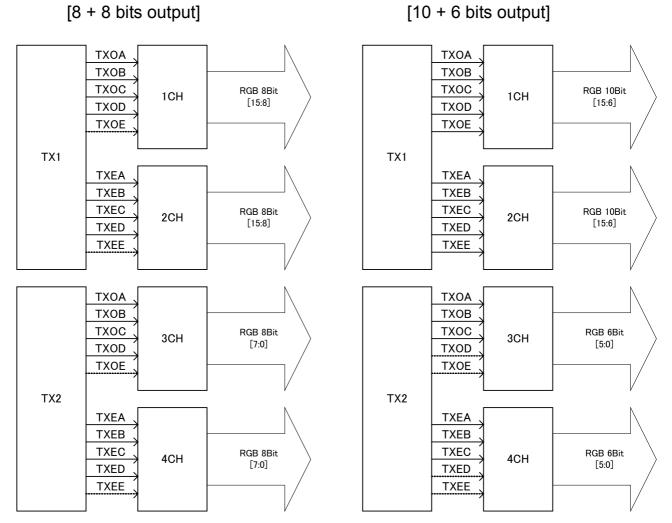


## <Specifications for outputs of 11 to 16 bits>

With outputs of 11 to 16 bits, 8 to 10 bits are treated as one output by the channel 1 and 2 set. The bits can be allocated in two ways as shown below. However, when Single (Auto) or Dual (Auto) has been set as the LVDS setting parameter mode, automatic switching is initiated to the channel 1 output for bits 8 to 10 and to the channel 2 output for bits 11 to 16.

When Single (10 bits), Dual (10 bits) or Quad (10 bits) has been set as the setting parameter mode, output is fixed from channel 1.

When Single (16 bits) or Dual (16 bits) has been set as the setting parameter mode, output is fixed from channel 2.



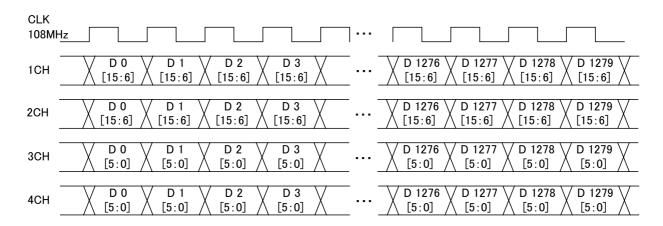
- \*1: The signal lines indicated by the dotted lines in the above figure are not used.
- \*2: The specification for 8 + 8 bits output is the default setting.

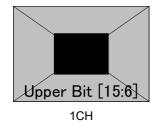
## Setting (1) [Single (16 bits)], [Normal], configuration [10 + 6 bits]

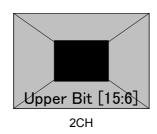
The 16-bit images are output with channels 1 and 3 forming one set and channels 2 and 4 forming another set.

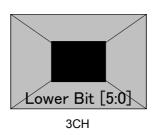
The 10 upper bits are output to channels 1 and 2, and the 6 lower bits are output to channels 3 and 4.

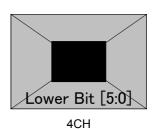
The example given here describes a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz, 16 bits apply for the gray scale, 10 bits are output to channel 1 and 6 bits are output to channel 2.









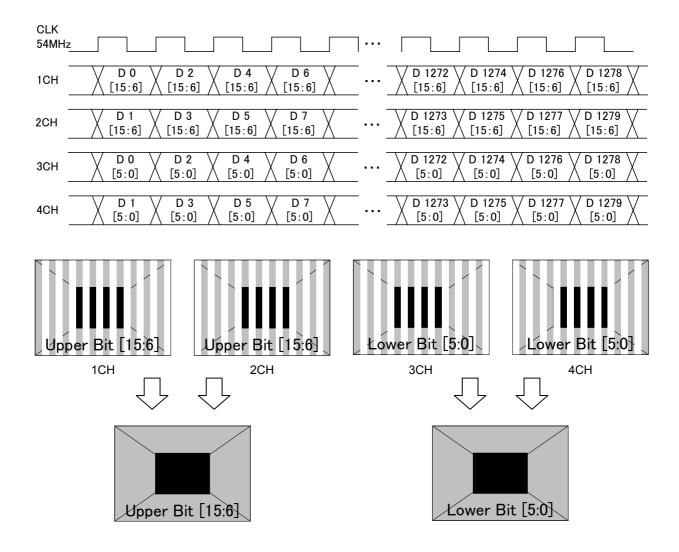


## Setting (2) [Dual (16 bits)], [Normal], configuration [10 + 6 bits]

With channels 1 and 3 forming one set and channels 2 and 4 forming another set, odd-numbered fields are output using one set and even-numbered fields are output using the other set.

The 10 upper bits are output to channels 1 and 2, and the remaining 6 lower bits are output to channels 3 and 4.

The example given here describes a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz, 16 bits apply for the gray scale, 10 bits are output to channel 1 and 6 bits are output to channel 2.

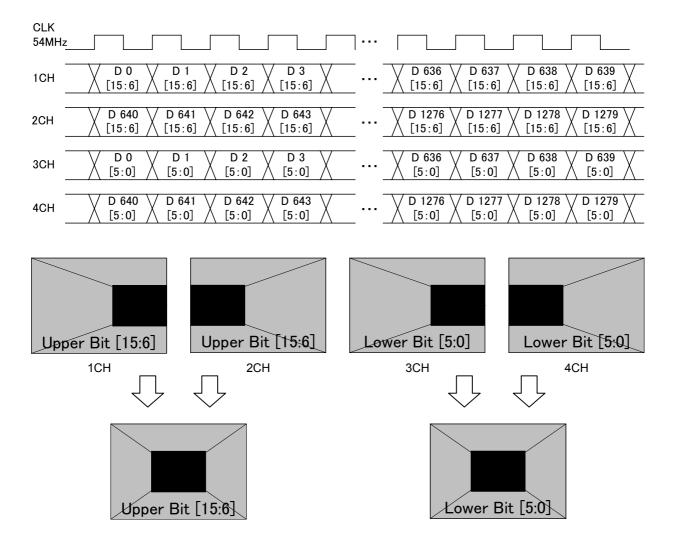


## Setting (3) [Dual (16 bits)], [2 split], configuration [10 + 6 bits]

With channels 1 and 3 forming one set and channels 2 and 4 forming another set, the left half of the image is output using one set and the right half of the image is output using the other set.

The 10 upper bits are output to channels 1 and 2, and the remaining 6 lower bits are output to channels 3 and 4.

The example given here describes a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz, 16 bits apply for the gray scale, 10 bits are output to channel 1 and 6 bits are output to channel 2.

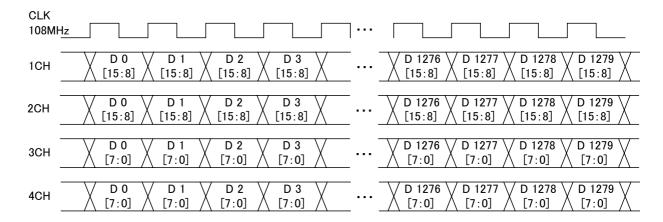


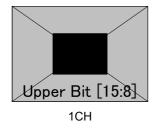
## Setting (4) [Single (16 bits)], [Normal], configuration [8 + 8 bits]

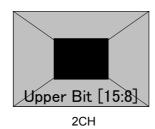
The 16-bit images are output with channels 1 and 3 forming one set and channels 2 and 4 forming another set.

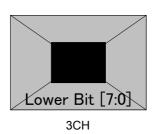
The 8 upper bits are output to channels 1 and 2, and the 8 lower bits are output to channels 3 and 4.

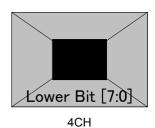
The example given here describes a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz, 16 bits apply for the gray scale, 8 bits are output to channel 1 and 8 bits are output to channel 2.









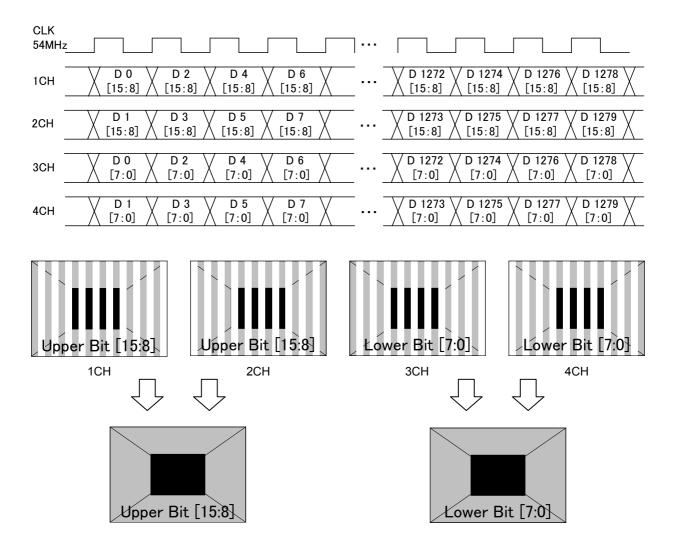


## Setting (5) [Dual (16 bits)], [Normal], configuration [8 + 8 bits]

With channels 1 and 3 forming one set and channels 2 and 4 forming another set, odd-numbered fields are output using one set and even-numbered fields are output using the other set.

The 8 upper bits are output to channels 1 and 2, and the 8 lower bits are output to channels 3 and 4.

The example given here describes a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz, 16 bits apply for the gray scale, 8 bits are output to channel 1 and 8 bits are output to channel 2.

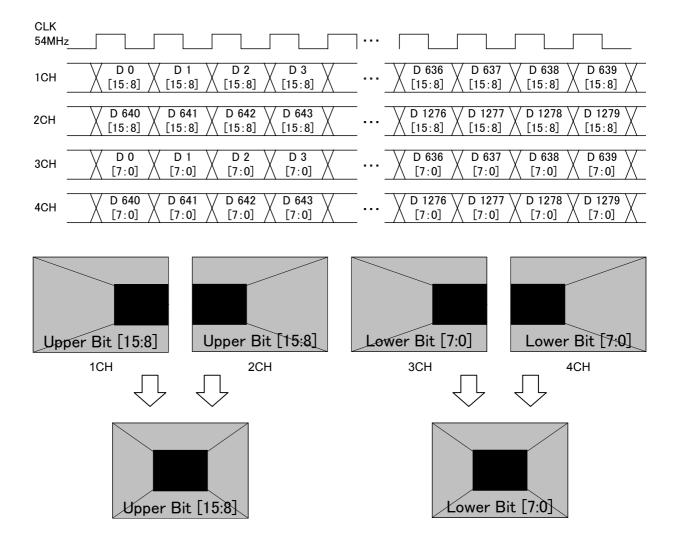


## Setting (6) [Dual (16 bits)], [2 split], configuration [8 + 8 bits]

With channels 1 and 3 forming one set and channels 2 and 4 forming another set, the left half of the image is output using one set and the right half of the image is output using the other set.

The 8 upper bits are output to channels 1 and 2, and the remaining 8 lower bits are output to channels 3 and 4.

The example given here describes a case where the resolution is  $1280 \times 1024$ , the dot clock frequency is 108 MHz, 16 bits apply for the gray scale, 8 bits are output to channel 1 and 8 bits are output to channel 2.



## 4.5.4 Bit arrays

Bit arrays has the following kinds; SAMPLE1 (DISM standard), SAMPLE2 (OpenLDI standard) and USER (1 to 3) which can be set by users.

For the setting procedure, refer to "4.5.2 LVDS setting procedure."

## Bit arrays for 8 to 10 bits for using one output connector

Operation	Data No.	8-bit mode			10-bit mode			
signal		SAMPLE1	SAMPLE2	USER	SAMPLE1	SAMPLE2	USER	
		(DISM)	(OpenLDI)		(DISM)	(OpenLDI)		
TA	TA0	R2	R0	R (X)	R4	R0	R (X)	
	TA1	R3	R1	R (X)	R5	R1	R (X)	
	TA2	R4	R2	R (X)	R6	R2	R (X)	
	TA3	R5	R3	R (X)	R7	R3	R (X)	
	TA4	R6	R4	R (X)	R8	R4	R (X)	
	TA5	R7	R5	R (X)	R9	R5	R (X)	
	TA6	G2	G0	G (X)	G4	G0	G (X)	
ТВ	TB0	G3	G1	G (X)	G5	G1	G (X)	
	TB1	G4	G2	G (X)	G6	G2	G (X)	
	TB2	G5	G3	G (X)	G7	G3	G (X)	
	TB3	G6	G4	G (X)	G8	G4	G (X)	
	TB4	G7	G5	G (X)	G9	G5	G (X)	
	TB5	B2	B0	B (X)	B4	B0	B (X)	
	TB6	B3	B1	B (X)	B5	B1	B (X)	
TC	TC0	B4	B2	B (X)	B6	B2	B (X)	
	TC1	B5	B3	B (X)	B7	B3	B (X)	
	TC2	B6	B4	B (X)	B8	B4	B (X)	
	TC3	B7	B5	B (X)	B9	B5	B (X)	
	TC4	HS	HS	HS	HS	HS	HS	
	TC5	VS	VS	VS	VS	VS	VS	
	TC6	DE	DE	DE	DE	DE	DE	
TD	TD0	R0	R6	R (X)	R2	R6	R (X)	
	TD1	R1	R7	R (X)	R3	R7	R (X)	
	TD2	G0	G6	G (X)	G2	G6	G (X)	
	TD3	G1	G7	G (X)	G3	G7	G (X)	
	TD4	B0	B6	B (X)	B2	B6	B (X)	
	TD5	B1	B7	B (X)	B3	B7	B (X)	
	TD6	L	L	L	L	L	L	
TE	TE0	L	L	L	R0	R8	R (X)	
	TE1	L	L	L	R1	R9	R (X)	
	TE2	L	L	L	G0	G8	G (X)	
	TE3	L	L	L	G1	G9	G (X)	
	TE4	L	L	L	B0	B8	B (X)	
	TE5	L	L	L	B1	B9	B (X)	
	TE6	L	L	L	L	L	L	

<sup>\*</sup> In the 9-bit mode, the lowest bit of 10-bit mode is discarded and placed to upward.

## Bit arrays for 8 to 16 bits for using two output connectors

Config setting: MultiBitMode/8+8 Bit

Operation	Data No.	8- to 16-bit mode								
signal		SAMPLE1		SAMPLE2		USER				
		(DISM)		(OpenLDI)						
		CH1,CH2	CH3,CH4	CH1,CH2	CH3,CH4	CH1,CH2	CH3,CH4			
TA	TA0	R10	R2	R8	R0	R (X)	R (X)			
	TA1	R11	R3	R9	R1	R (X)	R (X)			
	TA2	R12	R4	R10	R2	R (X)	R (X)			
	TA3	R13	R5	R11	R3	R (X)	R (X)			
	TA4	R14	R6	R12	R4	R (X)	R (X)			
	TA5	R15	R7	R13	R5	R (X)	R (X)			
	TA6	G10	G2	G8	G0	G (X)	G (X)			
ТВ	TB0	G11	G3	G9	G1	G (X)	G (X)			
	TB1	G12	G4	G10	G2	G (X)	G (X)			
	TB2	G13	G5	G11	G3	G (X)	G (X)			
	TB3	G14	G6	G12	G4	G (X)	G (X)			
	TB4	G15	G7	G13	G5	G (X)	G (X)			
	TB5	B10	B2	B8	B0	B (X)	B (X)			
	TB6	B11	B3	B9	B1	B (X)	B (X)			
TC	TC0	B12	B4	B10	B2	B (X)	B (X)			
	TC1	B13	B5	B11	B3	B (X)	B (X)			
	TC2	B14	B6	B12	B4	B (X)	B (X)			
	TC3	B15	B7	B13	B5	B (X)	B (X)			
	TC4	HS	HS	HS	HS	HS	HS			
	TC5	VS	VS	VS	VS	VS	VS			
	TC6	DE	DE	DE	DE	DE	DE			
TD	TD0	R8	R0	R14	R6	R (X)	R (X)			
	TD1	R9	R1	R15	R7	R (X)	R (X)			
	TD2	G8	G0	G14	G6	G (X)	G (X)			
	TD3	G9	G1	G15	G7	G (X)	G (X)			
	TD4	B8	B0	B14	B6	B (X)	B (X)			
	TD5	B9	B1	B15	B7	B (X)	B (X)			
	TD6	L	L	L	L	L	L			
TE	TE0	L	L	L	L	L	L			
	TE1	L	L	L	L	L	L			
	TE2	L	L	L	L	L	L			
	TE3	L	L	L	L	L	L			
	TE4	L	L	L	L	L	L			
	TE5	L	L	L	L	L	L			
	TE6	L	L	L	L	L	L			

<sup>\*</sup> If bit width smaller than 16-bit, the lower bits are discarded and the placed upward.

## Bit arrays for 8 to 16 bits for using two output connector

Config setting: MultiBitMode/10+6 Bit

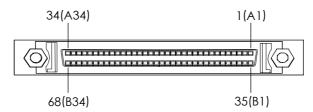
Operation	Data No.	8- to 16-bit mode									
signal		SAMPLE1		SAMPLE1		USER					
		(DISM)		(OpenLDI)							
		CH1,CH2	CH3,CH4	CH1,CH2	CH3,CH4	CH1,CH2	CH3,CH4				
TA	TA0	R10	R0	R6	R0	R (X)	R (X)				
	TA1	R11	R1	R7	R1	R (X)	R (X)				
	TA2	R12	R2	R8	R2	R (X)	R (X)				
	TA3	R13	R3	R9	R3	R (X)	R (X)				
	TA4	R14	R4	R10	R4	R (X)	R (X)				
	TA5	R15	R5	R11	R5	R (X)	R (X)				
	TA6	G10	G0	G6	G0	G (X)	G (X)				
TB	TB0	G11	G1	G7	G1	G (X)	G (X)				
	TB1	G12	G2	G8	G2	G (X)	G (X)				
	TB2	G13	G3	G9	G3	G (X)	G (X)				
	TB3	G14	G4	G10	G4	G (X)	G (X)				
	TB4	G15	G5	G11	G5	G (X)	G (X)				
	TB5	B10	B0	B6	В0	B (X)	B (X)				
	TB6	B11	B1	B7	B1	B (X)	B (X)				
TC	TC0	B12	B2	B8	B2	B (X)	B (X)				
	TC1	B13	B3	B9	B3	B (X)	B (X)				
	TC2	B14	B4	B10	B4	B (X)	B (X)				
	TC3	B15	B5	B11	B5	B (X)	B (X)				
	TC4	HS	HS	HS	HS	HS	HS				
	TC5	VS	VS	VS	VS	VS	VS				
	TC6	DE	DE	DE	DE	DE	DE				
TD	TD0	R8	L	R12	L	R (X)	L				
	TD1	R9	L	R13	L	R (X)	L				
	TD2	G8	L	G12	L	G (X)	L				
	TD3	G9	L	G13	L	G (X)	L				
	TD4	B8	L	B12	L	B (X)	L				
	TD5	B9	L	B13	L	B (X)	L				
	TD6	L	L	L	L	L	L				
TE	TE0	R6	L	R14	L	R (X)	L				
	TE1	R7	L	R15	L	R (X)	L				
	TE2	G6	L	G14	L	G (X)	L				
	TE3	G7	L	G15	L	G (X)	L				
	TE4	B6	L	B14	L	B (X)	L				
	TE5	B7	L	B15	L	B (X)	L				
	TE6	L	L	L	L	L	L				

<sup>\*</sup> If bit width smaller than 16-bit, the lower bits are discarded and the placed upward.

## 4.6 Parallel

## 4.6.1 Connectors and pin assignments

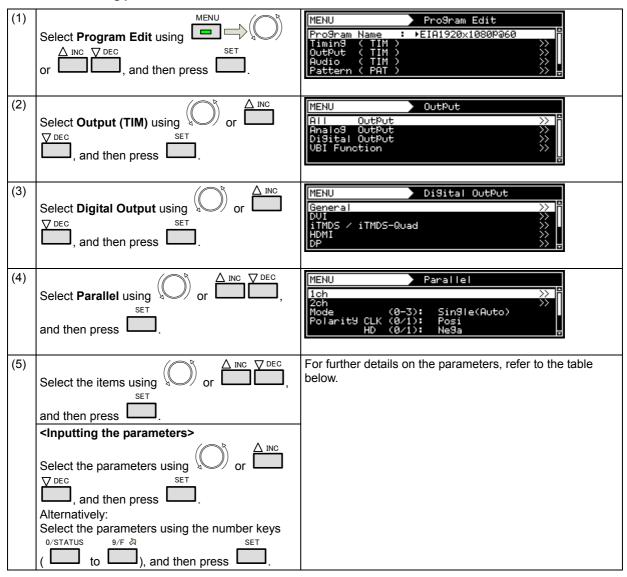
• Connector: 68-pin MINI D (half-pitch pin type)



CH1							CH2								
No.	Signal														
1	(GND)	18	VCC	35	RA0	52	VCC	1	(GND)	18	VCC	35	RB0	52	VCC
2	(GND)	19	GND	36	RA1	53	GND	2	(GND)	19	GND	36	RB1	53	GND
3	(GND)	20	GND	37	RA2	54	GND	3	(GND)	20	GND	37	RB2	54	GND
4	(GND)	21	(GND)	38	RA3	55	HS0	4	(GND)	21	(GND)	38	RB3	55	SW2
5	(GND)	22	(GND)	39	RA4	56	VS0	5	(GND)	22	(GND)	39	RB4	56	SW3
6	(GND)	23	(GND)	40	RA5	57	DISP0	6	(GND)	23	(GND)	40	RB5	57	DISP1
7	(GND)	24	(GND)	41	RA6	58	SW0	7	(GND)	24	(GND)	41	RB6	58	SW1
8	(GND)	25	(GND)	42	RA7	59	BA0	8	(GND)	25	(GND)	42	RB7	59	BB0
9	(GND)	26	(GND)	43	GA0	60	BA1	9	(GND)	26	(GND)	43	GB0	60	BB1
10	(GND)	27	(GND)	44	GA1	61	BA2	10	(GND)	27	(GND)	44	GB1	61	BB2
11	(GND)	28	(GND)	45	GA2	62	BA3	11	(GND)	28	(GND)	45	GB2	62	BB3
12	(GND)	29	(GND)	46	GA3	63	BA4	12	(GND)	29	(GND)	46	GB3	63	BB4
13	(GND)	30	(GND)	47	GA4	64	BA5	13	(GND)	30	(GND)	47	GB4	64	BB5
14	(GND)	31	(GND)	48	GA5	65	BA6	14	(GND)	31	(GND)	48	GB5	65	BB6
15	(GND)	32	(GND)	49	GA6	66	BA7	15	(GND)	32	(GND)	49	GB6	66	BB7
16	(GND)	33	GND	50	GA7	67	GND	16	(GND)	33	GND	50	GB7	67	GND
17	VCC	34	(GND)	51	VCC	68	CLK	17	VCC	34	(GND)	51	VCC	68	CLK

## 4.6.2 Parallel data setting procedure

#### Parallel data setting procedure



<sup>\*</sup> The output voltage level can be changed by the switch on the video unit (located on the rear panel of the generator).

For further details on setting procedure, refer to "11.1.7 PARALLEL unit."

## Parallel data setting parameters

(1)	1ch	MENII								
(')		MENU OutPut All	1ch (0/1): 1001							
		DATA CLK SYnc Power	(0/1): ON (0/1): ON (0/1): ON							
			0.4.448							
		Output All								
		0 <b>OFF</b>								
		1 <b>ON</b>								
		DATA	1 <del>-</del>							
		0 Hiz	This sets the parallel data to the high-impedance (HiZ) state.							
		1 <b>ON</b>	This outputs the parallel data.							
		CLK								
		0 <b>Hiz</b>	This sets the CLK signal to the high-impedance (HiZ) state.							
		1 <b>ON</b>	This outputs the parallel clock signal.							
		Sync								
		0 Hiz	This sets the parallel clock signal to the high-impedance (HiZ) state.							
		1 <b>ON</b>	This outputs the parallel clock signal.							
		Powe	· · · · · · · · · · · · · · · · · · ·							
		0 Hiz	This sets the parallel power supply to the high-impedance (HiZ) state.							
		1 <b>ON</b>	This outputs the parallel power.							
			For further details on the settings, refer to "1.5.6 Parallel unit"							
		SW	·							
		0 <b>CS</b>	CS output from SW							
		1 <b>VD</b>	VD output from SW							
		2 <b>HD</b>	HD output from SW							
		3 <b>Low</b>	Fix SW to Low							
		4 High	Fix SW to High							
(2)	2ch		rom <b>Output</b> to <b>Power</b> are the same as for channel 1.							
		SW1								
		0 <b>CS</b>	CS output from SW1							
		1 <b>VD</b>	VD output from SW1							
		2 <b>HD</b>	HD output from SW1							
		3 Low	Fix SW1 to Low							
		4 High	Fix SW1 to High							
		SW2								
		0 <b>HS</b>	HS output from SW2							
		1 <b>VD</b>	VD output from SW2							
		2 <b>HD</b>	HD output from SW2							
		3 <b>Low</b>	Fix SW2 to Low							
		4 High	Fix SW2 to High							
		SW3	I.a							
		0 <b>VS</b>	VS output from SW3							
		1 VD	VD output from SW3							
		2 <b>HD</b>	HD output from SW3							
		3 <b>Low</b>	Fix SW3 to Low							
		4 High	Fix SW3 to High							

(3)	Mode (0/1)	par dra au' the wit "S to "D	rallel connector. A setting awing can be selected. It tomatically. The portion be bit length which has been had been been been been been been been bee	en the dot clock frequency ranges from 0.2 MHz to be output.
		"4.	1.5 Setting the bit length	(gray scale) for pattern drawing"
		0	Single (8 bits)	The data is output by Single Link from output channel 1. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded. The same data as for output channel 1 is output for channel 2.
		1	Dual (8 bits)	The data is output by Dual Link from output channels 1 and 2. The portion by which the bit length for pattern drawing exceeds 8 bits is discarded.
		2	Single (16 bits)	The data is output by Single Link from output channels 1 and 2. The portion by which the bit length for pattern drawing is deficient from 16 bits is discarded.
		3	Single (Auto)	The data is output by Single Link. <b>Single (10 bits) or Single (16 bits)</b> is automatically selected depending on the bit length for pattern drawing.
(4)	Polarity CLK (0/1)	Th	is selects whether to reve	erse the polarity of the parallel clock signal.
		0	Nega	This outputs the clock signal with a reversed polarity.
		1	Posi	This outputs the clock signal with a non-reversed polarity.
(5)	HD (0/1)	Fo		erse the polarity of the parallel HD. tting procedure, refer to "3.2 Vertical timing data
		0	Nega	This outputs the clock signal with a reversed polarity.
		1	Posi	This outputs the clock signal with a non-reversed polarity.
(6)	VD (0/1)	Fo		erse the polarity of the parallel VD. tting procedure, refer to "3.2 Vertical timing data
		0	Nega	This outputs the clock signal with a reversed polarity.
		1	Posi	This outputs the clock signal with a non-reversed polarity.
(7)	CS (0/1)	Th	is selects whether to reve	erse the polarity of the parallel CS.
		0	Nega	This outputs the clock signal with a reversed polarity.
		1	Posi	This outputs the clock signal with a non-reversed polarity.
(8)	DISP (0/1)	Th	is selects whether to reve	erse the polarity of the parallel DISP.
		0	Nega	This outputs the clock signal with a reversed polarity.
		1	Posi	This outputs the clock signal with a non-reversed polarity.

# 4.7 Analog component signals

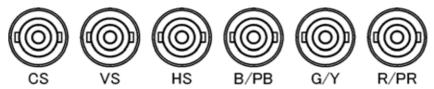
### 4.7.1 Connectors and output signals

■ TV encoder board component outputs (BNC)



Connector	Signal
PB	Pb
Υ	Υ
PR	Pr

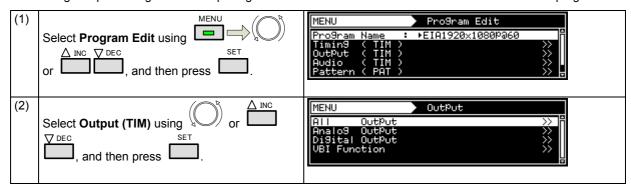
■ PC unit board component outputs (BNC)

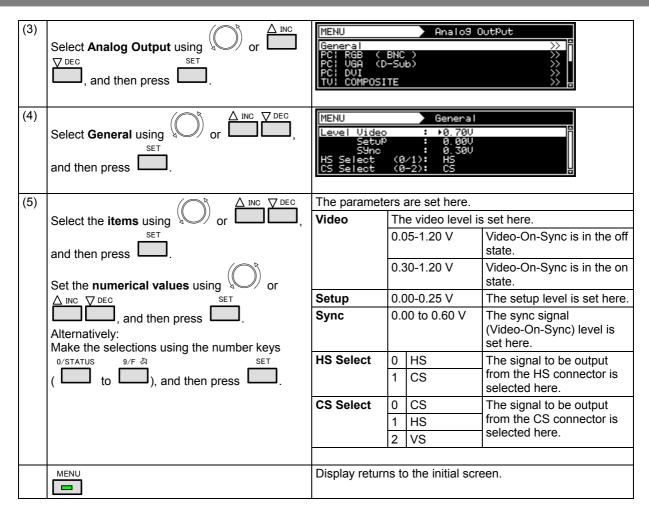


Connector	Signal
CS	CS
VS	VS
HS	HS
B/PB	B or Pb
G/Y	G or Y
R/PR	R or Pr

# 4.7.2 Setting the analog output connectors

The analog component signals and output signals from the HS and CS connectors can be set for each program.







# 4.7.3 Setting the analog video level

For details on changing the analog video level, refer to "4.1.7 Setting the analog level."

#### 4.7.4 Sync signal settings

For further details on the sync signal ON/OFF settings and polarity settings, refer to "4.1.2 Setting the sync signals to ON or OFF and setting the sync signal polarities."

# 4.8 Composite connector and Y/C connector (S connector)

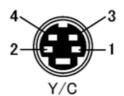
# 4.8.1 Connectors and output signals

#### ■ Composite connector



Connector	Signal
COMPOSITE	Composite video

#### ■ Y/C connector (S connector)



Pin no.	Signal
1	GND
2	GND
3	Υ
4	С

#### ■ Concerning the output signals

The following video signals can be output from the composite connector and Y/C connector of the TV encoder unit.

- NTSC-M, NTSC-J, NTSC-443
- PAL-60, PAL, PAL-M, PAL-N, PAL-Nc
- SECAM



 If the timing of the composite and Y/C signals (such as the period and sync width data) has been changed from that in the internal program, it may no longer be possible to draw the patterns on the monitor correctly.

#### ■ Difference between VM-1812 and VM-1812-B

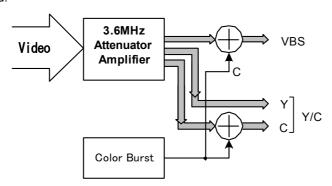
VM-1812-B has the following restriction.

- PAL-N, PAL-60 and SECAM are not supported.
- Option Pattern No. 77 (SMPTE Color SVBS) is not supported.

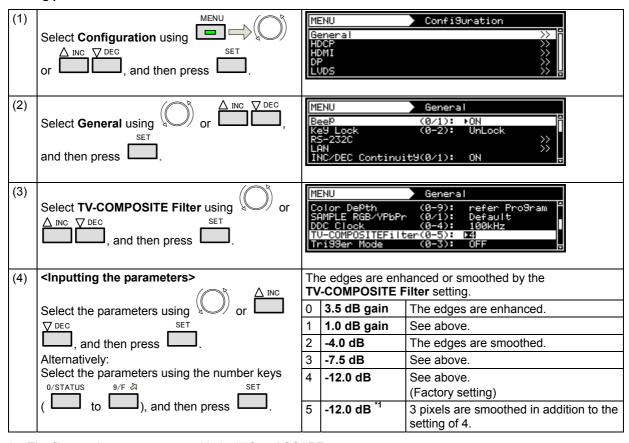
# 4.8.2 Composite signal filter settings

The amount of attenuation (or gain) in the 3.6 MHz frequency of the composite and Y/C signals can be set.

This setting is processed before the chrominance is added to the video signals so that the color burst is not affected.



#### **Setting procedure**

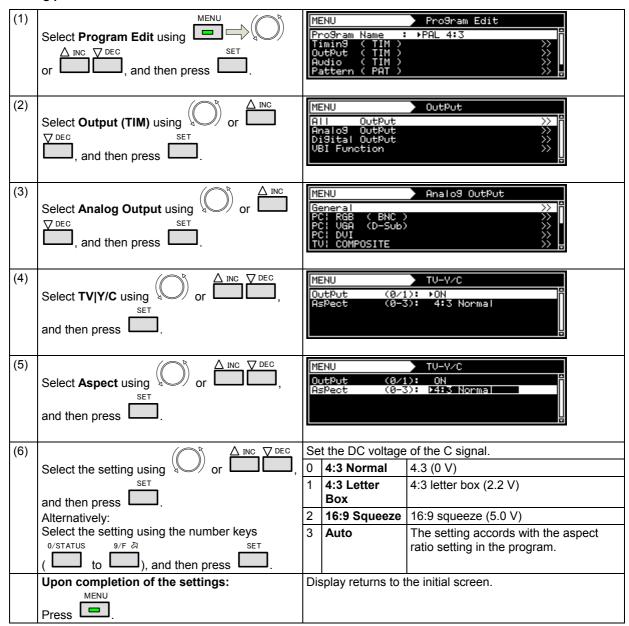


<sup>\*</sup> The filter settings are common with the Y/C and SCART outputs.

# 4.8.3 Setting the ID signals (Y/C)

With the Y/C signals, identification of the aspect ratio is enabled by superimposing the ID DC signal onto the C signal.

#### Setting procedure



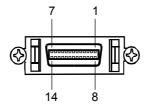
### 4.8.4 Functions available with TV standard signals

Microvision, closed caption, V-Chip, Teletext, WSS and CGMS-A/ID-1 can be multiplexed with the composite signals and Y signal.

For details on the setting procedure, refer to "5. FUNCTIONS AVAILABLE WITH TV STANDARD SIGNALS."

# 4.9 D5 (D connector)

### 4.9.1 Connectors and pin assignments

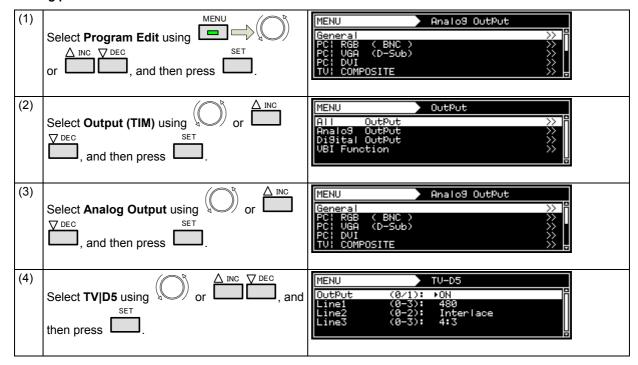


Pin no.	Signal	Pin no.	Signal
1	Υ	8	Line 1
2	GND (Y)	9	Line 2
3	Pb	10	NC
4	GND (Pb)	11	Line 3
5	Pr	12	NC
6	GND (Pr)	13	NC
7	NC	14	NC

# 4.9.2 ID signals

ID signals indicating the resolution, scanning system and aspect ratio can be output from the D connector. The ID signals are DC signals, and they identify the formats using three lines. These lines are referred to as line 1, line 2 and line 3.

### **Setting procedure**



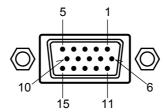
(5)	<selecting items="" the=""> Select the items using or or □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □</selecting>	For further details on the setting items and parameters, refer to <table d5="" items="" of="" setting=""> below.</table>
	and then press .	
	<setting parameters="" the=""></setting>	
	Select the parameters using $\bigcirc$ or $\bigcirc$ or	
	, and then press .	
	Alternatively:	
	Select the parameters using the number keys	
	( $\stackrel{\text{0/STATUS}}{\square}$ to $\stackrel{\text{9/F}}{\square}$ ), and then press $\stackrel{\text{SET}}{\square}$ .	
	Upon completion of the settings:	Display returns to the initial screen.
	Press Press	

### <Table of D5 setting items>

(1)	Line1 (0-2)	Line1: This sets the resolution.		
		0 480	720 × 480	
		1 720	1280 × 720	
		2 1080	1920 × 1080	
		3 Auto	The setting accords with the program setting.	
(2) Line2 (0/1)		Line2: This sets the scanning system.		
		0 Interlace	Interlaced	
		1 Progressive	Progressive	
		2 Auto	The setting accords with the program setting.	
(3) Line3 (0-2)		Line3: This sets th	e aspect ratio.	
		0 4:3	4:3	
		1 4:3 Letter Box	4:3 letter box	
		2 16:9	16:9	
		3 Auto	The setting accords with the program setting.	

# 4.10 VGA (D-Sub)

# 4.10.1 Connectors and pin assignments



Pin no.	Signal	Pin no.	Signal
1	R	9	+5 V (DDC power supply *1)
2	G	10	GND
3	В	11	GND
4	NC	12	DDC DATA
5	NC	13	HS
6	GND (R)	14	VS
7	GND (G)	15	DDC CLK
8	GND (B)		

<sup>\*1:</sup> Restrictions apply to the supply current of the DDC power supply. Refer to "12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply."

# 4.10.2 Video level settings

The setting procedure is the same as for the analog component signals.

Refer to "4.1.7 Setting the analog level" and "4.7.2 Setting the analog output connectors."

# 4.10.3 Sync signal settings

The setting procedure is the same as for the analog component signals. Refer to "4.1.2 Setting the sync signals to ON or OFF and setting the sync signal polarities"

#### 4.10.4 EDID

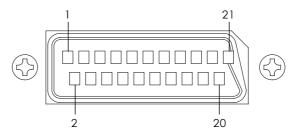
The operation procedure is the same as for HDMI. Refer to "6.13.3 EDID."

#### 4.10.5 DDC/CI

The operation procedure is the same as for DVI. Refer to "6.13.4 DDC/CI."

# 4.11 **SCART**

# 4.11.1 Connectors and pin assignments



Pin no.	Signal	Pin no.	Signal
1	Audio right channel output	11	Component G output
2	N.C.	12	N.C.
3	Audio left channel output	13	GND
4	GND	14	GND
5	GND	15	Component R output/C output
6	GND	16	RGB status
7	Component B output	17	GND
8	Video Status	18	GND
9	GND	19	Composite/Y output/CS
10	N.C.	20	N.C
		21	GND

# 4.11.2 SCART setting procedure

# <SCART setting procedure>

(4)	MENU (C)	
(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080Pa60  Timin9 (TIM) OutPut (TIM) Audio (TIM) Pattern (PAT)
(2)	Select <b>Output (TIM)</b> using or or or SET, and then press	MENU OutPut  AII OutPut Analog OutPut Digital OutPut VBI Function  OutPut  Out
(3)	Select Analog Output using or	MENU Analog OutPut  General
(4)	Select <b>SCART</b> using or or or or or or and then press.	MENU TU-SCART  OutPut 1ch (0/1): ▶ON 2ch (8/1): ON OutPut Select(0-2): COMPOSITE Video Status (0-3): Auto  RGB Status (0-3): Auto
(5)	Select the items using or	For further details on the parameters, refer to <b>SCART</b> setting parameters>.
	( by to by), and then press being.	

### <SCART setting parameters>

(1)	Output 1ch (0/1)	Th	This sets On or Off for each channel.		
,	Output 2ch (0/1)	The same settings as the ones described in "4.1.1 Setting the output			
		inte	erfaces to ON or OFF" ca	an also be established.	
		0 Off No output.		No output.	
		1	On	Output.	
(2)	Output Select (0-2)	Th	is sets the format of the v	video signals which are output from the SCART	
		COI	nnector.		
		0	COMPOSITE	Composite signals are output.	
		1	Y/C	Y/C signals are output.	
		2	RGB	RGB signals are output.	
(3)	Video Status (0-3)	Th	is sets the video status si	ignal which is output from the SCART connector.	
		0	Auto	The setting accords with the program setting.	
		1	4:3	4:3 (identified voltage: 12 V (9.5 to 12.0 V))	
		2	16:9	16:9 (identified voltage: 5 V (4.5 to 7.0 V))	
		3	No Signal	No output. (identified voltage: 0 V (0.0 to 2.0 V))	
(4)	RGB Status (0-3)	This sets the RGB status signal which is output from the SCART connector.			
		0	Auto	The signal is set automatically by <b>Output Select</b> .	
		1	VBS	Composite or Y/C signals (identified voltage: 0 V)	
		2	RGB	RGB (identified voltage: 5 V)	
		3	Fast Blanking	The fast blanking signal is output.	
(5)	Fast Blanking Area	Th	is sets the output range of	of the fast blanking signal.	
		Н	The horizontal output ra	nge is set as a percentage of H-Disp.	
			Setting range: 0% to 10	g range: <b>0% to 100</b> %	
		V The vertical output ra		e is set as a percentage of V-Disp.	
			Setting range: 0% to 10	0%	
(6)	Audio Out1ch (0/1)	Th	This sets on or off for each channel.		
		0	OFF	No output.	
		1	ON	Output.	
(7)	Audio Out2ch (0/1)	This setting is the same as for the Audio Out1ch setting.			

# 4.11.3 Functions available with TV standard signals

Microvision, closed caption, V-Chip, Teletext, WSS and CGMS-A/ID-1 can be multiplexed with the composite signals and Y signal.

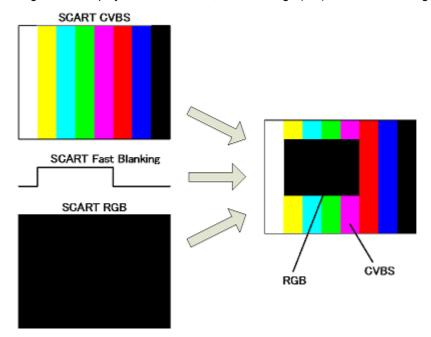
For details on the setting procedure, refer to "5. FUNCTIONS AVAILABLE WITH TV STANDARD SIGNALS."

# 4.11.4 Filter settings

For further details on the setting procedure, refer to "4.8.2 Composite signal filter settings."

# 4.11.5 Concerning the fast blanking signal

The fast blanking signal is a control signal for selecting the composite output and RGB output video signals which are output from the 16-pin SCART connector, and displaying them. By using it, displays can be shown as with on-screen displays. When the fast blanking signal level is low (0 V), the CVBS video signals are displayed on the monitor; when it is high (5 V), the RGB video signals are displayed.





With the VG-870B/871B/873/874 can not output different images from the composite and RGB output pins of the SCART connector. If COMPOSITE or Y/C has been set for "Output Select" in the SCART setting, a black-filled image is output to the RGB output, if RGB has been set, the same image as the composite pin is output.

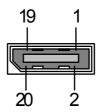
### 4.11.6 Audio settings

For details on the setting procedure, refer to "4.15 Analog audio settings."

# 4.12 DisplayPort

# 4.12.1 Connectors and pin assignments

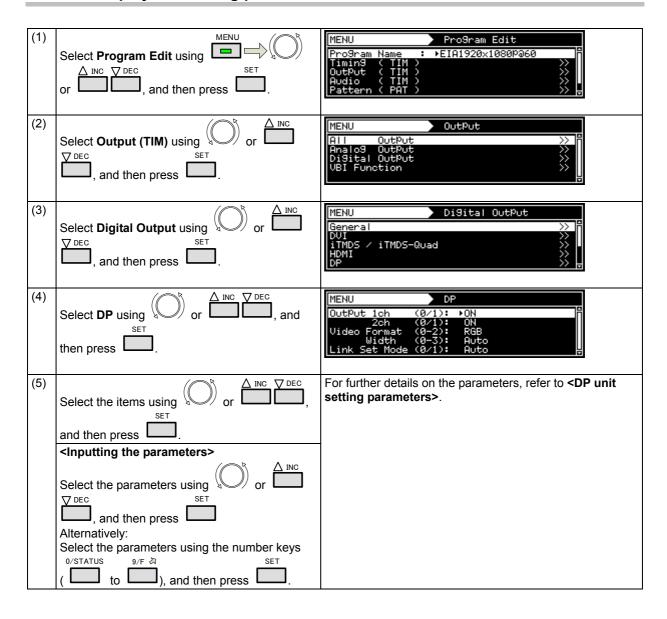
# ■ DisplayPort



Pin No.	Signal
1	MainLink Lane0 (p)
2	GND
3	MainLink Lane0 (n)
4	MainLink Lane1 (p)
5	GND
6	MainLink Lane1 (n)
7	MainLink Lane2 (p)
8	GND
9	MainLink Lane2 (n)
10	MainLink Lane3 (p)
11	GND
12	MainLink Lane3 (n)
13	GND
14	GND
15	AUX CH (p)
16	GND
17	AUX CH (n)
18	Hot Plug Detect
19	PWR_Return (not used)
20	DP_PWR (+3.3 V)

<sup>\*</sup> The DDC power supply current is limited. Refer to "12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply."

# 4.12.2 DisplayPort setting procedure



# <DP unit setting parameters>

(1)	Output 1ch (0/1)	Thi	is sets Or	or Off for each	channel	
( ' )	Output 2ch (0/1)				nes described in "4.1.1 Setting the output	
	( ,				an also be established.	
		0	OFF	No output.		
		1	ON	Output.		
(2)	Mode (0-2)	Thi	This sets the drawing mode.			
		For	r further c	letails, refer to "	b) Concerning the drawing mode" in this section.	
		0	Single		Output in Single mode.	
		1	Dual		Output in Dual mode.	
		2	Split		Output in Split mode.	
(3)	Video Format (0-2)	Thi			the video output from DisplayPort.	
		*			format has been selected, it is not possible to	
					which accords with the bit length (gray scale)	
					4-step gray scale with an 8-bit output) Use this e Main Stream Attribute parameters rather	
					s the image quality.	
		0	RGB		The video is output using RGB signals.	
		1	YCbCr4	:4:4	The video is output using YCbCr4:4:4 signals.	
		2	YCbCr4	:2:2	The video is output using YCbCr4:2:2 signals.	
(4)	Width (0-3)	Thi			e output video. A setting independent of the bit	
,	, ,	len	gth for pa	attern drawing c	an be selected, or the same bit length can be	
		selected automatically.				
					e bit length for pattern drawing exceeds the bit	
		Do:	length set here is discarded. Any deficient portion is filled with zeros.			
		*	Refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing"  * The bit length setting differs according to the video format. For further			
			details, refer to "c) Concerning the bit length setting" in this section.			
		*		,	e bit length to be output, refer to "c) Concerning	
			the bit length setting" in this section.			
		0			6, 8, 10 or 12 bits is selected here automatically	
					depending on the bit length for pattern drawing.	
		-	6bit		6-bit output	
		$\vdash$	8bit		8-bit output	
		3	10bit		10-bit output	
		4	12bit		12-bit output	
					Note: this item can be set in case of Video Format = 2 (YCbCr4:2:2). Refer to "c) About bit	
					length" for details.	
(5)	Colorimetry (0/1)	Thi	is selects	the Main Stream	m Attribute "YCbCr Colorimetry".	
,		*	* This sets only the Main Stream Attribute. To change the color difference			
				ent setting, refer to "4.1.6 Selecting RGB or YPbPr and setting the		
				erence coefficie	<u> </u>	
			ITU601		This sets ITU-R BT601.	
			ITU709		This sets ITU-R BT709.	
(6)	Link Set Mode (0/1)	<u> </u>		the Link Rate a	and Number of Lane setting method.	
		0	Auto		Output accords with the DPCD of the sink component.	
		1	Manual		Output accords with the settings of items (7) and (8).	
(7)	Link Rate (0/1)	*	This par	ameter can be	set when Link Set Mode is "Manual".	
		Thi	This sets the link rate.			
		0	HBR(2.7	'Gbps)	Output at the link rate "HBR (2.7 Gbps)"	
		1	RBR(1.6	2Gbps)	Output at the link rate "RBR (1.62 Gbps)"	
	•					

(8)	Number of Lane (0-2)	* This parameter can be set when the Link Set Mode is "Manual".			
		This sets the number of output lanes.			
		0	1lane		Output on 1 lane
		1	2lanes		Output on 2 lanes
		2	4lanes		Output on 4 lanes
(9)	Nvid	1-1667216 This sets the Nvid value.			
					alue is automatically calculated from the dot the Nvid value.
(10)	HPD Mode (0/1)	Th	is sets th	e Hotplug detec	tion status.
		0	0 <b>OFF</b>		Hotplug is ignored.
		1	ON		Hotplug is judged according to the status of the connected component.

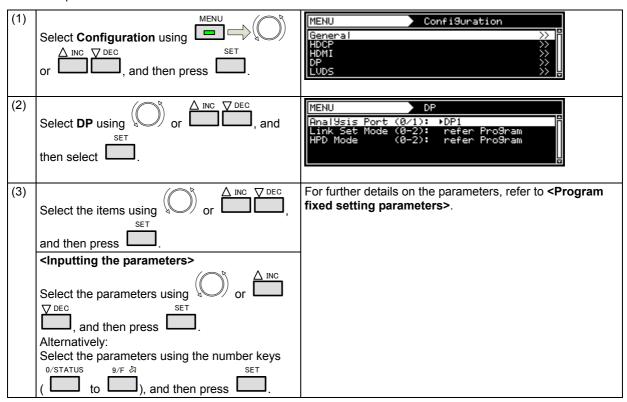
#### a) Concerning the all program fixed setting

The program settings for the following setting items can be ignored and output can be performed with the entire program fixed according to the device setting. For example, this is used to set an entire sample program to HPD mode OFF, or to change the output video bit length but leave other settings unchanged, etc.

#### Parameters which can be set to program fixed

Item	Remarks
Width	Refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."
Link Set Mode	See below.
HPD Mode	

Follow the procedure below to set Link Set Mode and HPD Mode.



# <Program fixed setting parameters>

(1)	Link Set Mode (0-2)	This selects the Link Set Mode setting method.  For further details, refer to " <dp parameters="" setting="" unit=""> 'Link Set Mode'" in this section.</dp>		
		0	refer Program	The setting accords with the program setting.
				Output always accords with the DPCD of the sink component.
		2	Manual	Output always accords with the Link Rate and Number of Lane set by the program.
(2)	HPD Mode (0-2)	This selects the Hotplug detection method.		
		For further details, refer to " <dp parameters="" setting="" unit=""> 'HPD Mode'" in th section.</dp>		
		0	refer Program	The setting accords with the program setting.
		1	OFF	Hotplug is always ignored.
		2	ON	Hotplug is always judged according to the status of the connected component.

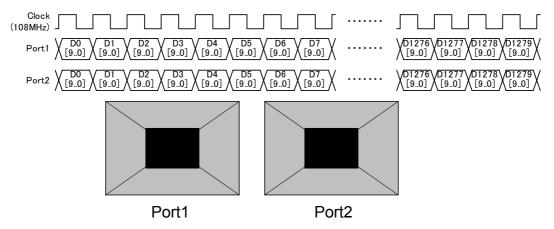
### b) Concerning the drawing mode

The following video output modes are possible using the two DisplayPort output ports.

The examples below describe the case for  $1280 \times 1024$  resolution, a 108 MHz dot clock, and a 10-bit output gray scale.

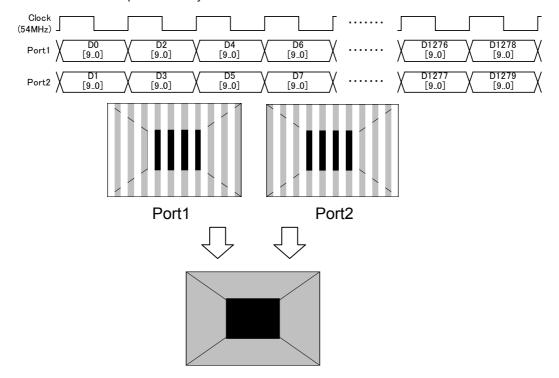
#### (1) Single mode

This is the normal output mode. The same video is output from both Port1 and Port2.



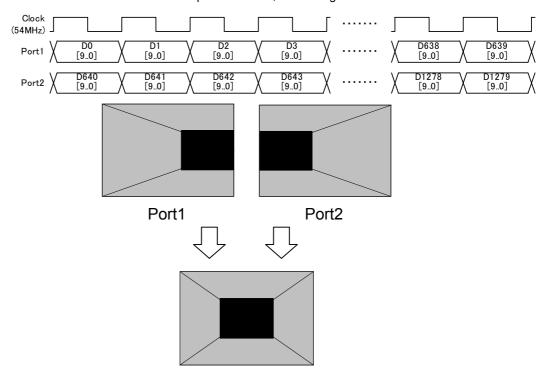
#### (2) Dual mode

The video data is output alternately from Port1 and Port2.



### (3) Split mode

The left half of the video data is output from Port1, and the right half of the video data from Port2.



### c) Concerning the bit length setting

The valid bit length settings differ according to the video format as follows.

Video Format	bit Width			
	6bit	8bit	10bit	12bit
RGB	0	0	0	×
YCbCr4:2:2	0	0	0	0
YCbCr4:4:4	0	0	0	×

\* When the YCbCr4:2:2 format has been selected, up to 12 bits can be set as the bit length. However, it is not possible to display the gray scale which accord with the bit length (gray scale) which has been set. (64-step gray scale with an 8-bit output) Use this parameter to check the Main Stream Attribute parameters rather than using it to assess the image quality.

# d) Concerning the Main Stream Attribute settings

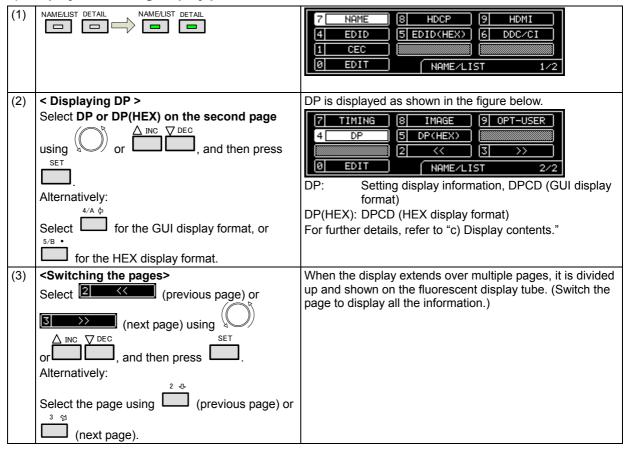
The Main Stream Attributes used for DisplayPort transfer are reflected by the following settings.

Item		Setting location
M and N for stream clock recovery	Mvid	This is automatically calculated from the Nvid setting value and the dot clock. (The VG-870B/871B is fixed to asynchronous mode, so the Mvid value varies.)
	Nvid	The setting is followed by the DisplayPort setting "Nvid". Refer to " <dp parameters="" setting="" unit="">" in this section.</dp>
Horizontal/Vertical Timing Total Active start Active video width Sync width		This is calculated from the value set by the Timing setting. For further details, refer to "Timing setting".
	polarity	The setting is followed by the sync signal polarity set by the Output setting.  For further details, refer to "4.1.2 Setting the sync signals to ON or OFF and setting the sync signal polarities"
Miscellaneous0	Synchronous Clock	The VG-870B/871B/873/874 are fixed to asynchronous mode.
	Component format	The setting is followed by the DisplayPort setting "Video Format".  Refer to " <dp parameters="" setting="" unit="">" in this section.  Full: VESA range  Limited: CEA range</dp>
	Dynamic range	The setting is followed by the Output setting "Level Mode". For further details, refer to "4.1.3 Setting the level mode".
	YCbCr Colorimetry	The setting is followed by the DisplayPort setting "Colorimetry". Refer to " <dp parameters="" setting="" unit="">" in this section.</dp>
	Bit depth per color component	The setting is followed by the DisplayPort setting "Width". Refer to " <dp parameters="" setting="" unit="">" in this section.</dp>
Other		Settings except above are not supported by the VG-870B/871B/873/874.

# 4.12.3 Displaying the DisplayPort setting information

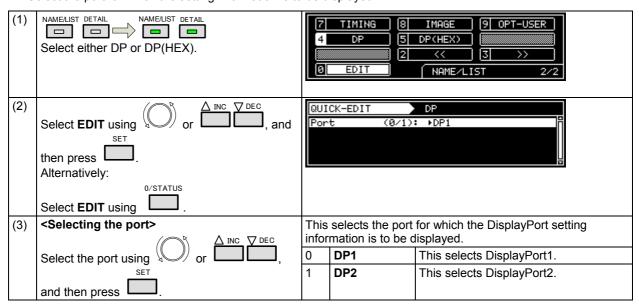
The DisplayPort setting information (Link Rate, Number of Lane, Link training results, DPCD) can be displayed.

#### a) DisplayPort setting display procedure



#### b) Selecting the port whose result is displayed

Select the port for which the setting information is to be displayed.

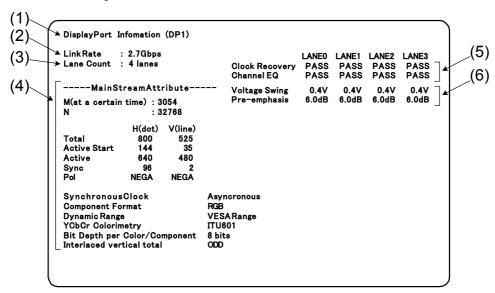


#### c) Setting contents

Displayed information

### (1) Setting display information (GUI page 1)

This page displays the DisplayPort interface settings (Link Rate, Number of Lane, Main Stream Attribute) and the link training results.



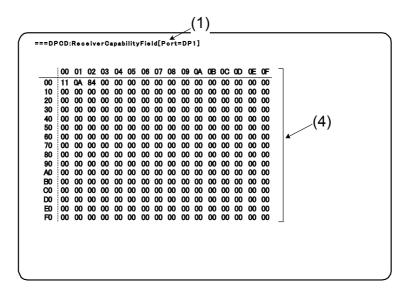
Details of the displayed information are as follows.

(1)	This is the port for which the setting status is displayed.
(2)	Link Rate setting value
(3)	Number of lane setting value
(4)	Main Stream Attribute (Only supported values are displayed.)  * The Mvid value actually varies, but the value at the time of this display setting is displayed.
(5)	Link training results (clock recovery, channel equalizer) for each lane
(6)	Voltage swing and pre-emphasis for each lane

#### (2) DPCD display (GUI page 2 and onward, HEX is DPCD display only

These pages display the DPCD (DisplayPort Configuration Data). The data for different addresses can be displayed by switching the page.

**DPCD display (GUI)** 



**DPCD display (HEX)** 

Details of the displayed information are as follows.

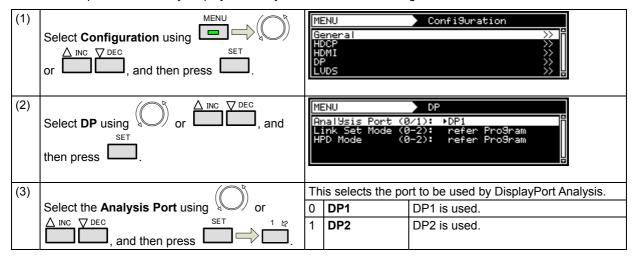
(1)	Port and DPCD field for which the setting status is displayed.
(2)	DPCD address
(3)	Setting values for each parameter
(4)	HEX display

# 4.12.4 DisplayPort Analysis

The DisplayPort Analysis mode is used to perform the link check evaluation and otherwise evaluate the DisplayPort interface.

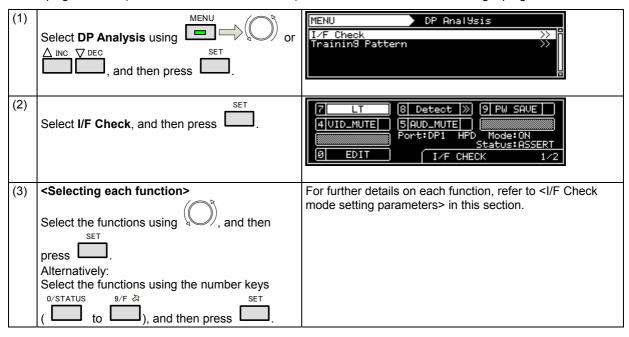
#### a) Setting the port to be used

Select the port to be used by DisplayPort Analysis with the device settings.



#### b) I/F Check mode

This mode performs a simple check on the DisplayPort interface. This makes it possible to check link training, Hotplug detection (cable connection/disconnection) and other behavior without creating a program.



#### <I/F Check mode setting parameters>

The following functions are available in I/F Check mode.

Page	Number key	Item	Description		
1/2	7	LT	Link training is performed each time this key is pressed.		
	8	Hotplug	This switches the Hotplug status.		
			Detect	The current connected status is judged.	
			Negate	The Hotplug status is forcibly set to the Negate status. (*1).	
	9	PW SAVE	This make	es the power save mode setting for the connected component.	
	4	VID_MUTE	This sets	Video MUTE to On or Off. (*2)	
	5	AUD_MUTE	This sets Audio MUTE to On or Off. (*2)		
	0	EDIT	This enters the DisplayPort output setting menu.		
2/2	7	DEFAULT	This returns to the currently selected program pattern.		
	8	LIST	This displays the DisplayPort display screen GUI (refer to "4.12.3 Displaying the DisplayPort setting information").  This reads the EDID of the connected component. This is the same operation as the normal EDID Read operation.		
	9	EDID			
	0	EDIT	This enter	rs the DisplayPort output setting menu.	

When Page 1 is displayed, the following Hotplug statuses are shown.

Item	Description						
Port	This displa	This displays the port to be used by the "DisplayPort Analysis" set by the device settings.					
HPD Mode	This display	This displays the HPD mode set by the DisplayPort output settings.					
Status	This display	displays the Hotplug status.					
	ASSERT	ERT Hotplug is asserted.					
	NEGATE Hotplug is negated.						
	HPD mode is OFF. (The Hotplug status is not judged.)						

#### \*1 Concerning the Hotplug Negate setting

The Hotplug signal is used by the sink component to notify its status to the source component. This function performs processing to forcibly set the Negate status (cable disconnected status) on the VG-870B/871B side.

This function can be used to check sink component operation with respect to the initial operation of the source component (the VG-870B/871B), without disconnecting and connecting the cable.

#### \*2 Concerning the VID\_MUTE and AUD\_MUTE settings

The VID\_MUTE setting is active only for the "Port" which was selected as the Configuration setting. The AUD\_MUTE setting is set for both Port 1 and Port 2.

\*3 The first page of the DisplayPort display screen GUI (refer to "4.12.3 Displaying the DisplayPort setting information") is shown when link training is restarted, such as when the LT key is pressed, the Hotplug detection is performed again, or operation recovers from power save mode, etc.

### c) Training Pattern mode

This mode optionally outputs the "D10.2 pattern", "PRBS-7 pattern" and other training patterns used to check DisplayPort interface conformance during link training, etc.

\* These patterns are not video patterns, and are instead patterns used for evaluation on the DisplayPort interface. Therefore, non-DisplayPort output and DisplayPort output that is not selected by "a) Setting the port to be used" remain as the video output displayed thus far.

(1)	Select <b>DP Analysis</b> using Or or and then press .	MENU DP Analysis  I/F Check >>> Training Pattern >>>
(2)	Select <b>Training Pattern</b> , and then press .	MENU Trainin9 Pattern (DP1)  Pattern Select(0-3): ▶PRBS7  Link Rate (0/1): RBR(1.62GbPs)  Number Of Lane(0-2): 4 lanes  Volta9e Swin9 (0-3): 0.4V  Pre-emPhasis (0-3): 0dB
(3)	Selecting each function>  Select the functions using , and then press .  Alternatively: Select the functions using the number keys O/STATUS 9/F & SET ( to ), and then press .	For further details on each function, refer to <training mode="" parameters="" pattern="" setting=""> in this section.</training>

#### <Training Pattern mode setting parameters>

The following settings can be made in Training Pattern mode.

(1)	Pattern Select (0-3)	Th	is sets the pattern.	
		0	D10.2 (TP1)	The D10.2 test pattern (Link Training Pattern1) is output.
		1	EQ (TP2)	The Link Training Pattern2 (Channel Equalization Sequence) is output.
		2	Symbol Error Rate	The Symbol Error Rate Measurement Pattern is output. (This pattern is output only, and the error rate is not measured.)
		3	PRBS7	The PRBS7 pattern is output.
(2)	Link Rate (0/1)	Th	is sets the link rate.	
		0	HBR (2.7Gbps)	Output at the link rate "HBR (2.7 Gbps)".
		1	RBR (1.62Gbps)	Output at the link rate "RBR (1.62 Gbps)".
(3)	Number of Lane (0-2)	This sets the number of output lanes.		
		0	1lane	Output on 1 lane.
		1	2lanes	Output on 2 lanes.
		2	4lanes	Output on 4 lanes.

(4)	Voltage Swing (0-3)	This sets the voltage swing level (differential level).		
		0	0.4 V	Output at 0.4 V
		1	0.6 V	Output at 0.6 V
		2	0.8 V	Output at 0.8 V
		3	1.2 V	Output at 1.2 V
(5)	Pre-emphasis (0-3)	This sets the pre-emphasis level.		
		0	0 dB	Output at 0 dB
		1	3.5 dB	Output at 3.5 dB
		2	6.0 dB	Output at 6.0 dB
		3	9.5 dB	Output at 9.5 dB

- \* The Training Pattern mode settings can be made only for the Main Link output. DPCD and other settings in accordance with each pattern and level are not made.
- \* When returning from Training Pattern mode to normal output, the settings return to the program settings selected thus far.
- \* The valid voltage swing level setting and pre-emphasis setting combinations are as follows.

Voltage Swing	Pre-emphasis				
	0 dB	3.5 dB	6 dB	9.5 dB	
0.4 V	0	0	0	0	
0.6 V	0	0	0	×	
0.8 V	0	0	×	×	
1.2 V	0	×	×	×	

# 4.12.5 Embedded audio

Embedded audio signals can be output from the DisplayPort.

For further details on the setting procedure, refer to "4.16 Digital audio".

### 4.12.6 EDID

For further details on the setting procedure, refer to "6.13.3 EDID".

### 4.12.7 HDCP

For further details on the setting procedure, refer to "8.1 HDCP settings".

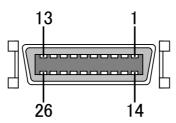
# 4.12.8 DDC/CI

For further details on the setting procedure, refer to "6.13.4 DDC/CI".

# 4.13 V-by-One HS

# 4.13.1 Connectors and pin assignments

• Connector: MDR 10226-1210-PE (made by 3M)



1CH		
Pin No.	Symbol	
1	GND	
2	GND	
2	NC	
4	Tx0n	
5	Tx0p	
6	Tx1n	
7	Tx1p	
8	GND	
9	SCL	
10	GND	
11	NC	
12	Tx3n	
13	Tx3p	
14	HTPDN1	
15	LOCKN1	
16	GND	
17	GND	
18	SDA	
19	GND	
20	NC	
21	NC	
22	Tx2n	
23	Tx2p	
24	NC	
25	GND	
26	GND	

2CH			
Pin No.	Symbol		
1	GND		
2	GND		
1 2 3	NC		
4 5 6 7	Tx4n		
5	Tx4p		
6	Tx5n		
7	Тх5р		
8	GND		
9	SCL		
10	GND		
11	NC		
12	Tx7n		
13	Tx7p		
14	HTPDN2		
15	LOCKN2		
16	GND		
17	GND		
18	SDA		
19	GND		
20	NC		
21	NC		
22	Tx6n		
23	Тх6р		
24 25 26	NC		
25	GND		
26	GND		

<sup>\*1</sup> No power is supplied.

# 4.13.2 V-by-One HS setting procedure

# <V-by-One HS setting procedure>

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : 170MHz 10Bit Timin9 ( TIM )
(2)	Select <b>Output</b> using or or or or and then press.	MENU OutPut  All OutPut  Analog OutPut  Digital OutPut  VBI Function  OutPut  Signature  Signature
(3)	Select <b>Digital Output</b> using or	MENU Digital OutPut  DVI
(4)	Select <b>V-by-One HS</b> using or or or or and then press.	MENU V-by-One HS  OutPut 1ch (Ø/1): ▶ON  2ch (Ø/1): ON  Number Of Lane(Ø-5): Auto  Pre-emPhasis (Ø/1): Ø%  Field BET Mode(Ø/1): Disable
(5)	Select the items using or	For further details on the parameters, refer to the table below.
	Select the parameters using or Select the parameters using or Select the parameters using or Select the parameters using the number keys olect the parameters using the parameters using or Select the parameters using the number keys olect the num	

# <V-by-One HS setting parameters>

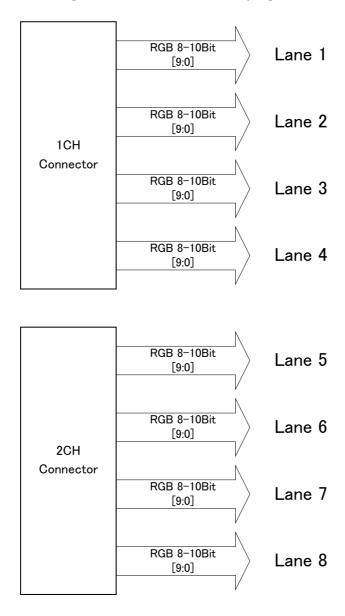
(1) Output 1ch (0/1) This sets On or Off for each channel.	This sets On or Off for each channel		
Output 2ch (0/1) 0 Off No output.			
1 On Output.			
(2) Number Of Lane (0/5) This sets the number of data lanes to be	pe output from V-by-One HS		
0 Auto The numb automatical	The number of data lanes is changed automatically in line with the dot clock frequency.  * Only lanes 1 to 4 are supported.		
20M - 75 N	MHz 1 Lane		
75M - 150	MHz 2 Lane		
150M - 30	00 MHz 4 Lane		
number of	The image is output using one lane as the number of data lanes. The same image is output from output channels 1 and 2.		
number of	The image is output using two lanes as the number of data lanes. The same image is output from output channels 1 and 2.		
number of	The image is output using four lanes as the number of data lanes. The same image is output from output channels 1 and 2.		
	The image is output using eight lanes as the number of data lanes.		
	e is output using 16 lanes as the f data lanes.		
(3) Split(0/8) This selects the screen splitting type in uses the frame memory on the board.	the ×4 mode or 4K×2K mode which		
0 MODE0 Screen sp	Screen split-into-4 output in the form of a squadivided into 4 equal parts		
1 MODE1 Screen ve	rtically split-into-4 output		
2 MODE2 Screen ho	Screen horizontally split-into-2 output		
3 MODE3 Screen ve	ertically split-into-2 output		
4 MODE0(x4 mode) Non Dividi	ing Mode		
5 MODE1(x4 mode) Normal Mo	ode		
6 MODE2(x4 mode) Cross Mod	de		
	Iormal Mode		
, , , , , , , , , , , , , , , , , , , ,	Cross Mode		
(4) <b>Pre-Emphasis(0/1)</b> This sets the pre-emphasis.			
	mphasis is set to 0%.		
	mphasis is set to 100%.		
(5) Field BET Mode(0/1) This sets the Field BET Mode.			
0 <b>Disable</b> The Field	BET Mode is set to Disable.		

# 4.13.3 Data transfer systems

# <Specifications of mode during 4K×2K mode output>

During the 4K×2K mode output, 8 lanes are combined to output one screen.

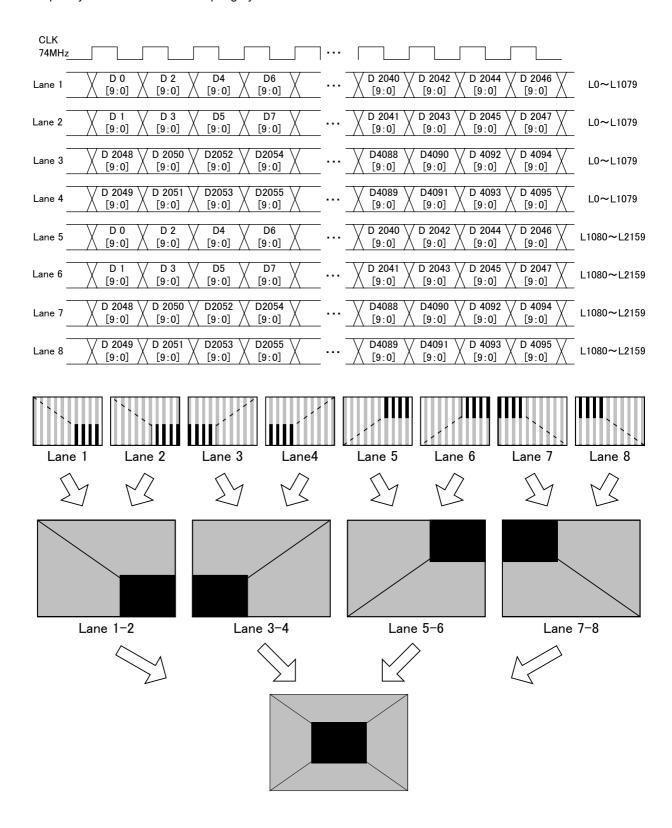
[4K×2K 60 Hz 8-lane output]



# [1] MODE0 (8 Lane) - Normal

The image is split into 4 in the form of a square divided into 4 equal parts and assigned in sequence using lanes 1 and 2 for the top left part, lanes 3 and 4 for the top right part, lanes 5 and 6 for the bottom left part and lanes 7 and 8 for the bottom right part.

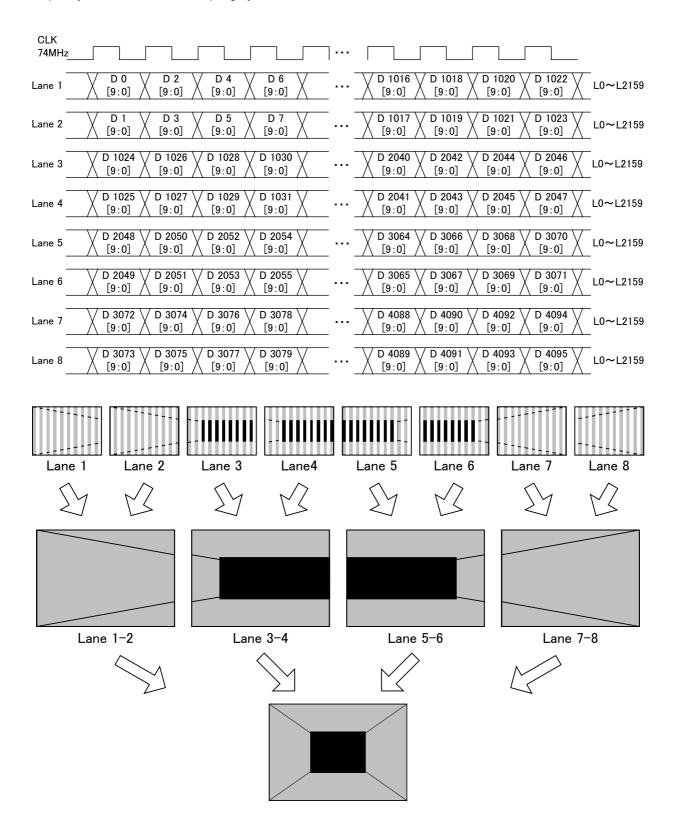
Given here as an example for explanatory purposes is a case where the resolution is 4096 × 2048, the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



# [2] MODE1 (8 Lane) - 4Split

The image is split horizontally into four parts and assigned in sequence from the left using lanes 1 and 2, lanes 3 and 4, lanes 5 and 6 and lanes 7 and 8.

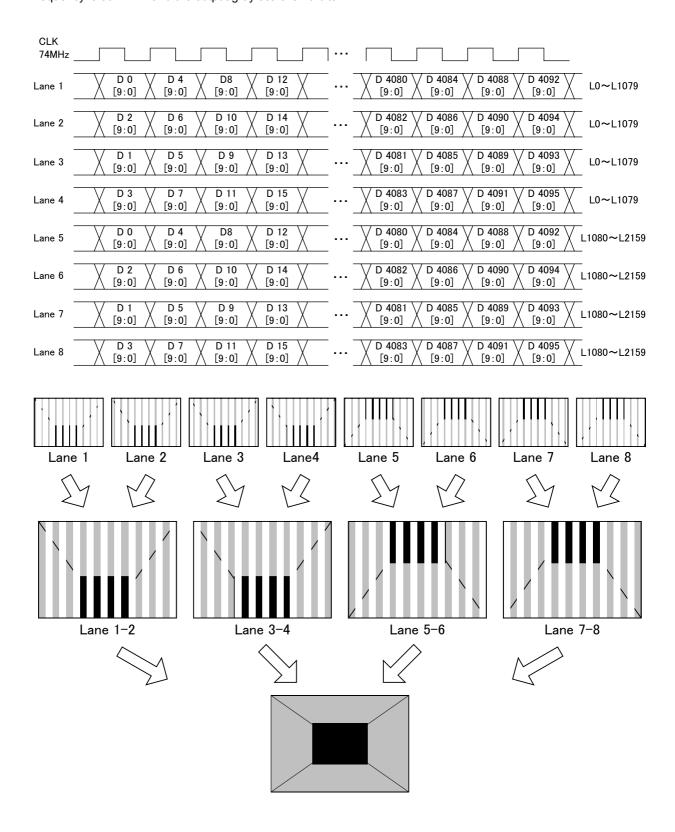
Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



# [3] MODE2 (8 Lane) (screen split horizontally into 2)

Using lanes 1 and 2 and lanes 3 and 4, the top half of the image is output in the even and odd numbers; similarly, using lanes 5 and 6 and lanes 7 and 8, the bottom half of the image is output in the even and odd numbers.

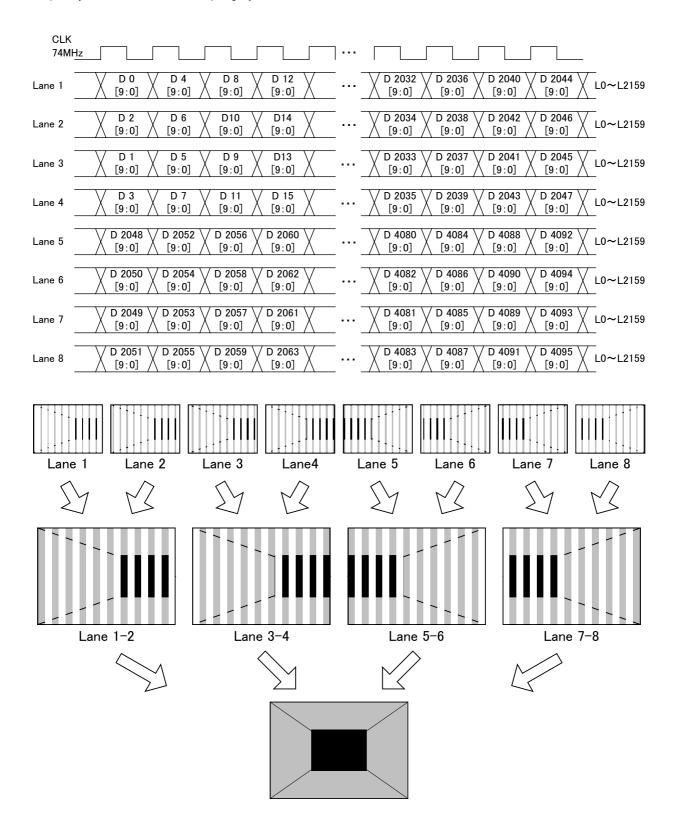
Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



# [4] MODE3 (8 Lane) - (screen split vertically into 2)

Using lanes 1 and 2 and lanes 3 and 4, the left half of the image is output in the even and odd numbers; similarly, using lanes 5 and 6 and lanes 7 and 8, the right half of the image is output in the even and odd numbers.

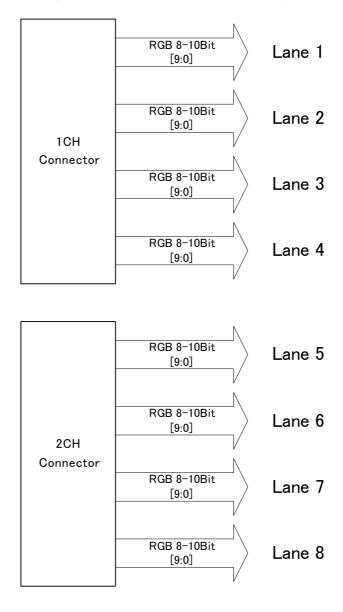
Given here as an example for explanatory purposes is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 592 MHz and the output gray scale is 10 bits.



## <Specifications of ×4 mode output>

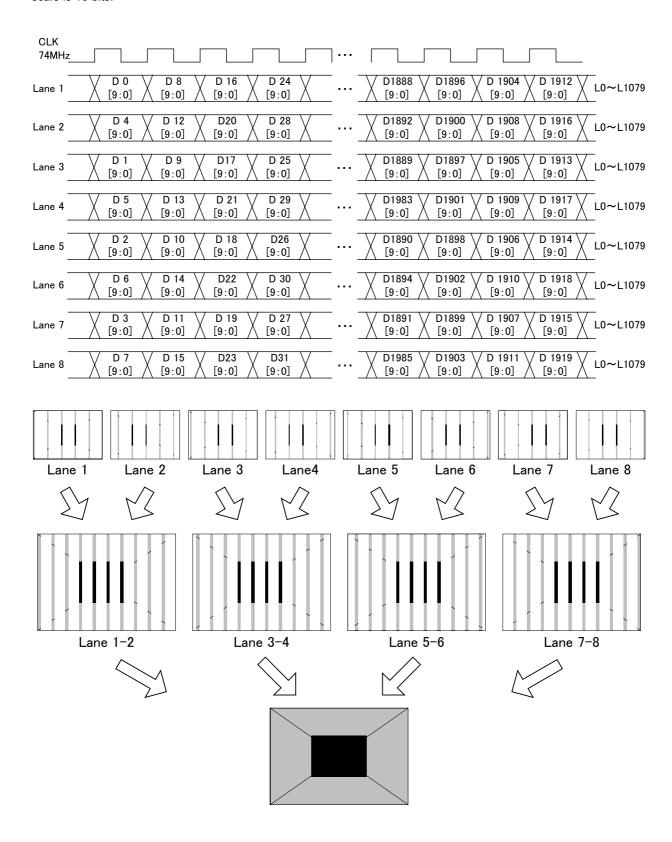
For the Full HD 240 Hz output, 8 lanes are combined to output one screen.

[Full HD 240 Hz 8-lane output]



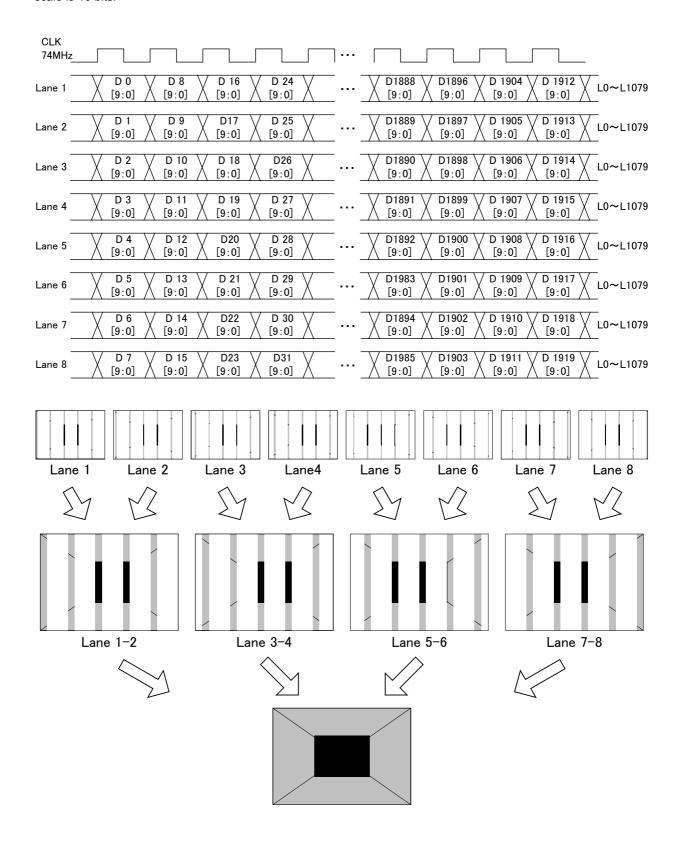
#### [1] MODE0 (8 Lane ) - No screen splitting

Using 1-8 lanes, the image is output with the pixel assignment as below without splitting.



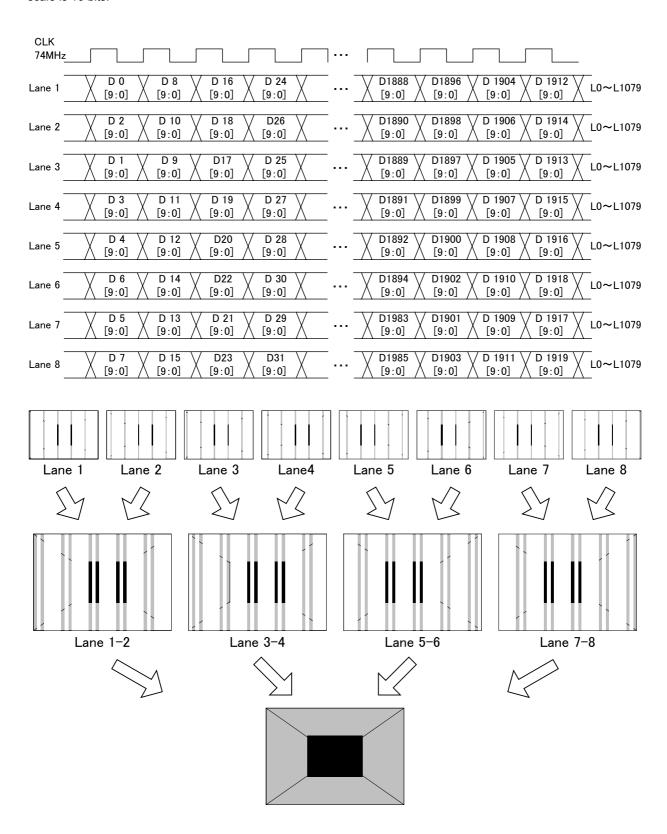
#### [2] MODE1 (8 Lane) - Normal Mode

Using 1-8 lanes, the image is output with the pixel assignment as below without splitting the screen.



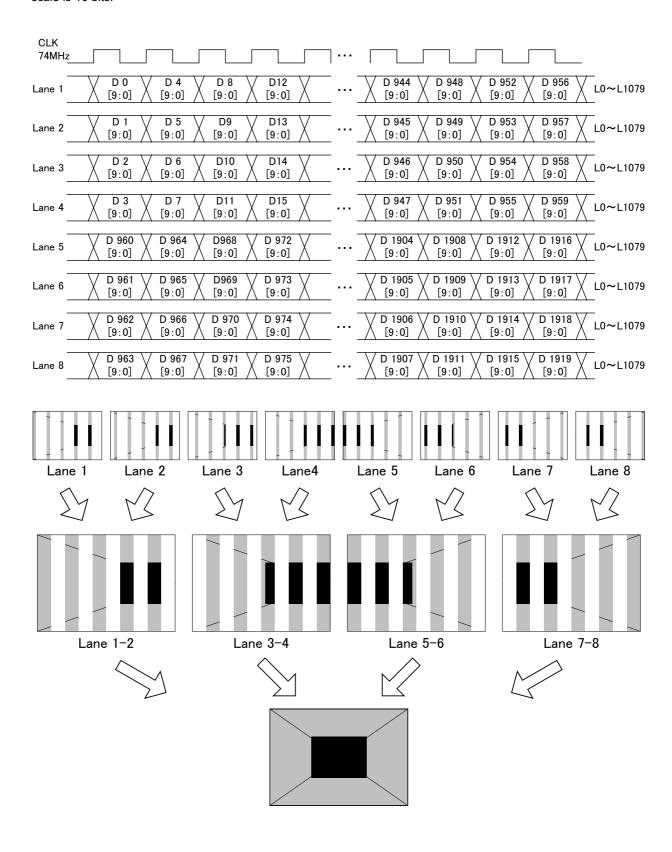
#### [3] MODE2 (8 Lane) - Cross Mode

Using 1-8 lanes, the image is output with the pixel assignment as below without splitting the screen.



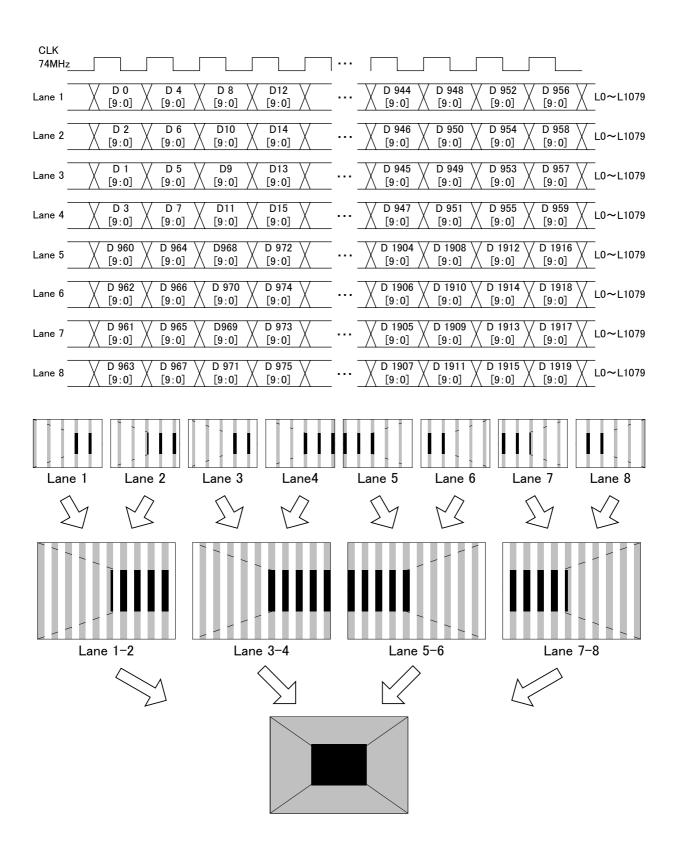
#### [4] MODE3 (8 Lane) - Dividing Normal Mode

Using lanes 1 - 2 lanes and 3- 4 lanes, the left half of the image is output in the even and odd numbers; similarly, using 5-6 lanes and 7- 8 lanes, the right half of the image is output in the even and odd numbers.



#### [5] MODE4 (8 Lane) - Dividing Cross Mode (same as 4K×2K MODE3)

Using lanes 1 -3 lanes and 2- 4 lanes, the left half of the image is output in the even and odd numbers; similarly, using 5-7 lanes and 6-8 lanes, the right half of the image is output in the even and odd numbers.



## Specifications of 16-lane modes of using VM-1825 x 2 boards mode (4K×2K 120 Hz mode) output

This mode is an output mode for 4K×2K 120 Hz testing.

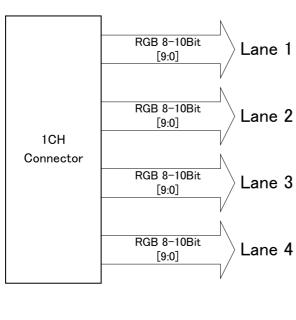
In the 4K×2K 2-board mode output, 16 lanes are combined using two output boards to output one screen.

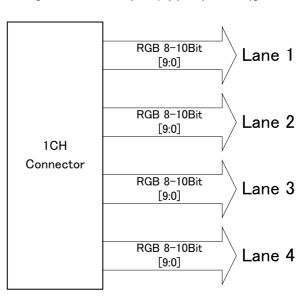
- <sup>\*</sup> 1 to 8 lanes are assined to the lower position of VM-1825 board.
- \* 9 to 16 lanes are assined to the upper position of VM-1825 board..

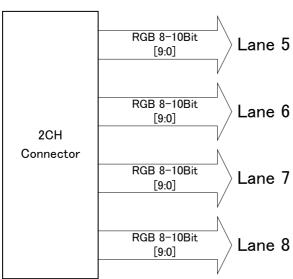
### [4K×2K 120 Hz - 16-lane output]

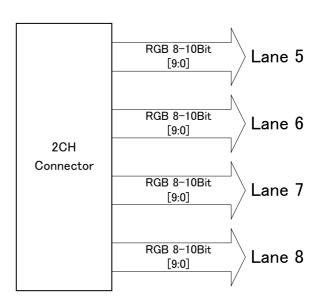
#### [1st board output (lower position)]

#### [2nd board output (upper positon)]





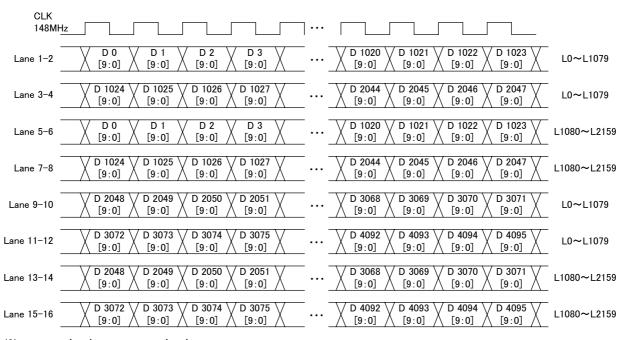




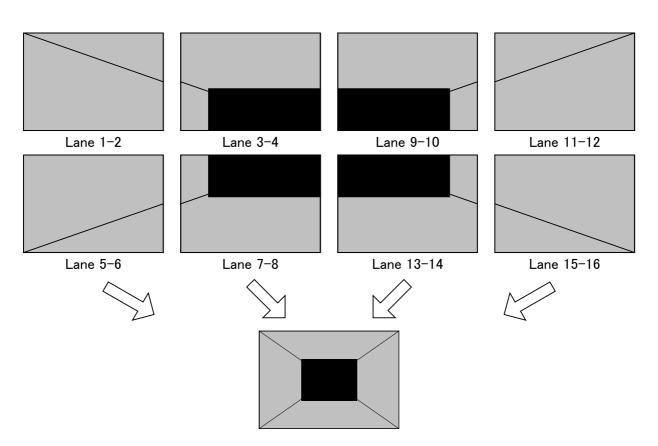
## [1] MODE0 (16Lane) (Checker split into 4 + vertically split into 2)

The screen is split it into 4 by checker and output from each output board , and split vertically into 2 at each board, and output.

This example is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 1184 MHz and the output gray scale is 10 bits.

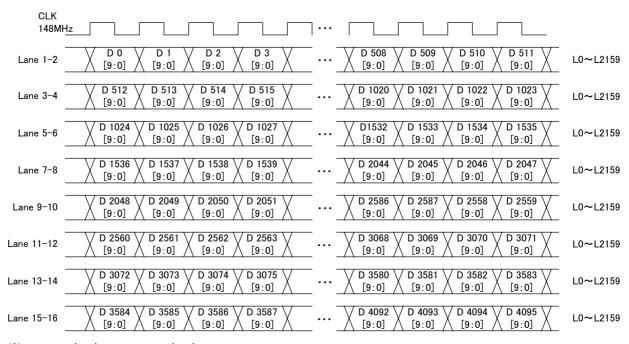


X Lane n - (n+1)  $\Rightarrow$  n:EVEN, (n+1):ODD

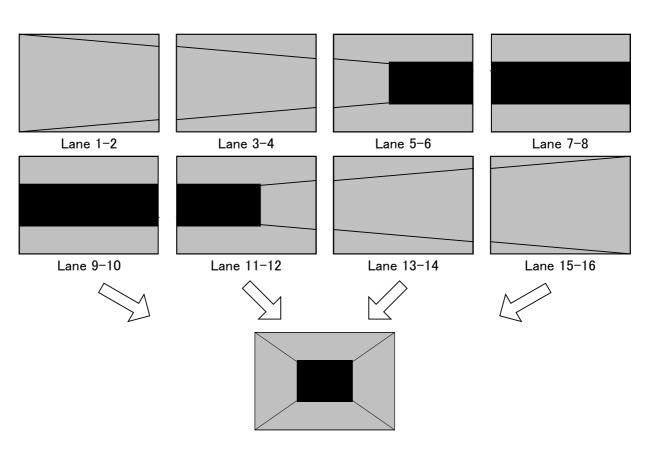


#### [2] MODE1 (16Lane) (vertically split into 4 + vertically split into 2)

The screen is vertically split into 4 and output from each output board, then it is vertically split into 2 at each board, and output.



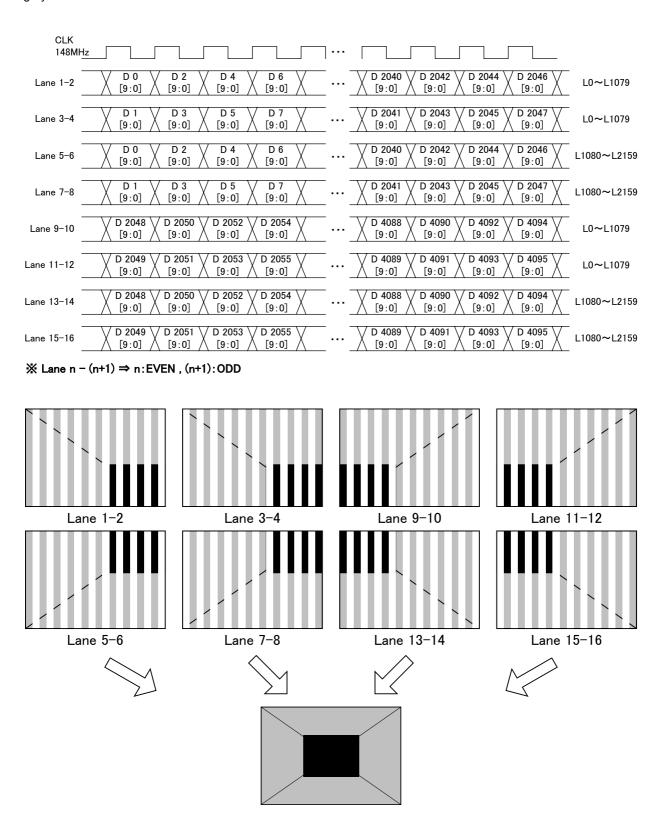
X Lane n - (n+1)  $\Rightarrow$  n: EVEN, (n+1): ODD



## [3] MODE2 (16Lane) (horizontally split into 2 + vertically split into 2)

The screen is horizontally split into 2 and output from each output board, then it is vertically split into 2 at each board, and output.

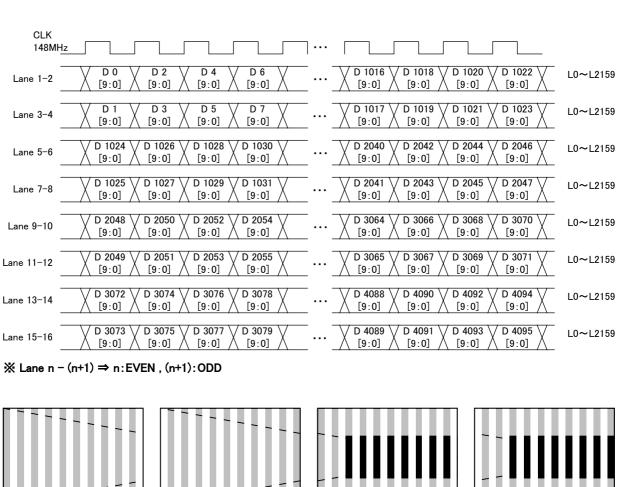
This example is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 1184 MHz and the output gray scale is 10 bits.

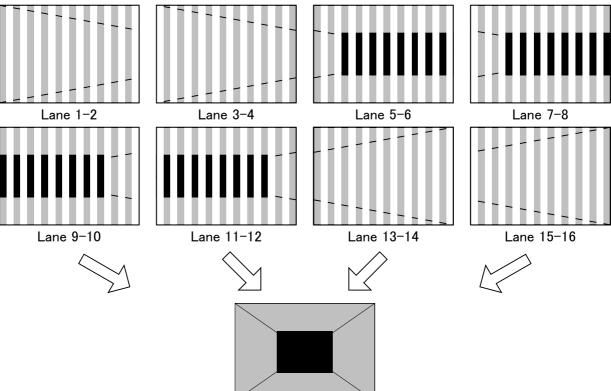


#### [4] MODE3 (16Lane) (vertically split into 2 + vertically split into 2)

Channels 1 to 8 are used. The screen is split vertically into 2 and output from each board, and then it is split vertically into two at each board, and output.

This example is a case where the resolution is  $4096 \times 2048$ , the dot clock frequency is 1184 MHz and the output gray scale is 10 bits.

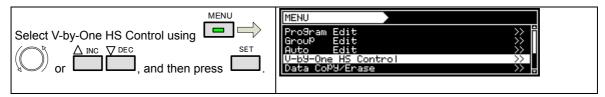


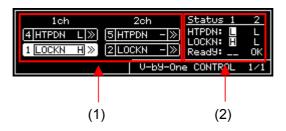


## 4.13.4 V-by-One HS Control

The VM-1825 makes it possible to control the V-by-One HS control signals (HTPDN and LOCKN).

#### <Operating procedure>





#### <Control parameters>

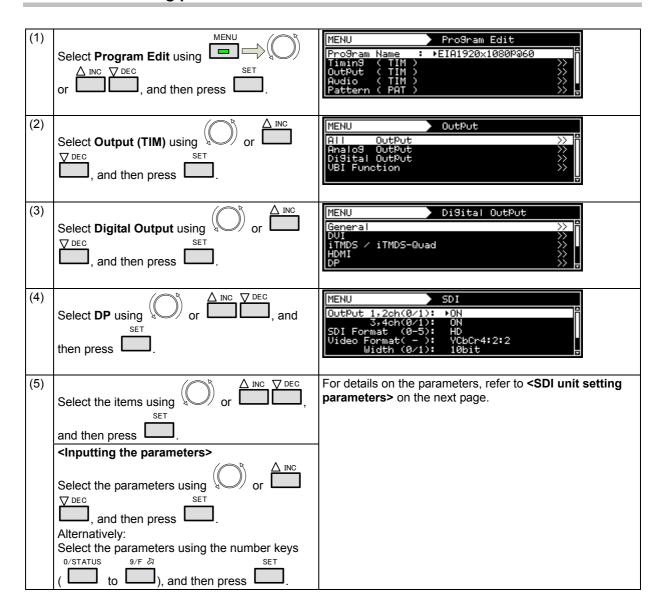
(1)	These parameters are used for channel 1 and channel 2 control.								
	-	- <b>Through</b> The signals from the receiver are passed straight through.							
	L	L Forced Low The signal is forcibly set to low.							
	Н	Forced High	d High The signal is forcibly set to high.						
(2)	The current statuses of channel 1 and channel 2 are indicated here.								
		The statuses are displayed in solid white lettering against a black background if the signals have been forcibly set to low or high.							

## 4.14 SDI

#### 4.14.1 Concerning the SDI output

Only timings complying with the SDI compatible standard (SMPTE) concerned are output as the SDI output. For details, refer to "11.1.11 SDI unit."

#### 4.14.2 SDI setting procedure



#### <SDI unit setting parameters>

(1)	Output 1,2ch (0/1)		This sets On or Off for each channel. (The same settings apply to channels 1				
	Output 3,4ch (0/1)		and 2 and to channels 3 and 4.)				
			ese are the same setting OFF."	gs as in "4.1.1 Setting the output interfaces to ON			
		0	OFF	No output			
				No output.			
(2)	0015 (405)	1	ON	Output.			
(2)	SDI Format (0-5)	Ih  *	is sets the SDI format.	The state of the s			
		^	details, refer to "11.1.1	upported differs depending on the timing. For			
		0	SD	SD-SDI			
		1	HD	HD-SDI			
		<u>                                   </u>					
		2	3G-A	3G-SDI. Level.A			
		3	3G-B	3G-SDI. Level.B			
		4	Dual	Dual-Link			
		5	3D	3D (Option)			
				* "3D" is an option. When the requisite license			
				has not been registered, sending using 3D is not possible. For details, consult with an			
				Astrodesign sales representative or your			
				distributor.			
(3)	Video Format (0-2)		This sets the color space of the images which are output from SDI.				
,	,	*	* The format which is supported differs depending on the timing. For				
			details, refer to "11.1.11 SDI unit."				
		0	RGB	RGB			
		1	YCbCr4:4:4	YCbCr4:4:4			
		2	YCbCr4:2:2	YCbCr4:2:2			
(4)	Width(0-4)	Th	is sets the output image	bit length. A setting which is independent of the			
		pat	pattern drawing bit length can be selected or the same bit length as the				
		pat		an be selected automatically.			
		*		rn drawing bit length which exceeds the bit length			
		D <sub>C</sub>		deficient portion is filled with zeros.  oit length (gray scale) for pattern drawing."			
		*		upported differs depending on the timing. For			
			details, refer to "11.1.1				
		0	Auto	Either 10 bits or 12 bits are selected			
				automatically in accordance with the pattern			
				drawing bit length.			
		1	10bit	10-bit output			
		2	12bit	12-bit output			
(5)	Audio Output(0/1)	Th	is sets the embedded au	idio output.			
		* For details on the embedded audio settings, refer to "4.2.5 Embedded audio s					
			audio, high bit rate audi	o (option)."			
		0	OFF	The embedded audio is not output.			
		1	ON	The embedded audio is output.			
(6)	Payload		is sets the payload ID da				
		For further details, refer to <sdi data="" id="" param<="" payload="" setting="" th=""><th><sdi data="" id="" parameters="" payload="" setting=""> in the</sdi></th></sdi>		<sdi data="" id="" parameters="" payload="" setting=""> in the</sdi>			
		next section.					

#### <SDI payload ID data setting parameters>

\* These settings are for the payload ID data only. For details of the settings which directly affect the transfer signals, refer to the respective items.

(1)	OFF/ON (0/1)	Th	is sets the payload ID d	ata to ON or OFF.
,	- ()	0	OFF	The payload ID data is not inserted.
		1	ON	The payload ID data is inserted.
(2)	Mode (0/1)	Th	is is the setting for Byte	1
,		0	Refer Program	According to each data setting, the setting value of Bye1 and Channel Assignment is followed.  Note) Byte1 and Channel Assignment is not displayed.
		1	Manual	Byte1 and Channel Assignment is set by manually.
(3)	Byte1	Se	t Byte1.	
		No	ote) this is available on	ly wheh Mode is set by "1 (manual)"
		00	- FF	
(4)	Transport (0/1)	Th	is sets the transport sca	nning mode.
		0	Interlace	Interlace scanning
		1	Progressive	Progressive scanning
(5)	Picture (0/1)	Th	is sets the image scann	ing mode.
		0	Interlace	Interlace scanning
		1	Progressive	Progressive scanning
(6)	Picture Rate(0-7)	Th	is sets the frame rate.	
		0	23.98Hz	The frame rate of each value is used as the
		1	24Hz	setting.
		2	25Hz	
		3	29.97Hz	
		4	30Hz	
		5	50Hz	
		6	59.94Hz	
		7	60Hz	
(7)	Aspect Ratio(0/1)	Th	is sets the aspect ratio.	
,		0	4:3	4:3 aspect ratio
		1	16:9	16:9 aspect ratio
(8)	H Y-Sampling (0/1)	Th	is sets the Y sampling v	
,		0	0	
		1	1	
(9)	Sampling Struc(0-4)	Th	is set the sampling struc	cture (video format)
(-)	Jampung Jamus(5 1)	0	4:2:2(YCbCr)	The video format of each value is used as the
		1	4:4:4(YCbCr)	setting.
		2	4:4:4(RGB)	
		3	4:4:4:4(YCbCr+A)	1
		4	4:4:4:4(RGB+A)	_
(10)	Channel Assignment		et transmission channel.	
(10)	(0-3)			ly wheh Mode is set by "1 (manual)"
	\ \frac{1}{2}	0	1ch	,
		1	2ch	1
		2	3ch	1
		3	4ch	1
ı	İ	-	1	1

(11)	Dynamic Range(0-2)	This sets the dynamic range.			
		0	100%	The dynamic range of each value is used as the	
		1	200%	setting.	
		2	400%		
(12)	Bit Depth(0-2)	This sets the bit length.			
		0	8bit	The bit length of each value is used as the	
		1	10bit	setting.	
		2	12bit		

## 4.14.3 Embedded audio

The embedded audio can be output as the SDI output.

For details of the setting procedure, refer to "4.16 Digital audio."

## 4.15 Analog audio settings

#### 4.15.1 Connectors and output signals



Connector	Signal
R	Audio right (R) channel
L	Audio left (L) channel

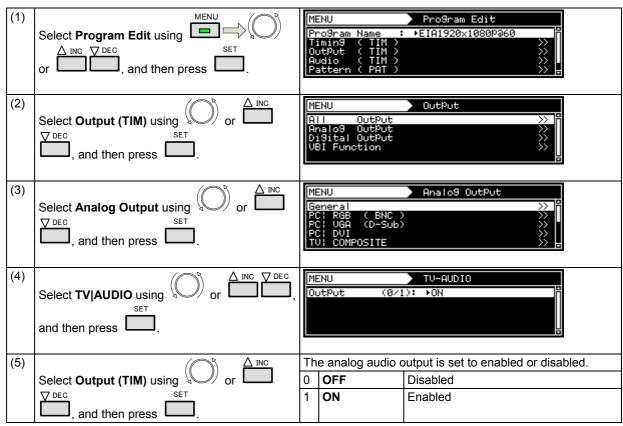
#### 4.15.2 Analog audio signals

Analog audio signals can be output.

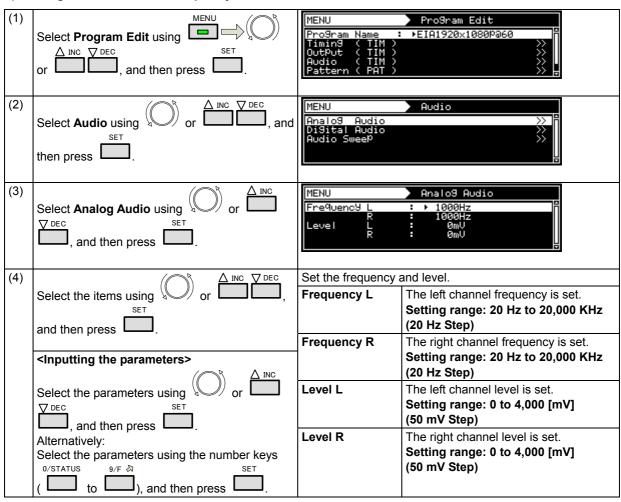
The operation procedure is described below.

- a) Set the analog audio to enabled.
- b) Set the sound source, frequency, level, etc.

#### a) Setting the analog audio to enabled



#### b) Setting the sound source, frequency, level, etc.



## 4.16 Digital audio

#### 4.16.1 Digital audio

The embedded audio and high-bit-rate audio (HDMI only) can be superimposed and output from the HDMI and DisplayPort connectors. The high-bit-rate audio is optionally supported. For further details, contact an Astrodesign sales representative or your dealer.

The audio sources supported for each output are listed in the table below.

Audio Source		HDMI		DisplayPort		SDI
		VM-1817	HDMI1.4a Boar d (ARC- compatible)	TX versions preceding the ones on the right	TX version "CG V1.1.9" and later	
Ext. Optical	External digital input (optical)	0	×	×	○ (Max. 96 kHz)	×
Ext. COAXIAL	External digital input (coaxial)	0		×	×	×
Ext. Analog to L-PCM	External analog input (L-PCM output)	0		×	×	×
Ext. Analog to DSD	External analog input (DSD output)	0	×	×	×	×
Int. L-PCM	Internal output (L-PCM)	0		×	○ (Max. 96 kHz)	○ (48 kHz)
Int. Non L-PCM	Internal output (non-L-PCM, memory incorporated)	Option		×	×	×
Int.L-PCM (Flash)	Internal output (non-L-PCM, memory incorporated)	Option		×	×	×
Int. DSD	Internal output (DSD)	Option		×	×	×
Ext. I2S L-PCM	External I2S input (L-PCM)	Option		×	×	×
Ext. I2S Non L-PCM	External I2S input (non-L-PCM)	Option		×	×	×

"O": Supported

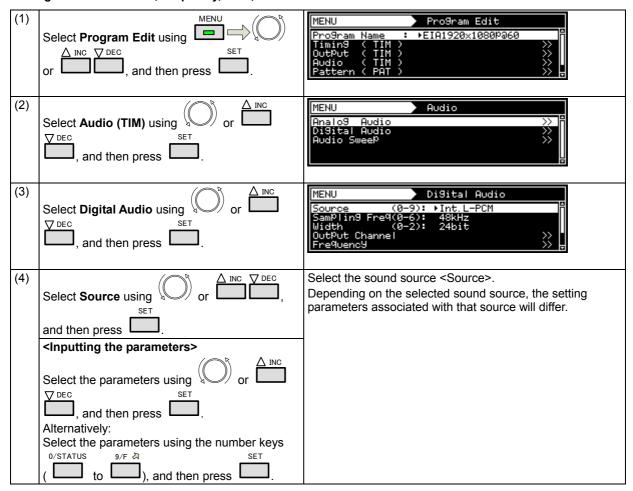
'X": Not supported (output is set to OFF even it is selected)

"Option": Optionally supported (For further details, contact ASTRODESIGN sales representative or your dealer.)

- \* See "10.3 Information" to confirm the TX version. For more details on upgrading the version, contact ASTRODESIGN sales representative or your dealer.
- \* Maximum sampling rate of DisplayPort is 96 kHz.
- \* The sample rate with SDI outputs is fixed at 48 kHz for all the settings.

The operation procedure is described below.

#### Setting the sound source, frequency, level, etc.



#### <Source>

0	OFF	No output.
1	Ext. Optical	The digital input (optical) is output. No parameters are provided.
2	Ext. COAXIAL	The digital input (coaxial) is output. No parameters are provided.
3	Ext. Analog to L-PCM	The analog input is converted to L-PCM, and output. For further details, refer to <ext. analog="" l-pcm="" parameters="" setting="" to="">.</ext.>
4	Ext. Analog to DSD	The analog input is converted to DSD, and output. For further details, refer to <ext. analog="" dsd="" parameters="" setting="" to="">.</ext.>
5	Int. L-PCM	Sine waves are output by the internal L-PCM. For further details, refer to <int. l-pcm="" parameters="" setting="">.</int.>
	Int. Non L-PCM Option.	The sound of the non L-PCM stored in the internal memory is output. For further details, refer to <int. l-pcm="" non="" parameters="" setting="">.</int.>
	Int.L-PCM(Flash) Option.	The sound of the L-PCM stored in the internal memory is output. For further details, refer to <int.l-pcm (flash)="" parameters="" setting="">.</int.l-pcm>
	Int. DSD Option.	The sound of the DSD stored in the internal memory is output. For further details, refer to <int. dsd="" parameters="" setting="">.</int.>
	Ext. I2S L-PCM Option.	The non L-PCM input from the I2S connector is output. For further details, refer to <ext. i2s="" l-pcm="" parameters="" setting="">.</ext.>

ſ	Ext. I2S Non L-PCM	The L-PCM input from the I2S connector is output.
	Option.	For further details, refer to <ext. i2s="" l-pcm="" non="" parameters="" setting="">.</ext.>

\* When selecting **Ext.Optical** or **Ext.Coaxial** as the **sound source setting**, select the setting after inputting stable signals. (Check the sampling frequency of the input audio signals only after selecting the setting.)

Described below are the setting parameters associated with each sound source.

#### <Ext. Analog to L-PCM setting parameters>

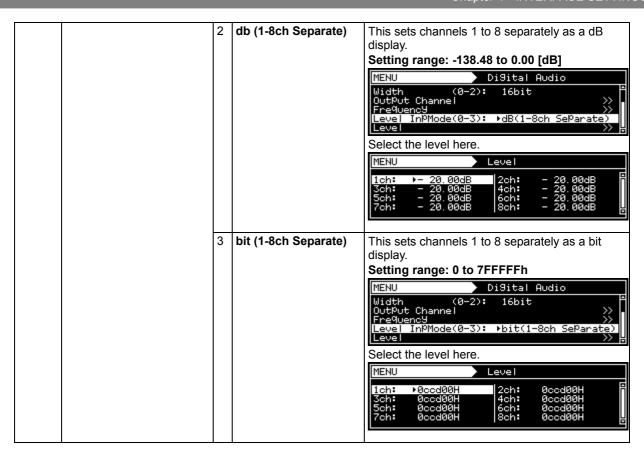
	Setting menu display			tal Audio t.ANALOG to L-PCM A	
		Si Oi		KHZ >>	
(1)	Sampling Frequency	Se	et the sampling frequency.		
	(0-6)	0	32 KHz	This is for sampling at a 32 kHz frequency.	
		1	44.1 KHz	This is for sampling at a 44.1 kHz frequency.	
		2	48 KHz	This is for sampling at a 48 kHz frequency.	
		3	88.2 KHz	This is for sampling at a 88.2 kHz frequency.	
		4	96 KHz	This is for sampling at a 96 kHz frequency.	
		5	176.4 KHz	This is for sampling at a 176.4 kHz frequency.	
		6	192 KHz	This is for sampling at a 192 kHz frequency.	
(2)	Output Channel	1	hether to set ON or OFF tre.	for channel 1 to 8 of the embedded audio is set	
		ME	MENU OutPut Channel		
			ch (0/1): OFF 4ch ch (0/1): OFF 6ch	1 (0/1): ON	
		0	OFF	No output.	
		1	ON	With channels 1, 3, 5 and 7: The left-channel input is output. With channels 2, 4, 6 and 8: The right-channel input is output.	

#### <Ext. Analog to DSD setting parameters>

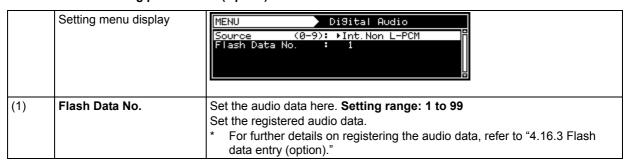
	Setting menu display	So	MENU Digital Audio Source (0-9): ▶Ext.ANALOG to DSD SamPlin9 Freq( - ): 44.1kHz OutPut Channel >>		
(1)	Output Channel	he	Whether to set ON or OFF for channel 1 to 8 of the embedded audio is set here.    MENU		
		0	0 OFF No output.		
		1	ON	With channels 1, 3, 5 and 7: The left-channel input is output. With channels 2, 4, 6 and 8: The right-channel input is output.	

## <Int. L-PCM setting parameters>

	Setting menu display	ME	ENU Digi	tal Audio		
		Sa		. 1kHz		
		0.	idth (0-2): 24 utPut Channel	bit ≳		
			requency	<u> </u>		
(1)	Sampling Frequency	Th	is sets the sampling freq	uency.		
	(0-6)	0	32 KHz	This outputs the signals at a 32 kHz sampling frequency.		
		1	44.1 KHz	This outputs the signals at a 44.1 kHz sampling frequency.		
		2	48 KHz	This outputs the signals at a 48 kHz sampling frequency.		
		3	88.2 KHz	This outputs the signals at a 88.2 kHz sampling frequency.		
		4	96 KHz	This outputs the signals at a 96 kHz sampling frequency.		
		5	176.4 KHz	This outputs the signals at a 176.4 kHz sampling frequency.		
		6	192 KHz	This outputs the signals at a 192 kHz sampling frequency.		
(2)	Width (0-2)	Th	is sets the bit length of th			
		0	16 BIT	The audio data is output with 16 bits.		
		1	20 BIT	The audio data is output with 20 bits.		
(0)		2	24 BIT	The audio data is output with 24 bits.		
(3)	Output Channel			annels 1 to 8 of the embedded audio is set here.		
				ut Channel		
		1ch (0/1): ▶0N 2ch (0/1): 0N 3ch (0/1): 0FF 4ch (0/1): 0FF 5ch (0/1): 0FF 6ch (0/1): 0FF 7ch (0/1): 0FF 8ch (0/1): 0FF				
		0	OFF	No output.		
		1	ON	Output.		
(4)	Frequency	This sets the frequency (sine wave) of the audio signals to be output.				
		MENU Frequency				
		30	ch: ▶ 1000Hz 2ch ch: 1000Hz 4ch ch: 1000Hz 6ch ch: 1000Hz 8ch	n: 1000Hz  ∥		
		20	- 96000	A frequency up to half the value of the sampling frequency can be set.  Example: When the sampling frequency is 48 kHz  This sets a frequency in the 20 Hz to 24 kHz ranges		
(5)	Level InpMode	Th	is sets the level input me	thod.		
		0	dB (1-8ch All)	This sets all the channels 1 to 8 as a dB display.  Setting range: -138.48 to 0.00 [dB]  MENU Digital Audio  Width (0-2): 16bit OutPut Channel SHEWELLE SHEW		
		1	BIT (1-8ch All)	This sets all the channels 1 to 8 as a bit display.  Setting range: 0 to 7FFFFh  MENU Digital Audio  Width (0-2): 16bit OutPut Channel Short C		



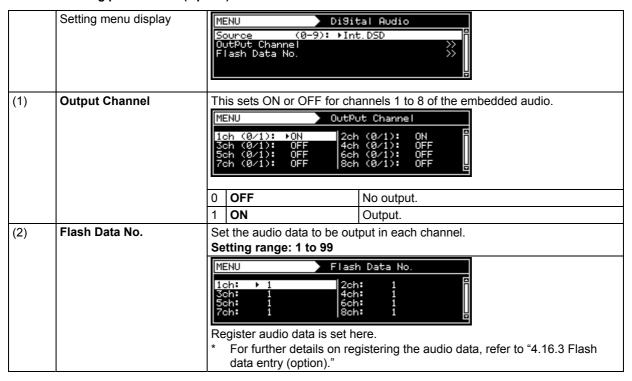
#### <Int. Non L-PCM setting parameters> (Option)



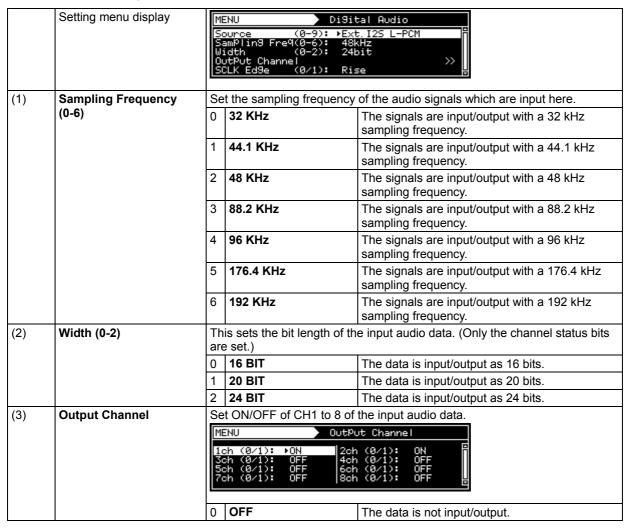
#### <Int.L-PCM(Flash) setting parameters> (Option)

	Setting menu display	MENU DiSital Audio Source (0-A): ▶Int.L-PCM(Flash) Flash Data No. : 1
(1)	Flash Data No.	Set the audio data here. Setting range: 1 to 99 Set the registered audio data.  * For further details on registering the audio data, refer to "4.16.3 Flash data entry (option)."

#### <Int. DSD setting parameters> (Option)



#### <Ext. I2S L-PCM setting parameters> (Option)



		1	ON	The data is input/output.
(4)	SCLK Edge (0/1)	The SCLK edge to be used when the I2S data is input is set here.		
		0	Rise	The data is captured at the SCLK rising edge.
		1	Fall	The data is captured at the SCLK falling edge.

#### <Ext. I2S Non L-PCM setting parameters> (Option)

	Setting menu display	MENU Di9ital Audio  Source (0-9): ►Ext.I2S Non L-PCM SamPlin9 Freq(0-A): 48kHz SCLK Ed9e (0/1): Rise					
(1)	Sampling Frequency	_	The sampling frequency of the input audio signals is set here.				
	(0-A)	0	32 KHz	The signals are input/output with a 32 kHz sampling frequency.			
		1	44.1 KHz	The signals are input/output with a 44.1 kHz sampling frequency.			
		2	48 KHz	The signals are input/output with a 48 kHz sampling frequency.			
		3	88.2 KHz	The signals are input/output with a 88.2 kHz sampling frequency.			
	4	96 KHz	The signals are input/output with a 96 kHz sampling frequency.				
		5	176.4 KHz	The signals are input/output with a 176.4 kHz sampling frequency.			
		6	192 KHz	The signals are input/output with a 192 kHz sampling frequency.			
		7	352.8 KHz	The signals are input/output with a 352.8 kHz sampling frequency.			
		8	384 KHz	The signals are input/output with a 384 kHz sampling frequency.			
		9	705.6 KHz	The signals are input/output with a 705.6 kHz sampling frequency.			
		Α	768 KHz	The signals are input/output with a 768 kHz sampling frequency.			
(2)	SCLK Edge (0/1)	Th	e SCLK edge to be used	when the I2S data is input is set here.			
		0	Rise	The data is captured at the SCLK rising edge.			
		1	Fall	The data is captured at the SCLK falling edge.			

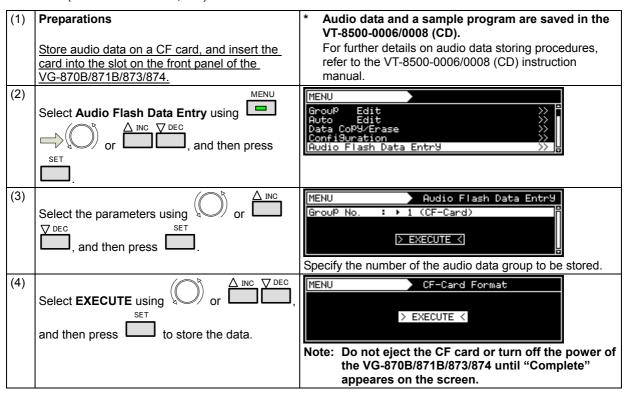
#### 4.16.2 Audio sweep settings

When **Internal PCM** is selected by the **Digital Audio > Source** setting, the audio sweep function is available, and the audio output frequency is incremented at the specified interval.

For further details on the audio sweep settings, refer to "4.1.9 Audio sweep settings."

#### 4.16.3 Flash data entry (option)

Audio data (VT-8500-0006/0008, etc.) can be stored in the VG-870B/871B/873/874.



<sup>\*</sup> Groups of audio data up to 64MB created by SP-8870 software can be saved in the VG-870B/871B/873/874.

## 4.17 Audio sweep setting

For details on the audio sweep settings, refer to "4.1.9 Audio sweep settings."



# FUNCTIONS AVAILABLE WITH TV STANDARD SIGNALS

Text, data, copy prevention information and other data are superimposed in the vertical blanking interval (VBI) of the NTSC, PAL and SECAM TV standard signals, and transmitted as multiplexed broadcasts.

The VG-870B supports the following functions.

- (1) Macrovision (Option)
- (2) Closed caption/V-Chip
- (3) Teletext
- (4) WSS
- (5) CGMS-A/ID-1

The TV standard signals and functions supported are summarized in the table below.

	Macrovision	Closed caption V-chip	Teletext	WSS	CGMS-A ID-1
NTSC-J	0	0	-	-	0
NTSC-M	0	0	-	-	0
NTSC-443	0	0	-	-	0
PAL-M	0	0	-	-	0
PAL-60	0	0	-	-	0
PAL(-BDGHIK)	0	0	0	0	-
PAL-N	0	0	-	0	-
PAL-Nc	0	0	-	0	-
SECAM	0	-	-	0	-



The Macrovision, Closed caption (V-Chip) and Teletext functions cannot be executed at the same time.

#### 5.1 Macrovision

#### 5.1.1 Description and specifications

#### ■ What is "Macrovision"?

Macrovision is a copy prevention system developed by Macrovision Corporation.

Widely used by video sources such as VHS and DVD-Video on the market and by satellite broadcasts, for instance, the system ensures that, by causing the automatic gain control (AGC) of the video decks to malfunction, users will not be able to record properly the video output sources which are protected by the system.

The AGC circuit is designed to adjust the gain of the input signals automatically and maintain the appropriate sensitivity, and almost all consumer-use VTRs now feature it. It serves to make dark images a little brighter and excessively light images darker and keep them this way.

While keeping the luminance and chrominance signal components of the video signals unchanged, Macrovision's copy prevention signals cause the AGC to malfunction by mixing signals with non-standard levels in the vertical blanking interval. This is why the brightness of the images will fluctuate even when the images are recorded. These signals also have the effect of causing trouble for the sync signals and disturbances in the images.

The trouble caused ensures that content is unwatchable even when it has been copied.

#### ■ What does the "color stripe" function do?

This function is part of the Macrovision standard, and it provides color stripes to overlap with the regular Macrovision signals.

It constitutes a method of superimposing the modulated color burst signal onto the video signals, and it is also referred to as a means of color burst copy prevention.

The color stripe function inserts thin horizontal lines into the copied images and, like the Macrovision system, it makes the images unwatchable.

\* The color stripes are provided only in the Type 2 and 3 modes of the NTSC-M and J systems.

#### ■ Macrovision specifications

Macrovision supports the TV signals of the following systems.

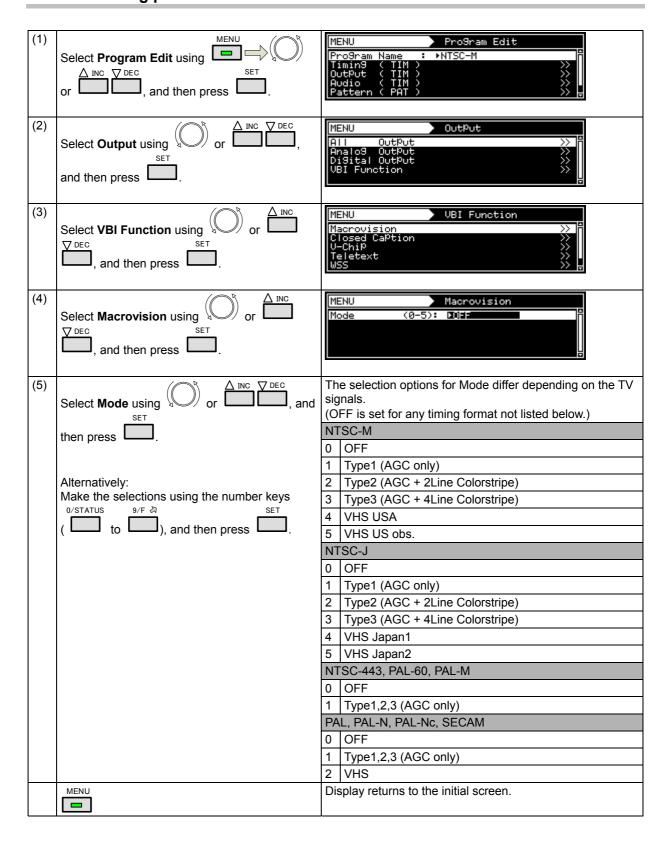
Its signals are superimposed onto the composite signals and Y/C signals.

- NTSC-M, NTSC-J, NTSC-443
- PAL-60, PAL, PAL-M, PAL-N, PAL-Nc
- SECAM



- Macrovision is an option. Contact ASTRODESIGN sales representative or your dealer.
- The Macrovision signals have different effects depending on the type of VHS or DVD player. When using the system, please check the terms and conditions of the agreement with Macrovision Corporation.

#### 5.1.2 Setting procedure



## 5.2 Closed captions/V-Chip

#### 5.2.1 Description and specifications

#### ■ What does the closed caption (CC) function do?

Closed captioning was developed in the United States, and it provides broadcasts with subtitles for the hearing impaired so that people who are deaf or hard of hearing can enjoy movies and news programs.

The captions are "closed" in the sense that they are not displayed on the screen during normal playback. Conversely, the Japanese-language subtitles and other such characters used with video content which are "burned into" the images from the start are referred to as "open captions."

Although closed captioning was originally developed as a technology for the hearing impaired, it is currently attracting the attention of educators and language learners as a tool which helps develop "listening."

The CC subtitle data is superimposed onto line 21 (first field) and line 284 (second field) of the NTSC output signals, and output. The subtitle data has two modes, captions and text. Another available service is the Extended Data Service (EDS) which transmits titles, ratings and other program information using line 284 (second field). The V-Chip described below uses the EDS function.

A total of 32 characters can be displayed per line by CC. There are 15 lines, but the maximum number of lines is limited to 4 in the caption mode (CC1 to 4). All 15 lines can be displayed in the text mode (T1 to 4).

#### ■ What does the V-Chip do?

The V-Chip refers to a semiconductor chip that blocks out TV programs containing violence, bad language and sex situations. "V" refers to violence, and the viewing of programs is blocked out according to the ratings which are categorized by the extremity of the program content. Once the ratings are set in a receiver (TV set) which incorporates the V-Chip function, the rating information of EDS is decoded, and whether the programs are to be output to the screen is automatically determined.

#### ■ Specifications of closed caption/V-Chip

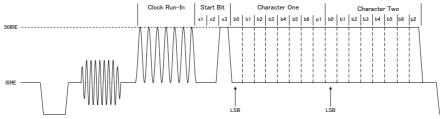
Closed caption/V-Chip supports the TV signals of the following systems.

Closed caption/V-Chip is superimposed onto the composite signals and Y/C signals.

- NTSC-M, NTSC-J, NTSC-443
- PAL-60, PAL, PAL-M, PAL-N, PAL-Nc

Note) When using VM-1812, if 625-line timing (PAL, PAL-N, PAL-Nc) is output, the caption data is superimposed on the 23th line (335-line). In case of using VM-1812-B, it is imposed on 21st line (334 line).

The closed caption/V-Chip waveform is shown below. Following the color burst come a sine wave known as Clock Run-In and continues to the start bit. The start bit is always "001." Two bytes of data (Char1, Char2) are sent for each line. Char1 and Char2 are decoded from LSB, and an odd parity is usually added to MSB (bit 8).



Closed Caption / V-Chip (21Line System)

## 5.2.2 Closed caption settings

(1)	Select <b>Program Edit</b> using SET or And then press SET.	MENU Pro9ram Edit  Pro9ram Name : ▶NTSC-M  Timin9 ( TIM )  OutPut ( TIM ) Audio ( TIM )  Pattern ( PAT )
(2)	Select <b>Output</b> using or	MENU OutPut  All OutPut  Analo9 OutPut  Digital OutPut  VBI Function  OutPut
(3)	Select <b>VBI Function</b> using or or or or or or and then press.	MENU VBI Function  Macrovision  Closed CaPtion  V-ChiP  Teletext  WSS   VBI Function   Closed CaPtion  Closed
(4)	Select Closed Caption using or	MENU Closed CaPtion  Mode (0-9): DFF Interval : 1s
(5)	Selecting the setting items from the table> Select the parameters using  A INC TOBEC Or DEC, and then press  SET  SET  SET  SET	For details on the setting items and parameters, refer to <b>Table of closed caption setting items&gt;</b> provided below.
(6)	Select the parameters using or Select the parameters using or Select the parameters using or Alternatively:  Select the parameters using the number keys O/STATUS O/F & SET ( to ), and then press .	
	Upon completion of the settings:  Press MENU  Press	Display returns to the initial screen.

#### <Table of closed caption setting items>

(1)	Mode (0-9)	The mode	The <b>mode</b> is set here.			
		0	O OFF The captions are set to OFF.			
		1	CC1	Closed caption mode 1 is selected here.		
		2	CC2	Closed caption mode 2 is selected here.		
		3	CC3	Closed caption mode 3 is selected here.		
		4	CC4	Closed caption mode 4 is selected here.		
		5	T1	Text data mode 1 is selected here.		
		6	T2	Text data mode 2 is selected here.		
		7	Т3	Text data mode 3 is selected here.		
		8	T4	Text data mode 4 is selected here.		
		9	USER Data	The user data is selected here. Up to 20 user data can be registered using SP-8870.		
(2)	Interval	0 to 60	Interval	The interval at which the closed caption data is transmitted is set. (in 1-second increments)		
(3)	USER Data No.	1 to 20		The user data number is set. This is valid when <b>USER Data</b> has been selected as the <b>Mode</b> setting in (1).		

Listed below are the types of closed caption services available.

- **CC1** Primary Synchronous Caption Service (caption service for primary language)
- **CC2** Special Non-Synchronous Use Caption (service which does not need to be synchronized with the sound, etc.)
- **CC3** Secondary Synchronous Caption Service (caption service for secondary language)
- **CC4** Special Non-Synchronous Use Caption (service which does not need to be synchronized with the sound, etc.)
- **T1** First Text service (text service)
- T2 Second Text service (text service)
- T3 Third Text service (text service)
- **T4** Fourth Text service (text service)

## ■ The content of Closed caption internal data (1/3)

Service	Caption style, Line, Color, Option setting, etc	Character
CC1 - CC4	_	Primary Synchronous Caption Service CC1 (CC1) Secondary Synchronous
		Caption Service CC2 (CC2)  Special Non-Synchronous Use Captions CC3 (CC3)  Special Non-Synchronous
	Roll-up3	Use Captions CC4 (CC4)
	ROW10 Background: blue, transparence Text: yellow	Roll-up Style characters are always displayed immediately. Each time a Carriage Return is received, the text is scrolled up one row.
	Roll-up4 ROW15, indent Background: cyan, transparence Text: Red	Standard characters 0123456789 ABCDEFGHIJ áàâçéèêíĩÑñóôúû !,.;;7"#% &@/() []+-÷<=>? Music note, solid block, Transparent space,solid block, Music note, solid block, Transparent space
	Pop-on ROW1 ROW2 ROW3 Background: red, half transparence Text: cyan	Pop-on Style Caption data are loaded into a non-displayed memory.
	Pop-on ROW4 ROW5 ROW6 Background: green, half transparence Text: blue, flash	End of Caption command (EOC) "flips" displayed and non displayed memory.
	Pop-on ROW7 indent ROW8 indent ROW9 indent Background: magenta, non transparence Text: green, italic	ABCDEFGHIJ 0123456789 Å å Ø ø <sub>П</sub>

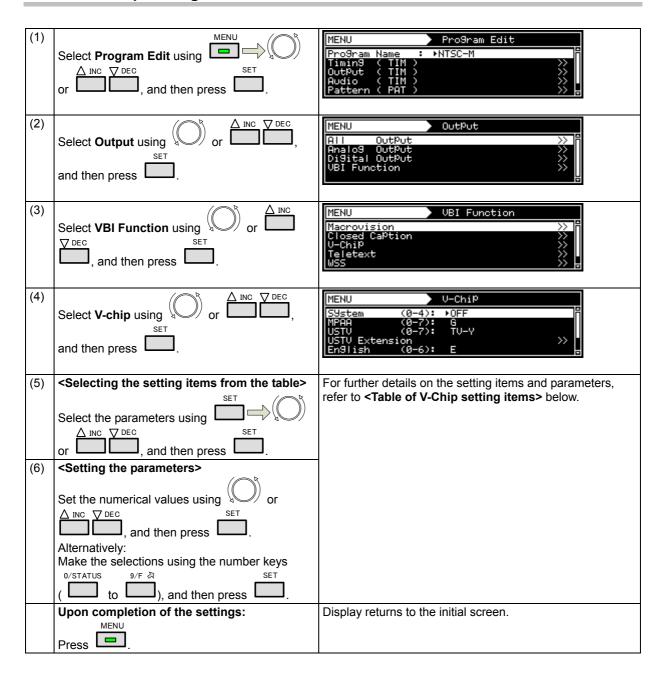
## ■ The content of Closed caption internal data (2/3)

Service	Caption style, Line, Color, Option setting, etc	Character
CC1 - CC4	Pop-on ROW12 indent ROW13 indent ROW14 indent ROW15 indent Background: white, non transparence Test: red, underline	ÁÉÓÚÜü, opening single quote, inverted exclamation mark ÀÂÇÈÊËĕĨĬïÔÙùÛ«» ÃãÍÌÌÒòÕõ{}∖^_ ~ ÄäÖöߥ¤
	Paint-on ROW1 ROW3 ROW4 ROW6 ROW7 ROW9 Background: yellow, half transparence Text: blue	Paint-on Style Characters are always displayed immediately. Characters on next row will be erased by Backspase. ABCDEFGH (A to H is deleted by Backspase)
	Paint-on ROW5 ROW6 ROW7 ROW8 ROW9 ROW10 ROW12, indent ROW14, indent Background: yellow, half transparence Text: blue	Once the cursor reaches the 32nd column position on any row, all subsequent characters will be displayed In thet column replaceing any previous character.  ABCDEFGHIJKLMNOPQRSTUVWXYZ (S to Y are replaced by Z) Abcdefghijklmnopqrstuvwxyz (n to y are replaced by z)
T1		First Text Service T1 Text Mode is a data service, generally not program related, which may be transmitted using either field of line21. Text Mode data are always displayed as soon as they are received and are intended to be displayed in a manner which isolates them from the video program used to transmit the data. Once the display window is filled these data are always scrolled upward through the display window provided by the decoder.
T2		Second Text Service T2 ABCDEFGHIJKLMNOPQRSTUVWXYZ abcdefghijklmnopqrstuvwxyz 012345678901234567890 !"#\$%&'()á+,/ :;<=>?@[é]íóú *© <sup>SM</sup> •□"¥

## ■ The contents of Closed caption internal data (3/3)

Service	Caption style, Line, Color, Option setting, etc	Character
Т3		Third Text Service T3 A Text Mode may be used that consists of data formatted to fill a box which in height is not less than 7 rows and not more than 15 rows (all of which should be contiguous), and in width is not less than 32 columns. Text should be displayed over a solid background to isolate it from the unrelated program video. Each row of text contains maximum of 32 characters.
Т4		Fourth Text Service T4 ABCDEFGHIJKLMNOPQRSTUVWXYZ abcdefghijklmnopqrstuvwxyz 012345678901234567890 !"#\$%&'()á+,/ :;<=>?@[é]íóú *© <sup>SM</sup> •□"¥

#### 5.2.3 V-Chip settings



### <Table of V-Chip setting items>

(1)	System (0-4)	The rating system is selected here.		
		0	OFF	The V-Chip is set to OFF here.
		1 MPAA MPAA is set here.		MPAA is set here.
		2	U.S.TV	U.S.TV is set here.
		3	English	Canadian English is set here.
		4 French Canadian Fre		Canadian French is set here.

The names of the rating systems and a brief description of each are presented below.

#### **MPAA: Motion Picture Association of America**

This organization was set up in order to promote the spread of American movies. It is active in many fields such as promoting exports overseas and cracking down on pirated movies. On the U.S. domestic front, it helps viewers exercise voluntary controls by establishing a rating system for violence, sexual content and discriminatory content, for example. Its rating standards are strict and its screening targets images and language that would hardly raise an eyebrow in Japan.

### U.S.TV: U.S. TV Parental Guideline Rating System

This rating system is incorporated in ordinary TV sets installed in American homes.

#### **English: Canadian English Language Rating System**

This rating system targets Canadian English.

### French: Canadian French Language Rating System

This rating system targets Canadian French.

(2) <b>MPAA</b>			These ratings which apply when <b>MPAA</b> has been selected as the <b>System</b> setting in (1) above are set here.			
		0	G	"General Audience" is set as the MPAA rating.		
	1	PG	"Parental Guidance" is set as the MPAA rating.			
		2	PG-13	"Parents Strongly Cautioned" is set as the MPAA rating.		
		3	R	"Restricted" is set as the MPAA rating.		
	4	NC-17	"No One 17 and Under Admitted" is set as the MPAA rating.			
		5	Х	"Adult Movie" is set as the MPAA rating.		
		6	Not Rated	"Not Submitted For MPAA Review" is set as the MPAA rating.		
		7	N/A	"N/A" is set as the MPAA rating.		

The names and descriptions of the MPAA ratings are presented below.

### **G:** General Audience

For general audiences.

### PG: Parental Guidance

Contains scenes unsuitable for young children.

### PG-13: Parents Strongly Cautioned

Contains scenes unsuitable for children aged 13 and under.

#### R: Restricted

Permission of a parent or guardian required for children up to 17 years of age.

### NC-17: No One 17 and Under Admitted

Cannot be viewed by anyone aged 17 years and under.

### X: Adult Movie

For adults only.

### Not Rated

Not Submitted For MPAA Review

#### N/A

No applicable restrictions

(3)	U.S.TV		ese ratings which apply v tting in (1) above are set	when <b>U.S.TV</b> has been selected as the <b>System</b> here.	
		0	TV-Y	"All children" is set as the U.S.TV rating.	
		1	TV-Y7	"Directed to older children" is set as the U.S.TV rating.	
			TV-G	"General Audience" is set as the U.S.TV rating.	
			TV-PG	"Parental Guidance Suggested" is set as the U.S.TV rating.	
		5 <b>TV-MA</b> "N		"Parents Strongly Cautioned" is set as the U.S.TV rating.	
				"Mature Audience Only" is set as the U.S.TV rating.	
		6	Not Rated 1	"Not Rated" is set as the U.S.TV rating. (Code = 000)	
		7	Not Rated 2	"Not Rated" is set as the U.S.TV rating. (Code = 111)	
(4)	USTV Extension	de	The extension bits for U.S. TV are set. The bits which can be set differ depending on the U.S. TV ratings. For further details, refer to "U.S. TV rating system extension bit settings."		

The names of the U.S. TV ratings and a description of each are presented below.

#### TV-Y: All children

Suitable for all children.

### TV-Y7: Directed to older children

Suitable for children aged 7 and above.

### TV-G: General Audience

Suitable for audiences of all ages (must not contain violent scenes, objectionable language or sexual content).

### TV-PG: Parental Guidance Suggested

Contains scenes involving some violence and sexual content unsuitable for young children or situations that may induce foul language or incite delinquency.

### **TV-14: Parents Strongly Cautioned**

Contains scenes involving violence and sexual content unsuitable for children aged 14 or below or situations that may induce foul language or incite delinquency.

#### TV-MA: Mature Audience Only

For adults only; programs with this rating are hardly ever broadcast.

### Not Rated1/2

No applicable restrictions

### [U.S. TV rating system extension bit settings]

	FV	V	S	L	D
TV-Y	Cannot be set.				
TV-Y7	0: - / 1: *	Cannot be set.			
TV-G	Cannot be set.	pe set.			
TV-PG	Cannot be set.	0: - / 1: *	0: - / 1: *	0: - / 1: *	0: - / 1: *
TV-14	Cannot be set.	0: - / 1: *	0: - / 1: *	0: - / 1: *	0: - / 1: *
TV-MA	Cannot be set.	0: - / 1: *	0: - / 1: *	0: - / 1: *	Cannot be set.
Not Rated 1	Cannot be set.				
Not Rated 2	Cannot be set.				

<sup>\* &</sup>quot;-" denotes OFF, and "\*" ON.

The names of the U.S. TV rating extension service ratings and a description of each are presented below.

### **FV**: Fantasy Violence

Acts of fantasy violence = violence in animated features and comics.

### V: Violence

Violence

## S: Sexual Situations

Sexual content

### L: Adult Language

Foul language

### D: Sexually Suggestive Dialog

Sexually suggestive dialog

(5)	English		These ratings which apply when <b>English</b> has been selected as the <b>System</b> setting in (1) above are set here.			
		0	E	"Exempt" is set as the English rating.		
		1	С	"Children" is set as the English rating.		
	2	C8+	"Children eight years and older" is set as the English rating.			
		3	G	"General Programming, suitable for all audiences" is set as the English rating.		
		4	PG	"Parental Guidance" is set as the English rating.		
		5	14+	"Viewers 14 years and older" is set as the English rating.		
		6	18+	"Adult Programming" is set as the English rating.		

The names of the Canadian English ratings and a description of each are presented below.

#### E: Exempt

No age restrictions apply.

### C: Children

Programming may be viewed by all children.

### C8+: Children eight years and older

Programming may be viewed by children aged 8 and above.

### G: General Programming, suitable for all audiences

General programming

### **PG: Parental Guidance**

Permission of a parent required to view programming.

### 14+: Viewers 14 years and older

Programming may be viewed by children 14 years and older.

### 18+: Adult Programming

Programming for adults only.

(6)	French		These ratings which apply when <b>French</b> has been selected as the <b>System</b> setting in (1) above are set here.			
		0	E	"Exempt" is set as the French rating.		
		1	G	"General" is set as the French rating.		
			8ans+	"Not recommended for young children" is set as the French rating.		
		3	13ans+	"Programming may not be suitable for children under 13" is set as the French rating.		
	_	4	16ans+	"Programming is not suitable for children under 16" is set as the French rating.		
		5	18ans+	"Programming restricted to adults" is set as the French rating.		

The names of the Canadian French ratings and a description of each are presented below.

### E: Exempt

No age restrictions apply.

### G : General

General programming.

### 8ans+: Not recommended for young children

Programming unsuitable for young children

#### 13ans+: Programming may not be suitable for children under 13

Programming unsuitable for children aged 13 and under

#### 16ans+: Programming is not suitable for children under 16

Programming unsuitable for children aged 16 and under

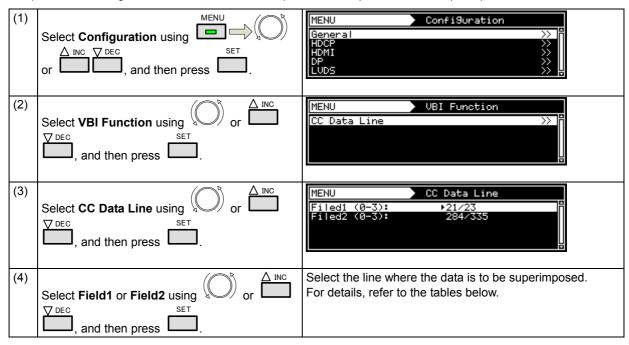
### 18ans+: Programming restricted to adults

Programming for adults only.

(7)	Interval	0 to 60	Interval	The interval at which the V-chip data is
				transmitted is set. (in 1-second increments)

## 5.2.4 Changing the data superimposing line

It is possible to change the line where the closed captions or V-chip data is to be superimposed.



### <525 line system timing (NTSC-M, NTSC-J, NTSC-443, PAL-60 and PAL-M)>

	Field1	Field2
0	19 lines	282 lines
1	20 lines	283 lines
2	21 lines	284 lines
3	22 lines	285 lines

### <625 line system timing (PAL, PAL-N and PAL-Nc)>

	Field1	Field2
0	21 lines	333 lines
1	22 lines	334 lines
2	23 lines	335 lines
3	24 lines	336 lines



- · Lines cannot be set separately for each program.
- It is possible to change the line while closed captions or V-chip data is being
  executed, but it takes time for the setting to be reflected in the output.
- VM-1812-B can not use this function.

## 5.3 Teletext

### 5.3.1 Description and specifications

#### ■ What is "Teletext"?

Teletext is the name of a system used to send still picture program data of text and graphics after multiplexing it in the vertical sync blanking interval of the TV signals. Programs broadcast in Teletext include subtitled broadcasts, news broadcasts, weather forecasts and stock market information. Teletext has achieved a high penetration rate in various countries in Europe and Southeast Asia where the 625/50i system is used.

With Teletext, a total of 40 characters × 25 lines can be contained on a page (per screen), and between 100 and 899 pages of information can be displayed.

#### ■ Teletext specifications

Teletext supports the following TV signals.

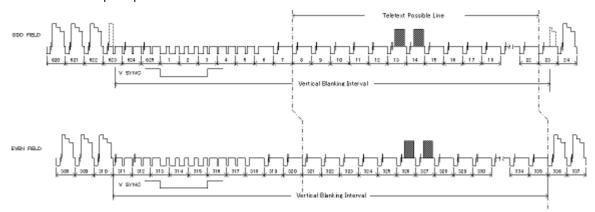
Teletext is superimposed onto the composite signals and Y/C signals.

· PAL (but PAL-60, PAL-N and PAL-Nc are not included.)

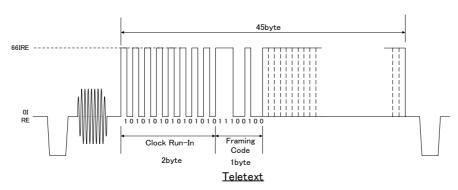
The vertical sync blanking interval (VBI) and Teletext waveforms are shown below.

The Teletext data can be output in lines 8 to 22 (first field) and lines 321 to 335 (second filed) in the vertical sync blanking interval of the PAL signals.

A total of 45 bytes consisting of the Clock Run-In, Framing Code and data bytes (42 bytes) are superimposed in one line.

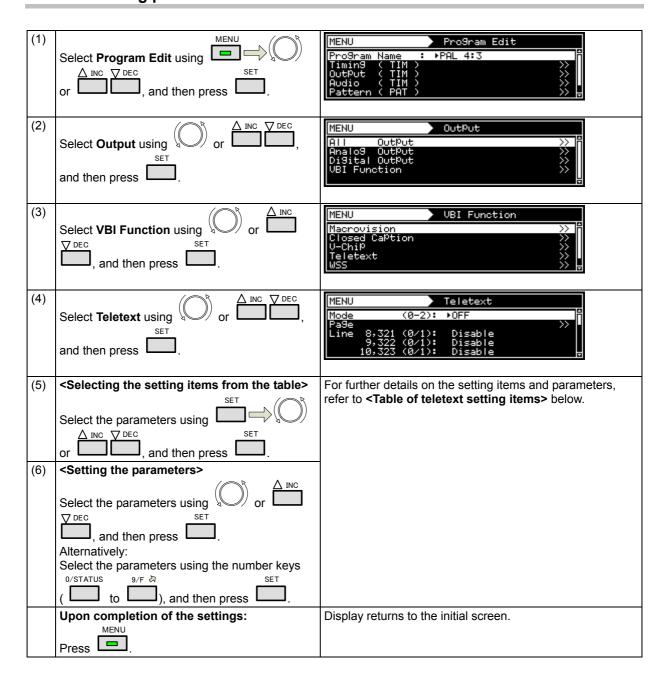


**VBI** waveforms



**Teletext waveforms** 

## 5.3.2 Setting procedure



## <Table of teletext setting items>

(1)	Mode (0-2)	The Teletex	t operation i	node is selected here.			
		0	Off	Teletext OFF.			
		1	Default	The default pages are output.			
				For further details, refer to the teletext default on			
				next pages			
		2	Page Sele	The pages selected by <b>page</b> in (2) below is output here.			
(2)	Page	MENU Page					
	Press to display the setting menu.	Ay Page Data					
		This genera	ator enables	up to 20 pages of teletext screens to be registered.			
		Numbers fo	r the interna	I data are set on each page.			
(3)	Line	The lines in	which the T	eletext data is to be output are set here.			
		The figure i	ndicate the l	ine numbers in the first field and second field.			
		8,321	0 Disable	The data is not output in line 8 and line 321.			
			1 Enable	The data is output in line 8 and line 321.			
		ng as above.					
		10,323 Same setting as above.					
		•					
		22,335	Same setti	ng as above.			

## ■ Teletext default pages (page 1 of 2)

Page No.	Description	Screen	Page No.	Description	Screen
100	Index Page	CONTENTS INFORMATION THE PAGE 100 SUCCESSIONS CONTENTS INFORMATION THES PAGE 100 SUCCESSIONS THES PAGE 100 SUCCESSIONS THES PAGE 100 SUCCESSIONS THE PAGE 100 SUCCESSIONS THE PAGE 100 SULFERNIT SEA SUCCESSION 100 SUCCESSI	101	Test Page	ASTRODESIGN  TELETENT SIDEL DESIGNATION  THE LETENT SIDEL DESIGNAT
102	Newsflash		103	Subtitle	103 PAGE 103
200	Character (English)	CHARLES AND CONTRACT OF THE CO	201	Character (German)	CHARACTER  SEPARTE  OF TORRESPONDENCE  OF TORRESPON
202	Character (Swedish /Finnish /Hungarian)	THE SHEET OF THE STATE OF THE S	203	Character (Italian)	CHARLENGE CONTRACTOR OF THE CO
204	Character (French)	204 5/25 204  T	205	Character (Portuguese /Spanish)	CHARACTER  Particular Control  20  40  10  10  10  10  10  10  10  10  1

## ■ Teletext default pages (page 2 of 2)

Page No.	Description	Screen	Page No.	Description	Screen
206	Character (Czech /Slovak)	CHARLES AND CONTRACT OF THE CO		Colours	302 PADS 5 502
302	White Flat	303 PADE 1-702	505	Clock Cracker	SUS PACE   500 
515	Multi Page	SUBCODE:0  SUBCODE:3  4 sub-pages	555	Test Pattern1	SSC PAGE 1 DES 2344-VANTY PAGE 1 PAGE 1 DES 2344-VANTY PAGE 1 DES 2344-VANTY PAGE 1 PAGE 1 PAGE 1 DES 2344-VANTY PAGE 1 P
560	Test Pattern2	CAS PROC. CAD  STREET OF THE PROCESS	-	Other pages	Screen which appears for page 700

## 5.4 WSS

### 5.4.1 Description and specifications

### ■ What is WSS (Wide Screen Signaling)?

"WSS (Wide Screen Signaling)" is a system for multiplexing the aspect ratio information of the images in the vertical sync blanking interval, and sending it.

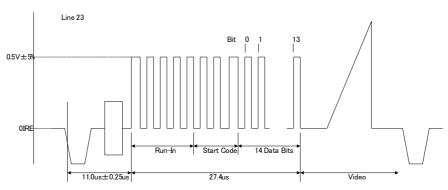
### **■** WSS specifications

WSS supports the following TV signals.

WSS is superimposed onto the composite signals and Y/C signals.

- PAL, PAL-N, PAL-Nc
- SECAM

The aspect ratio information of WSS is superimposed on line 23 of the first field. The WSS waveform consists of Run-In, Start Code and the 14-bit data. This waveform and the bit allocation are shown below.



WSS(Widscreen Signaling)

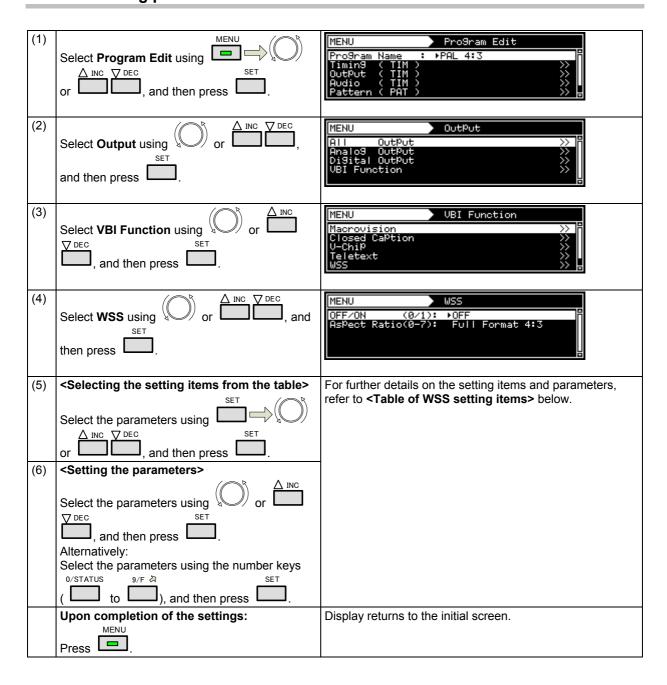
### · Bit3-0: Aspect

Bit		Aspect Ratio	Full format or Letterbox	Position
012	3			
000	1	4:3	Full format	Not applicable
100	0	14:9	Letterbox	Center
010	0	14:9	Letterbox	Тор
110	1	16:9	Letterbox	Center
0 0 1	0	16:9	Letterbox	Тор
101	1	>16:9	Letterbox	Center
0 1 1	1	14:9	Full format	Center
111	0	16:9	Full format	Not applicable

Bit 3 is the parity bit.

• Bit4-13: Other service information (not supported by the VG-870B/871B/873/874)

## 5.4.2 Setting procedure



## <Table of WSS setting items>

(1)	OFF/ON (0/1)	W	Whether the WSS information is to be output is set here.		
			OFF	The WSS information is not output.	
		1	ON	The WSS information is output.	
(2)	Aspect Ratio (0-7)	The aspect ratio is set here.		).	
		0	The aspect ratio is set to	o Full Format 4:3.	
		<ol> <li>The aspect ratio is set to LB 14:9 center.</li> <li>The aspect ratio is set to LB 14:9 top.</li> </ol>		o LB 14:9 center.	
				o LB 14:9 top.	
		3 The aspect ratio is set to <b>LB 16:9 center</b> .		o LB 16:9 center.	
		<ul> <li>4 The aspect ratio is set to LB 16:9 top.</li> <li>5 The aspect ratio is set to LB &gt;16:9 center.</li> <li>6 The aspect ratio is set to Full Format 14:9.</li> </ul>		o LB 16:9 top.	
				o LB >16:9 center.	
				o Full Format 14:9.	
		7	The aspect ratio is set to	o Full Format 16:9.	

## 5.5 CGMS -A/ID-1

### 5.5.1 Description and specifications

### ■ What is CGMS-A (Copy Generation Management System)?

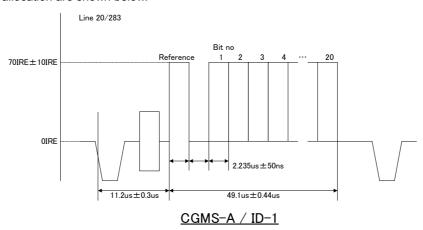
CGMS-A is a system of multiplexing the copy control information in the vertical sync blanking intervals, and sending it.

### ■ What is ID-1?

ID-1 is a system of multiplexing the aspect ratio information in the vertical sync blanking intervals, and sending it.

- NTSC, NTSC-M, NTSC-443
- PAL-60, PAL-M

CGMS-A and ID-1 are superimposed onto line 20 (first field) and line 283 (second field). The CGMS-A and ID-1 waveform consists of the reference bit and 20-bit data. This waveform and the bit allocation are shown below.



### • Bit1-0: Aspect (ID1)

Bit		Applications	
1	2	Aspect ratio	Picture display format
0	0	4:3	Normal
1	0	16:9	Normal
0	1	4:3	Letter Box
1	1	Not Defined	

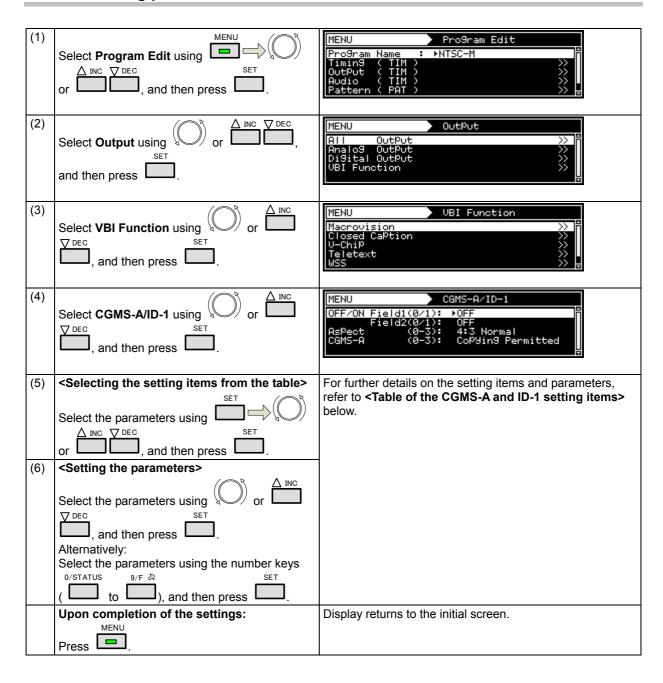
• Bit6-2: Fixed at "0000"

Bit8-7: CGMS-A

Bit		Application	
7	8		
0	0	Copy is permitted without restriction	
1	0	Condition not to be used	
0	1	One generation of copies may be made	
1	1	No copying is permitted	

- Bit14-9: Other service information (not supported by the VG-870B/871B/873/874)
- Bit20-15: CRC

## 5.5.2 Setting procedure



## <Table of the CGMS-A and ID-1 setting items>

(1)	OFF/ON Field1 (0/1)	Wł	nether to output the data	to line 20 of the first field is set here.
		0	OFF	The data is not output.
		1	ON	The data is output.
(2)	OFF/ON Field2 (0/1)	Wh	nether to output the data	to line 283 of the second field is set here.
		0	OFF	The data is not output.
		1	ON	The data is output.
(3)	3) Aspect The aspect ratio setting is selected here.		elected here.	
		0	4:3 Normal	The aspect ratio is set to 4:3.
		1	16:9 Normal	The aspect ratio is set to 16:9.
		2	4:3 Letter Box	The aspect ratio is set to 4:3 letter box.
		3	Not Defined	The aspect ratio is left undefined.
(4)	CGMS-A	Co	py protection is set here.	
		0	Copying Permitted	Copying is permitted.
		1	Not Used Condition	The CGMS-A is left undefined.
		2	Copy Once	Copy-once is set.
		3	No Copying Permitted	Copying is not permitted.

 $<sup>^{\</sup>ast}$   $\,\,$  The same data is superimposed onto line 20 and line 283.



# **PATTERN SETTINGS**

## 6.1 Color bar patterns

## 6.1.1 Types of color bar patterns

When color bars have been selected using the pattern key, for instance, color bar patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

0	сиѕтом	Customized pattern	
1	100/100-H	100%/100% color bars	
2	100/75-H	100%/75% color bars	
3	75/75-H	75%/75% color bars	
4	SMPTE	SMPTE color bars	75%/75% color bars
5	RGBW-V	Horizontal color bars	
6	xvYCC 4%	xvYCC 4% color bars	
7	xvYCC 8%	xvYCC 8% color bars	
8	xvYCC 12%	xvYCC 12% color bars	SMPTE color bars



The xvYCC color bars are turned off in the SCART output (RGB) of the TV encoder unit. xvYCC Patterns from other output than HDMI is not displayed correctly.

## 6.1.2 Color bar pattern customizing

Described below is the procedure for selecting the color bar pattern types and for performing the settings when **CUSTOM** has been selected as the color bar pattern.

(If **CUSTOM** is selected, the color bar patterns can be displayed with any width and color.)

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep   >>>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>   >>>   >>   >>>   >>    >>>   >>   >>   >>   >>   >>   >>   >>   >>   >>   >>    >>
(3)	Select Pattern/RGB/INV select using or or NC DEC, and then press.	MENU Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep Monoscope
(4)	Select Color Bar using or or or or or and then press.	MENU Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep Monoscope
(5)	Press Esc.	MENU Pattern  Pattern/RGB/INV Select  Color Bar Gray Scale  Ramp  Sweep   >>   >>   >>   >>   >>   >>   >>
(6)	Select Color Bar using or INC DEC or and then press	MENU Color Bar  Type (0-8): ▶CUSTOM   CUSTOM   >>
(7)	Select Type using or or then press or then press.	For further details on the patterns, refer to "6.1.1 Types of color bar patterns."
	Select the parameters using or or or or or line.  Alternatively:	
	Select the parameters using the number keys  O/STATUS  O	
	<b>Color of the Property of Select CUSTOM</b> using SET or Select CUSTOM using SET and then press	MENU CUSTOM  Format (0-3): ▶H direction  RePeat : 16  InPut Mode (0/1): ½  Width [½]: H= 6.3 U= 6.3  Color/Level >> 5

(8)	When <b>CUSTOM</b> has been selected as the <b>Type</b> setting	For further details on the settings, refer to <b>Table of customized color bar pattern setting items</b> > below.
	<selecting items="" the=""></selecting>	
	Select the items using $\bigcirc$ or $\bigcirc$ INC $\bigcirc$ DEC	
	and then press .	
	<pre><inputting parameters="" the=""></inputting></pre>	
	Select the parameters using or or	
	DEC SET .	
	Alternatively:	
	Select the parameters using the number keys	
	( $O$ /STATUS $O$ /F $\Diamond$ ), and then press $O$ /SET .	

## <Table of customized color bar pattern setting items>

(1)	Format (0-3)	The drawing directi	on of the color bars is set here.		
		0 H Direction	Horizontal direction		
		1 V Direction	Vertical direction		
		2 H Direction&di	w.V Horizontal direction (loopback by Repeat)		
		3 V Direction&di	v.H Vertical direction (loopback by Repeat)		
(2)	Repeat	One or more of the set here becomes t	1 to 16 data set by <b>Color/Level</b> are set here. The value he number of colors which are displayed. <b>Range: 1 to 16</b>		
(3)	Input Mode (0/1)	The method of spec	sifying the display size per color is set here.		
		0 %	The size is set as a percentage of the entire screen.		
		1 dot	The size is set in 1-dot increments.		
(4)	Width [%]	The display size pe	r color is set here.		
	Width [dot]	When a percentagused for Input Mo			
		When dots are use Input Mode	Set the width in 1-dot increments.		
(5)	Color/Level	The display color a	The display color and level are set here.		
		MENU  1: ▶ 100.0  3: _G 100.0  5: _B 100.0  7: _GB 100.0	CUSTOM Color/Level  2: R 100.0		
		Number	The colors from 1 to the Repeat setting are used for the display.		
		Color	The display colors are selected here.		
			0 Black		
			1 <b>R</b> Red		
			2 <b>_ G</b> _ Green		
			3 RG_ Yellow		
			4 <b>B</b> Blue		
			5 R B Magenta		
			6 <b>GB</b> Cyan		
			7 RGB White		
		Level	The level as a percentage of the peak brightness is set here. <b>Setting range: 0.0 to 100.0%</b>		

# 6.2 Gray scale patterns

## 6.2.1 Types of gray scale patterns

When gray scale has been selected using the pattern key, for instance, gray scale patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

0	сиѕтом	Customized pattern	
1	8Step-H	8 steps (horizontal)	
2	16Step-H	16 steps (horizontal)	
3	32Step-H	32 steps (horizontal)	16Step-H
4	8Step-V	8 steps (vertical)	
5	16Step-V	16 steps (vertical)	
6	32Step-V	32 steps (vertical)	16Step-V

## 6.2.2 Gray scale pattern customizing

Described below is the procedure for selecting the types of gray scale patterns and for performing the settings when **CUSTOM** has been selected as the gray scale pattern.

(If CUSTOM is selected, the color bar patterns can be displayed with any width and level.)

Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ►EIA1920x1080P@60  Timin3 ( TIM )
Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar Gray Scale Ramp Sweep   >>>   >>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>    >>    >>   >>    >
Select Pattern/RGB/INV select using or or NC DEC, and then press.	MENU Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep Monoscope
Select <b>Gray Scale</b> using or	MENU Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep Monoscope
Press .	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep   >>>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>>   >>   >>   >>   >>    >>>   >>    >>>   >>>   >>    >>>   >>>   >>    >>   >>>   >>    >>>   >>    >>>   >>    >>>   >>    >>    >>>   >>    >>
Select <b>Gray Scaler</b> using or	MENU Gray Scale  Type (0-6): ►CUSTOM >>   CUSTOM >>
Select Type using or	For further details on the patterns, refer to "6.2.1 Types of gray scale patterns."
	Select Program Edit using or AINC ODEC, and then press .  Select Pattern (PAT) using Or AINC ODEC, and then press .  Select Gray Scale using Or AINC ODEC, and then press .  Select Gray Scaler using Or AINC ODEC, and then press .  Select Gray Scaler using Or AINC ODEC, and then press .  Select Type using Or AINC ODEC, and then press .  Select Type using Or AINC ODEC, and then press .  Select Type using Or AINC ODEC, and then press .  Select Type using Or AINC ODEC, and then press .  Select Type using Or AINC ODEC, and then press .  Select Type using Or AINC ODEC, and then press .  Select the parameters using Or AINC ODEC, and then press .  Select the parameters using Or AINC ODEC, and then press .  Alternatively: Select the parameters using the number keys

(8)	When <b>CUSTOM</b> has been selected as the <b>Type</b>	MENU CUSTOM
	setting	Format (0-3): ▶H direction
	<pre><detailed customized="" of="" patterns="" settings=""></detailed></pre>	RePeat : 16 InPut Mode (0/1): %
	Select <b>CUSTOM</b> using or or DEC,	Width [%]: H= 6.3 V= 6.3 Level >> 5
	and then press .	
	<selecting items="" the=""></selecting>	For further details on the settings, refer to <table of<="" th=""></table>
	Select the items using or DEC or DEC,	customized gray scale pattern setting items> below.
	and then press .	
	<inputting parameters="" the=""></inputting>	
	Select the parameters using or or	
	, and then press .	
	Alternatively:	
	Select the parameters using the number keys	
	0/STATUS 9/F 為 SET	
	( Loud to Loud), and then press Loud.	

## <Table of customized gray scale pattern setting items>

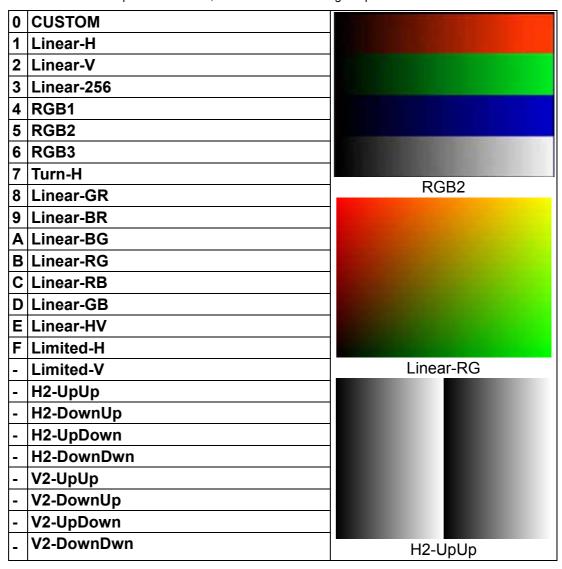
(1)	Format (0-3)	The drawing direction of the gray scale is set here.			
		0	H Direction	Horizonta	al direction
		1	V Direction	Vertical o	direction
		2	H Direction&div.V	Horizonta	al direction (loopback by Repeat)
		3	V Direction&div.H	Vertical of	direction (loopback by Repeat)
(2)	Repeat	One or more of the 1 to 16 data set by <b>Level</b> are set here. The value set here becomes the number of steps which are displayed. <b>Range: 1 to 16</b>			
(3)	Input Mode (0/1)	Th	e method of specifying th	e display	size per step is set here.
		0	%	The size screen.	is set as a percentage of the entire
		1	dot	The size	is set in 1-dot increments.
(4)	Width[%]	Th	e display size per step is	set here.	
	Width[dot]		nen a percentage is ed for Input Mode	Set any s	size from 0.0% to 100.0%.
		1	nen dots are used for out Mode	Set the s	size in 1-dot increments.
(5)	Level	The display level is set here.			
	Bit Length  MENU CUSTOM Level : (Sbit)				
				(Sbit)	
		1: 0 2: 17 3: 34 4: 51 5: 68 6: 85 7: 102 8: 119 9: 136 10: 153 11: 170 12: 187 13: 204 (4) (221) 15: 238 16: 255 6			51
		NI	Number Level	Th4	- from 4 to the Demontration and the
		NU	ımber	for the di	is from 1 to the Repeat setting are used isplay.
		Le	vel		l is set here.
				ng range differs depending on the <b>color</b>	
				8 BIT	0 to 255
				9 BIT	0 to 511
				10 BIT	0 to 1023
				11 BIT	0 to 2047
				12 BIT	0 to 4095
				13 BIT	0 to 8191
					0 to 16383
					0 to 32767
				16 BIT	0 to 65535

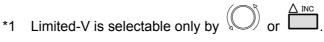
## 6.3 Ramp patterns

## 6.3.1 Types of ramp patterns

When ramp has been selected using the pattern key, for instance, ramp patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."





<sup>\*2</sup> The luminance level of Limited-H/V is 16-232 and color-difference level is 16-240.

<sup>\*3</sup> The luminance level from H2-UpUp to V2-DownDwn is incremented (Up) from 0 to 255 and decremented (Down) from 255 to 0.

## 6.3.2 Ramp pattern type settings and customizing

Described below is the procedure for selecting the types of ramp patterns and for performing the settings when **CUSTOM** has been selected as the ramp pattern type.

(If **CUSTOM** is selected, the ramp patterns can be displayed with any level and steps.)

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Program Edit  Program Name : ▶EIA1920x1080P@60  Timin9 ( TIM )
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep  Sweep
(3)	Select Pattern/RGB/INV select using or or NC DEC, and then press.	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select <b>Ramp</b> using or or $\stackrel{\triangle}{\longrightarrow}$ or $\stackrel{\square}{\longrightarrow}$ , and then press.	MENU Pattern/RGB/INV Select  Color Bar Gray Scale  Ramp Sweep Monoscope
(5)	Press Esc	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep
(6)	Select <b>Ramp</b> using $\begin{picture}(20,10) \put(0,0){\line(1,0){100}} \pu$	MENU RamP  TYPe (0-E): ►Linear-HV  CUSTOM >>
(7)	Select Type using or	For further details on the patterns, refer to "6.3.1 Types of ramp patterns."

(8)	When <b>CUSTOM</b> has been selected as the <b>Type</b> setting	MENU CUSTOM : 8bit
•	Select CUSTOM using  SET  Select Select CUSTOM using Or	Direction(0/1): FH   H-Line : 1   Level Line1 : 0 255 16   Line2 : 0 255 32   (Start) (End) (Step)     For further details on the settings, refer to <table items="" of="" pattern="" ramp="" setting=""> below.</table>
	and then press .	pattern setting items/ below.
	Select the items using or $\triangle$ INC $\nearrow$ DEC or and then press $\square$ .	
	<inputting parameters="" the=""></inputting>	
	Select the parameters using or or	
	, and then press	
	Alternatively:	
	( to by ), and then press set.	

### <Table of ramp pattern setting items>

(1)	Direction (0/1)	The resolution is set here.		
		0 <b>H</b>	Horizontal ramp	
		1 <b>V</b>	Vertical ramp	
(2)	H-Line	one screen.	erns with different levels and steps can be displayed on	
(3)	Line1 to Line4	Range: 1 to 4 types. These types are used in sequence from The start level, end level and step are set here.		
(0)		The start level, end	Bit Length	
		MENU	CUSTOM : 8bit	
		Level Line1 : Line2 : Line3 :	0 255 16 f 0 255 32   9 255 64	
			(Ø) (255) (256) tart) (End) (Step)	
			<b>A</b>	
		Otanti avai	/ \ End Level Step	
		Start Level	2.13 20101	
		(Start)	The start level is set here.	
		(End)	The end level is set here.	
		(Step)	The number of display steps from the start level to end level is set here.	
			Setting range: 1 ≤ setting ≤ (End) - (Start) + 1	
			The setting range for the above levels differs depending on the <b>color depth</b> .	
			8 BIT <b>0 to 255</b>	
			9 BIT <b>0 to 511</b>	
			10 BIT <b>0 to 1023</b>	
			11 BIT <b>0 to 2047</b>	
			12 BIT <b>0 to 4095</b>	
			13 BIT <b>0 to 8191</b>	
			14 BIT <b>0 to 16383</b>	
			15 BIT <b>0 to 32767</b>	
			16 BIT <b>0 to 65535</b>	

- Concerning H-Line
  - This item takes effect only when "0" has been selected as the Direction setting.

Concerning Direction
When "1" has been selected as the Direction setting, only "1" takes effect as the H-Line setting. (Splitting in the vertical direction is not possible.)

# 6.4 Sweep patterns

## 6.4.1 Types of sweep patterns

When sweep has been selected using the pattern key, for instance, sweep patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

0	Multi-Burst 100	Multi-burst	0 1999
1	Multi-Burst 50	Multi-burst	
2	Sweep	Sweep	Multi-burst

## 6.4.2 Sweep pattern selection

The types of sweep patterns can be set using the procedure below.

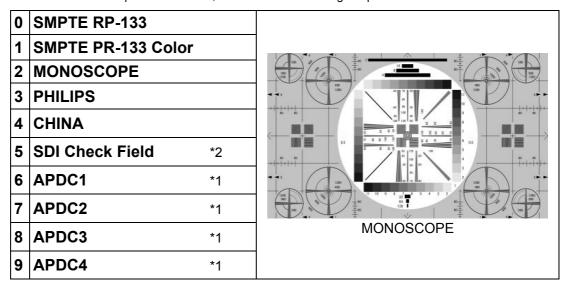
(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep  Sweep
(3)	Select Pattern/RGB/INV select using or or, and then press	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select <b>Sweep</b> using or or or or or and then press.	MENU Pattern/RGB/INV Select Color Bar Gray Scale Ramp USweep Monoscope
(5)	Press .	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep  Sweep
(6)	Select <b>Sweep</b> using or or or or and then press .	MENU SweeP  TyPe (0-3): ►Multi-Burst 100
(7)	Select Type using or ☐ → → → → → → → → → → → → → → → → → →	For further details on the patterns, refer to "6.4.1 Types of sweep patterns."

## 6.5 Monoscope patterns

### 6.5.1 Types of monoscope patterns

When monoscope has been selected using the pattern key, for instance, monoscope patterns can be selected from among the types listed below.

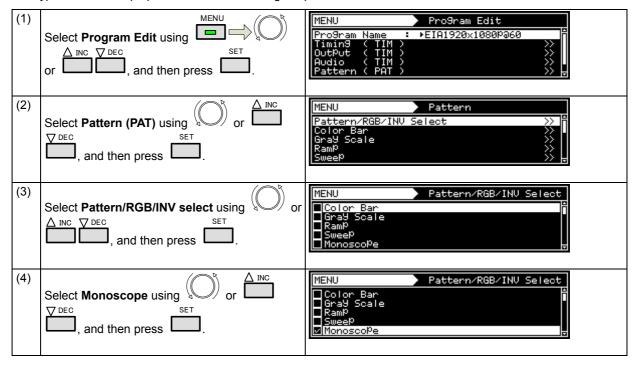
For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."



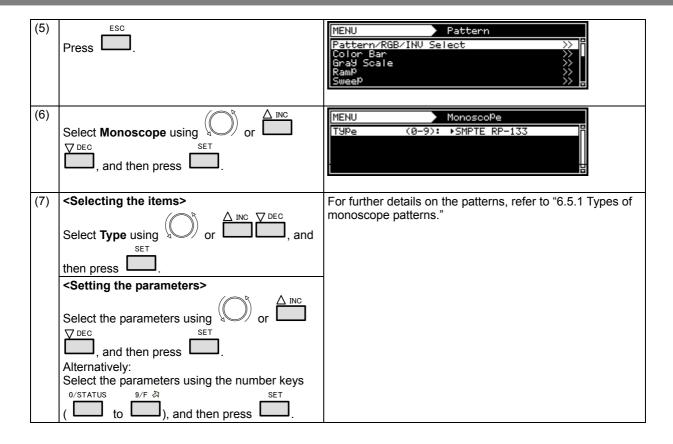
<sup>\*1</sup> APDC1 to APDC4 are patterns provided by the Advanced PDP Development Center Corporation (APDC), and are used to evaluate movie resolution. These patterns are an option. For further details, contact ASTRODESIGN sales representative or your dealer.

### 6.5.2 Monoscope pattern selection

The types of monoscope patterns can be set using the procedure below.



<sup>\*2</sup> This is the check pattern which is used only for SDI outputs. It is output only from SDI outputs.

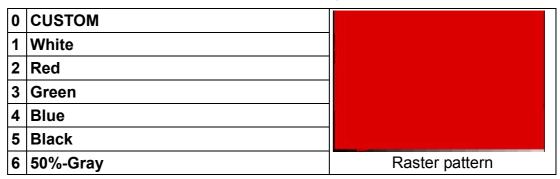


## 6.6 Raster patterns

### 6.6.1 Types of raster patterns

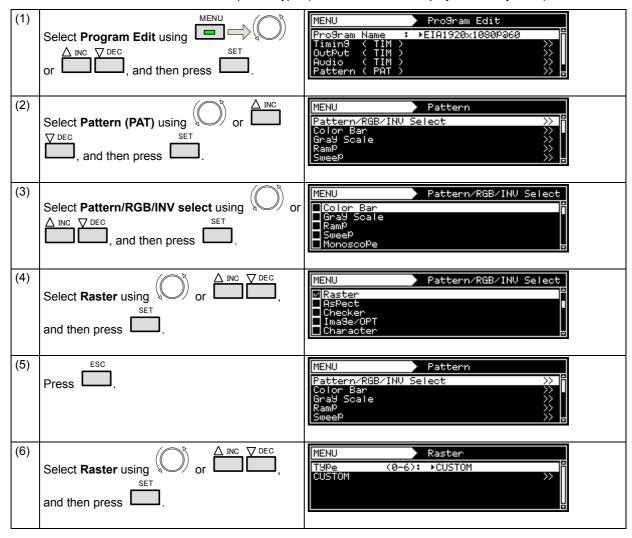
When raster has been selected using the pattern key, for instance, raster patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."



### 6.6.2 Raster pattern type settings and customizing

Described below is the procedure for selecting the types of raster patterns and for performing the settings when CUSTOM has been selected as the raster pattern type. (The rasters can be displayed with any level.)



(7)	<setting the="" type=""></setting>			on the types of patterns, refer to "6.6.1
	Select Type using or or DEC, and	Types of r	aster pat	terns."
	SET			
	then press .			
	<setting parameters="" the=""></setting>			
	Select the parameters using or or			
	DEC SET .			
	Alternatively:			
	Select the parameters using the number keys			
	(			
(8)	When <b>CUSTOM</b> has been selected as the <b>Type</b> setting			Bit Length
	<detailed customized="" of="" patterns="" settings=""></detailed>	MENU		CUSTOM : 8bit
	Select <b>CUSTOM</b> using or	R :	255 255 255	
	and then press .	Le		i i
	Select the items using or or DEC	R,G,B	The sett dependi	ting range for the above levels differs ng on the <b>color depth</b> .
	SET		8 BIT	0 to 255
	and then press .		9 BIT	0 to 511
	·		10 BIT	0 to 1023
	Alternatively		11 BIT	0 to 2047
	Select the parameters using the number keys		12 BIT	0 to 4095
	0/STATUS 9/F ऄ SET		13 BIT	0 to 8191
	( Loto Lot), and then press Lot.		14 BIT	0 to 16383
			15 BIT	0 to 32767
			16 BIT	0 to 65535

# 6.7 Aspect ratio patterns

## 6.7.1 Types of aspect ratio patterns

When aspect ratio has been selected using the pattern key, for instance, aspect ratio patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

0	Over Scan		·#. ·#.
	ΑI	FD	
	0	As the coded frame	
	1	4:3 (center)	hajin phine
	2	16:9 (center)	
	3	14:9 (center)	
	4	box 16:9 (top)	Over Coor
1	5	box 14:9 (top)	Over Scan
	6	box 13:7 (center)	
	7	box 2:1 (center)	
	8	box 11:5 (center)	
	9	box 12:5 (center)	$\mathcal{A}$
	Α	4:3 (14:9 center)	
	В	16:9 (14:9 center)	
	С	16:9 (4:3 center)	AFD pattern

## 6.7.2 Aspect ratio pattern type settings and customizing

Described below is the procedure for selecting the types of aspect ratio patterns and for performing the settings when **CUSTOM** has been selected as the aspect ratio pattern type.

(If **CUSTOM** is selected, the aspect ratio patterns can be displayed with any level.)

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Program Edit  Program Name : ►EIA1920×1080Pa60  Timing (TIM )
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep
(3)	Select Pattern/RGB/INV select using or or NC DEC, and then press.	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select <b>Aspect</b> using or or DEC, and then press	MENU Pattern/RGB/INV Select  Raster  AsPect Checker Image/OPT Character
(5)	Press Esc.	MENU Pattern  Pattern/RGB/INU Select  Color Bar Gray Scale Ramp Sweep  >>> 5
(6)	Select <b>Aspect</b> using or or or or or and then press.	MENU AsPect Type (0/1): ▶Over Scan AFD >>>
(7)	Select Type using or	For further details on the types of patterns, refer to "6.7.1 Types of aspect ratio patterns."

(8)	When <b>AFD</b> has been selected as the <b>Type</b>	MENU	AFD	: 8bit
	setting	AsPect (0/1):	<b>▶</b> 4:3	
	<pre><detailed afd="" of="" patterns="" settings=""></detailed></pre>	Type (0-C): ColorCircle R,G,B:	as the coded 255 , 255 ,	frame 255
	Select <b>AFD</b> using or $\Delta$ inc $\nabla$ DEC, and	Back R,G,B: Bar R,G,B:	128 , 128 , 128 , 128 ,	128 128 =
		For further details on t	the settings, refer	to <table afd<="" of="" td=""></table>
	then press	pattern setting items	> below.	
	Select the items using or or DEC,			
	and then press .			
	<inputting parameters="" the=""></inputting>			
	Select the parameters using $\bigcirc$ or $\bigcirc$ inc			
	DEC SET .			
	Alternatively:			
	Select the parameters using the number keys			
	0/STATUS 9/F ፟ឱ SET			
	( to ), and then press .			

# <Table of AFD pattern setting items>

(1)	Aspect (0/1)	The aspect ratio o	f the screen is set here.
		0 4:3	The images are displayed on the screen with a 4:3 aspect ratio.
		1 16:9	The images are displayed on the screen with a 16:9 aspect ratio.
(2)	Туре	For details on the patterns."	types of patterns, refer to "6.7.1 Types of aspect ratio
(3)	Color		Bit Length
		MENU AsPect (0/1) Type (0-C) ColorCircle R.G.E Back R.G.E (Bar) R.G.E	AFD : 8bit : 14:3 : as the coded frame : 255 , 255 , 255 : 128 , 128 , 128 : 128 , 128 , 128
		Port	Lovel
		Part	Level
		Gircle	In the Back
		Circle	Display the circle level.
			The R, G and B levels are displayed in sequence from the left.  The setting range for the above level differs
			depending on the color depth.  8 BIT 0 to 255
			9 BIT <b>0 to 511</b>
			10 BIT <b>0 to 1023</b>
			11 BIT <b>0 to 2047</b>
			12 BIT 0 to 4095
			13 BIT 0 to 8191
			14 BIT <b>0 to 16383</b> 15 BIT <b>0 to 32767</b>
			16 BIT <b>0 to 65535</b>
		Back	Set the background level.
		Por.	(Details are the same as for <b>Color Circle</b> .)  Set the bar level.
		Bar	(Details are the same as for <b>Color Circle</b> .)
<u> </u>			(Bottailo di o di o dallo do loi obioi olivie.)

# 6.8 Checkerboard patterns

### 6.8.1 Types of checkerboard patterns

When checkerboard has been selected using the pattern keys or other keys, any of the following types of patterns can be selected.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

			<u> </u>
0	DOT × DOT	Dot	
1	BLOCK × BLOCK	Block	
2	SubPixel	Sub-pixel	

# 6.8.2 Checkerboard pattern customizing

The types of checkerboard patterns are selected and their intervals are set using the procedure below.

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ►EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar Gray Scale  Ramp  Sweep
(3)	Select Pattern/RGB/INV select using or or, and then press	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select <b>Checker</b> using or	MENU Pattern/RGB/INV Select  Raster AsPect Checker Image/OPT Character
(5)	Press .	MENU Pattern  Pattern/RGB/INU Select  Color Bar Gray Scale  Ramp  Sweep
(6)	Select <b>Checker</b> using or	MENU Checker : 8bit  Type (0-2): ▶BLOCK×BLOCK  DOT×DOT H : 1  BLOCK×BLOCK H : 4  U : 4
(7)	Select Type using or ☐ → → → → → → → → → → → → → → → → → →	For details on the Type settings, refer to "6.8.1 Types of checkerboard patterns."  Depending on the Type setting, the setting items differ. Refer to the <table checkerboard="" items="" of="" setting="">.</table>

# <Table of checkerboard setting items>

(1)	DOTxDOT H/V	<valid <b="" only="" when="">DOT×DOT has been selected as the Type setting&gt;</valid>
		Set the number of horizontal (H) and vertical (V) pixels for one color.  Setting range: 1 to 8
(2)	BLOCKxBLOCK H/V	Setting range. 1 to 8 <valid as="" been="" block×block="" has="" only="" selected="" setting="" the="" type="" when=""></valid>
(2)	BEGGIABLOOK 11/4	Set the number of horizontal (H) and vertical (V) blocks.
		Setting range: 2 to 32
(3)	Sub Pixel H/V	<valid <b="" only="" when="">Sub Pixel has been selected as the Type setting&gt; Set the number of horizontal (H) sub pixels and number of vertical (V) pixels. Setting range: 0 to 3  * When <b>RGB 0%/100%</b> has been selected for Color Select, H=1 and V=1 will be set regardless of the settings. Setting example: H=2, V=3 2 2</valid>
		When User Color has been selected  : User Color 1 is used.  : User Color 2 is used.
(4)	SubPixel Offset H/V	<valid as="" been="" has="" only="" pixel="" selected="" setting="" sub="" the="" type="" when=""></valid>
		Set the number of sub pixels at the top left in the horizontal (H) direction and the number of pixels at the very top in the vertical (V) direction. (The <b>Sub Pixel H and V</b> settings subsequently apply.) <b>Setting range: 0 to 2</b> * When <b>RGB 0%/100%</b> has been selected for Color Select, H=0 and V=0 will be set regardless of the settings.
		Setting example: Sub Pixel H=3, V=1, SubPixel Offset H/V = refer to figures.
		Offset H=0 / V=0 V=2 (/H=0)
		∏ H=1 (N=0)
(5)	Color Select (0/1)	This sets the color and level.
		0 <b>RGB 0%/100%</b> 0% / 100%
		1 User Color The colors selected using User Color 1 and 2 are used.

(6)	User Color 1 R, G, B	When <b>RGB 0%/100%</b> has	Setting	ranges by color depth
		been selected for Color	8BIT	0 - 255
		Select, set any color and level to be indicated by	9BIT	0 - 511
		"0%."	10BIT	0 - 1023
		In the case of the <b>Sub</b>	11BIT	0 - 2047
		Pixel item, the same level	12BIT	0 - 4095
		as the RGB level is set.	13BIT	0 - 8191
			14BIT	0 - 16383
		The setting range differs depending on the color	15BIT	0 - 32767
		depth.	16BIT	0 - 65535
(7)	User Color 2 R, G, B	When <b>RGB 0%/100%</b> has been selected for Color Select, set any color and level to be indicated by "100%." In the case of the <b>Sub Pixel</b> item, the same level as the RGB level is set. The setting range is the same as for User Color 1.		

# 6.9 Image/OPT

# 6.9.1 Types of Image/OPT

When Image/OPT has been selected using the pattern key, for instance, optional and image patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

0	IMAGE	Image pattern	IMAGE  A still image registered as the default can be used or any still image can be registered by the user and used.
1	OPT-SAMPLE	Sample option pattern	OPT-SAMPLE
2	OPT-USER	User option pattern	Option pattern registered by the user Any test pattern can be described using a programming language.
3	MOVING-IMAGE (option)	Moving image pattern	Both the factory registered moving images and the user registered moving images can be used.

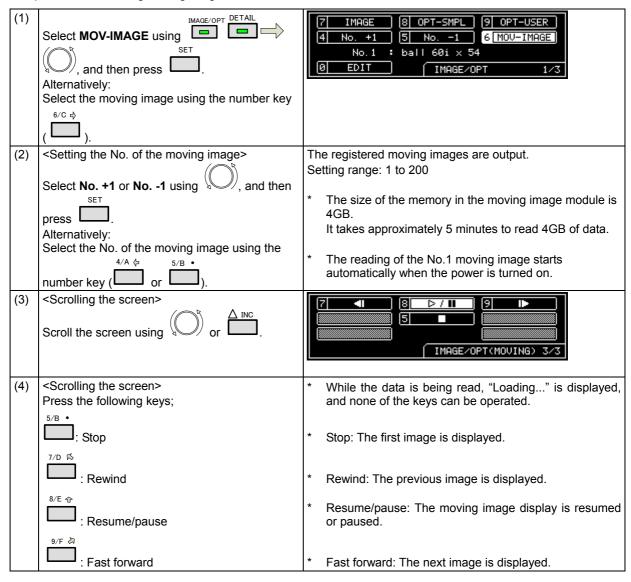
# 6.9.2 Option and image patterns setting

The procedure for setting optional and image patterns is described below.

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Program Name : Timing ( TIM ) OutPut ( TIM ) Audio ( TIM ) Pattern ( PAT )	Program Edit  ▶EIA1920×1080Pa60  >>> >>> >>> >>> >>> >>> >>> >>> >>>
(2)	Select Pattern (PAT) using or or or or or or and then press.	MENU Pattern/RGB/INU Color Bar Gray Scale Ramp Sweep	Pattern Select
(3)	Select Pattern/RGB/INV select using or or, and then press	MENU Color Bar Gray Scale Ramp Sweep Monoscope	Pattern/RGB/INV Select
(4)	Select Image/OPT using or	MENU Raster Aspect Checker ImaSe/OPT Character	Pattern/RGB/INV Select
(5)	Press .	MENU Pattern/RGB/INU Color Bar Gray Scale RamP SweeP	Pattern Select
(6)	Select Image/OPT using or	MENU No. TYPe (0-1 9 Marker ( 0P1 3D Pattern ( 0P1 * 3D Pattern can license has bee	No. 76 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3
(7)	<selecting items="" the=""> <math display="block">Select the item using \ SET \ or \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ </math></selecting>		
(8)	<setting no.="" the=""> Select the No. using or <math>\triangle</math> INC <math>\bigvee</math> DEC and then press</setting>	No.	Specific numbers are allocated to the option and image patterns. The number of the pattern to be displayed is set in <b>No.</b> Setting range: 1 to 200
	Select the Type using and then press or	Туре	<ul> <li>For further details on the patterns, refer to "6.9.1 Types of Image/OPT."</li> <li>When MOV-IMAGE is selected, refer to "6.9.3 Moving images settings (option)" for the detailed procedures.</li> </ul>

### 6.9.3 Moving images settings (option)

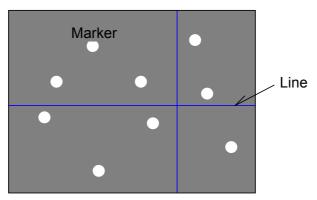
The procedure for setting moving images is described below.



- \* For details on the playback times of moving images, refer to section 11.1.9.
- \* The moving image data is registered using the SP-8870. For the recording format, conversion tools and other details, refer to the SP-8870 instruction manual.
- Use the CF card provided with the moving image module for moving image data registration.

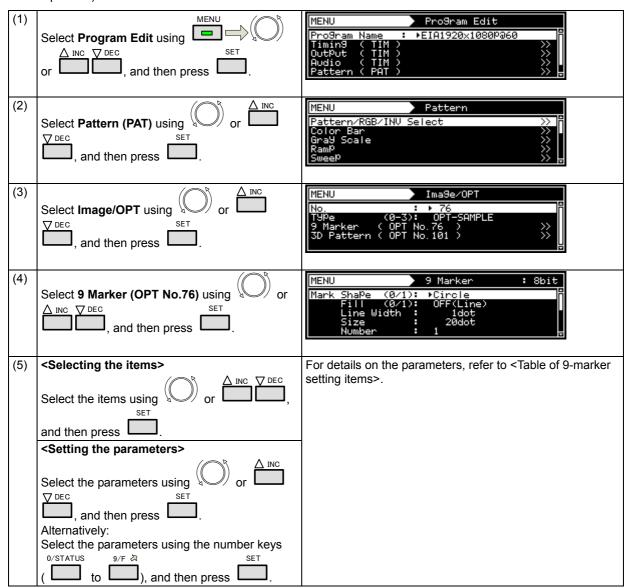
### 6.9.4 9-marker (OPT No.76) settings

Sample optional pattern No.76 is a pattern which displays up to nine markers and lines at the desired positions.



The setting procedure is given below.

For details on how to display the patterns, refer to "6.9.2 Option and image patterns setting." (Type = OPT-SAMPLE, No.76 specified)



# <Table of 9-marker setting items>

(1)	Mark Shape (0/2)	The shape of the markers is specified here.			here
( · )	. , ,	0	Circle	Circle	11010.
		1	Square	Square	
			Line	Straight li	ne
(2)	Mark Fill (0/1)				here (disabled when straight line has
		be	en selected as the Mark		<del></del>
		0	OFF (Line)	Markers r	not filled ○□
		1	ON	Markers f	
(3)	Mark Line Width	set	e width of the marker fra t here. tting range: 1 to 15 [dot]		Setting example: Mark Line Width = 1 Mark Size = 5
		*	When "ON" has been so the Mark Fill setting, the be 1 dot regardless of the	elected as e width will he setting.	Center 5 dots
(4)	Mark Size	The size of the markers is set here.  Setting range: 1 to 9999 [dot]  ->    1 dot			
(5)	Mark Number		e number of the markers	is set here	ð.
		Setting range: 0 to 9			
(6)	Mark Position H,V	The center coordinate of the marker position is set here.  Setting range: 0 to 4095 [dot]			
(7)	Mark Direction [H,V]		e direction of the straight	_	er is set here
(')	mark Bricotion [11,1]		H	Horizonta	
		1	V	Vertical lin	
(8)	Mark Color R,G,B	Th	e color of the markers is		
, ,		Th	e setting range differs de	epending or	n the color depth. * See below.
(9)	Line Mode (0-3)		nether to display the lines played are set here.	s and the sl	hape of the lines when they are
		0	None	No lines	
		1	V-Line	Vertical lin	
	2 <b>H-Line</b> Horizontal line				
		3	HV-Line	Cross cor	nsisting of one horizontal line and one
(10)	Line Width	Th	ı e width of the lines is set		17
			tting range: 1 to 15 [dot]		
(11)	Line Position H,V		e positions of the lines a		
			tting range: 0 to 4095 [do		
(12)	Line Color R,G,B	The color of the lines is set here.			
		ĮΙh	e setting range differs de	epending or	n the color depth. * See below.

### Color setting range.

Color Depth	Setting range
8BIT	0 - 255
9BIT	0 - 511
10BIT	0 - 1023
11BIT	0 - 2047
12BIT	0 - 4095
13BIT	0 - 8191
14BIT	0 - 16383
15BIT	0 - 32767
16BIT	0 - 65535

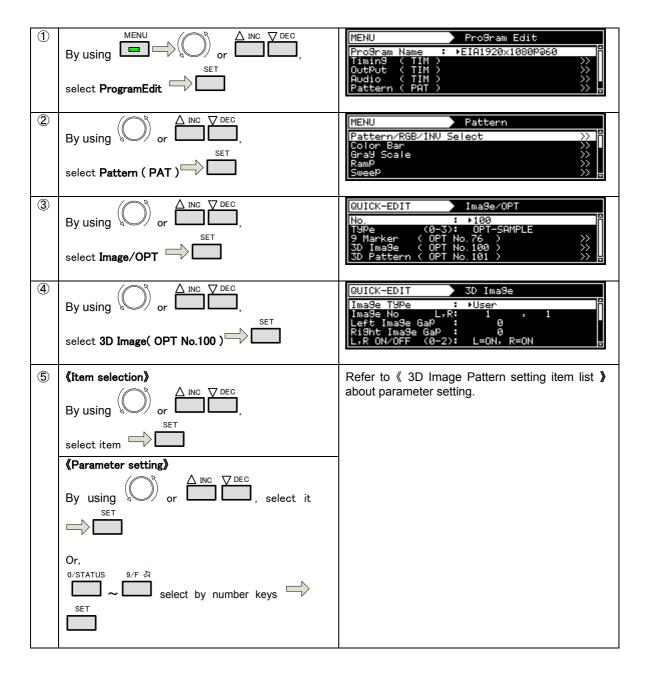
### 6.9.5 3D Image Pattern (OPT No.100) setting

Sample Option Pattern No.100 is 3D Image pattern.

This function is available only for VG-873 / 874.

Besides below setting, the setting in "HDMI Vendor Specific InfoFrame" is referred.

Refer to "6.9.2 Image / OPT setting" about pattern display. (designate Type=OPT-SAMPLE, No.100)



# 《 3D Image Pattern setting item list 》

	ge Pattern setting iter					
(1)	Image Type (0-2)		Select images.			
		0	User			
		1	MonoSc	оре		
		2	China			
(2)	Image No. L,R	1	Γype is set as 0(User), the image number (user image) selected here is			
		displayed.				
(3)	Left Image Gap	Each image is	shift to let	ft direction (	minus setting), and right direction (plus setting).	
(4)	Right Image Gap	Minus value is	input by	Shift + num	ber keys.	
(5)	L,R ON/OFF (0-2)	Right and Left	and Left picture can be set ON/OFF separately.			
		If you set OFF	et OFF, the color you set by <b>Off Color</b> is displayed.			
		* This menu i	s display	ed when u	sing Non Interlace timing, or 3D format except	
		frame packing	g.			
		0	L=ON	, R=ON		
		1	L=ON	, R=OFF		
		2	L=OFF	, R=ON		
(6)	L,R Field ON/OFF	Right and Left	field can	be set ON/0	OFF separately.	
		If you set OFF	, the colo	r you set by	Off Color is displayed in OFF field	
		* This menu is	s display	ed when u	sing Interlace timing and frame packing.	
		ON/OFF	0	ON	Output pattern	
			1	OFF	Pattern is not output.	
(7)	Level L,R	Level of Right	Level of Right and Left pattern can be changed. Setting range: 0 - 100 [%]			
(8)	Off Color R,G,B	Set a color of 0	OFF-fram	e or OFF-fie	eld when you set OFF in « L,R ON/OFF » or « L,R	
		Field ON/OFF » Setting range: 0 – 255				
(9)	Output Mode	Set pattern output mode				
		0				
				-	ferring to the setting of « HDMI 3D Structure »,	
					monly used.	
		1		Sequential		
			The left	and the rigi	nt pattern are displayed by every one frame.	
			In the S	imple anima	ation function in the "Action", set "H=1, V=2" in the	
			"Repeat	", this frame	e sequential becomes active.	
			Please	refer to 6.15	5.2 Simple Animation	
(10)	L,R Text (0-2)	Set display typ	e of « LE	FT » and «	RIGHT » character.	
		0	OFF	Not dis	played.	
		1	TOP	Display	on the top.	
		2	CENTER	Displa	y in center.	
(11)	BkBackLRText(0/1)	Set black back	ground a	round « <b>LE</b>	FT » and « RIGHT » character.	
		0	OFF	Does	not set black around « LEFT » and « RIGHT »	
			character.			
		1	ON Set black around « LEFT » and « RIGHT » character.			
(12)	Sub Sampling(0/1)	In case of « S	Side by S	ide » and «	Top & Bottom », set it either normal drawing or	
		thin-out drawin	ng.			
		0	OFF	Normal	drawing	
		1	ON Sub-sampling drawing			
(12)	Sub Sampling(0/1)	In case of « S thin-out drawin 0	Side by Sing.	Set bla	ck around « LEFT » and « RIGHT » character.  Top & Bottom », set it either normal drawing  drawing	

#### 《 3D Image pattern example 》

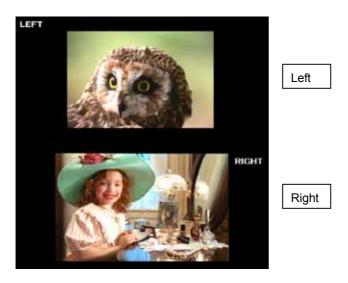
#### [0] User

Different images are displayed in each right and left picture.

You select Image No. to each picture.

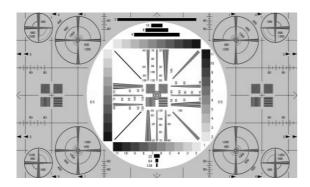
(Image should be saved beforehand.)

(Example 1080@FramePacking)



#### [1] MonoScope

Right and left picture display Monoscope (refer to section 6.5) pattern.



#### [2] China

Right and left picture display China Monoscope (refer to section 6.5) pattern.

### 6.9.6 Setting the 3D pattern (OPT No.101)

Sample optional pattern No.101 is a 3D pattern.

The requisite license is needed to display this pattern. For details, consult with an Astrodesign sales representative or your distributor.

The setting procedure is given below.

In addition to what is described below, the "HDMI ■ Vendor Specific InfoFrame" setting is referenced.

For details on how to display the pattern, refer to section "6.9.2 Option and image pattern setting." (Type=OPT-SAMPLE, No.101 specified)

(1)	Select <b>Program Edit</b> using MENU  or DEC, and then press SET.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080Pa60  TiminS ( TIM )
(2)	Select Pattern (PAT) using or or or SET, and then press	MENU Pattern  Pattern/RGB/INV Select  Color Bar  Gray Scale  Ramp  Sweep  Sweep
(3)	Select Image/OPT using or	MENU
(4)	Select 3D Pattern (OPT No.101) using or DEC and then press .	QUICK-EDIT 3D Pattern  TYPE (0-7): ►Color Bar V-1 L,R ON/OFF (0-2): L=ON , R=ON Level L,R: 100% , 100% Back Color R,G,B: 0 , 0 , 0 OutPut Mode (0/1): HDMI 3D Structure
(5)	Selecting the items> Select the items using or □□□, and then press □.	For details of the parameters, refer to <list 3d="" items="" of="" pattern="" setting="">.</list>
	Select the parameters using or Select the parameters using or SET or Select the parameters using or SET or	

# <List of 3D pattern setting items>

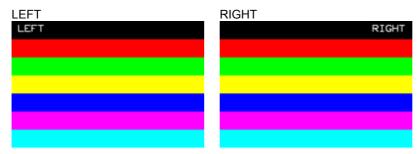
settings are referenced.			
settings are referenced.			
Slant Gray Bar			
Raster			
□×+0			
Window / Circle			
Cross Hatch (it will be referred to Cross Hatch setting).			
attern outputs.			
is output.			
f it is set off.			
sing Frame Packing mode,			
n is output n is not output			
set here.			
Set liele.			
Setting range: 0 - 100 [%]  The output color when Off has been selected as the L,R ON/OFF or L, R			
TIC L,IX ON/OTT OF L, IX			
nd output.			
the VRAM using one frame			
alternately by setting			
nple animation function.			
mation settings."			
ginal value or designated leter.			
Patterb and background is drawn by the value set by Left/ Right color in each right and left picture.			
character.			
GHT » character.			
GHT » character.			
it s that the state of the stat			

(10)	Sub Sampling (0/1)	In case of « Side by Side » and « Top & Bottom », set it either normal					
		drawing o		•		,	
		0	OFF		ormal drawing		
		1	ON	Th	nin-out drawin	ng	
(11)	Left/Right Color	If you set « <b>Use</b> » in <b>L, R Color</b> , set pattern color and back color in each left and right picture.					
		Coor(L) (R) R,G,B			et pattern col	or of each Left and Right picture.	
					etting range : tMode.)	0-65535 (max value will change by	
		Back cold	or L) (R )	S	et back color.		
		R,G,B			etting range : tMode.)	0-65535 (max value will change by	
(12)	V.Bar Width (1-16)	-				e width of vertical bar can be set.	
		Setting ra					
(13) Slant Angle (0-B) If you set « Slant Color here.				ar » or « Slan	t Gray Bar » in Type, set drawing angle		
		0		gree			
		1		egree			
		2		egree			
		3	45 0	egree	<u> </u>		
		A	150	degre	25		
		В		degre			
(14)	Checker	A checker		pattern is set when Checker DOT or Checker BLOCK has			
( ' ' '					oe setting.		
		section "6	.8 Check	erboar	d patterns.")	checkerboard pattern. (Refer to	
(15)	Window / Circle		et a parameter of Window / Circle that is selected in Type.				
		Type (0-3)			t kinds of pat		
				0	Window	Draw one Window.	
				1	Circle	Draw one Circle.	
				2	9 Window	Draw 9 Windows.	
		Deel Cire	In (0/4)	3	9 Circle	Draw 9 Circles.	
		Real Circ	Real Circle (0/1)		In Type, if you select Circle or 9 Circle, select if the circle shoul be true-circle or not.		
				0	OFF	Draw circle based on H and V size.	
				1	ON	Draw true-circle.	
		Size		Set the drawing size of the pattern.			
			1, V	Setting range : 0-100 [%]			
		LPosition		Set the drawing position of <b>Window / Circle</b> in each Left			
		RPosition	n	and Right picture. The setting value becomes the center of the pattern.			
					g range: 0-1	00 [%]	
		Position				sition of 9 Windows / 9 Circles.	
			-		The setting value is regarded as the blank area from the		
					of screen.	00.10/1	
(16)	Cross Hatab	Setting range : 0-100 [%]					
(16)	Cross Hatch	If you selet <b>Cross Hatch</b> in <b>Type</b> , set the details of Cross Hatch.  This is the same setting as the regular Cross Hatch pattern. (Refer to section "6.10.3 Cross Hatch.")					

#### <3D pattern details>

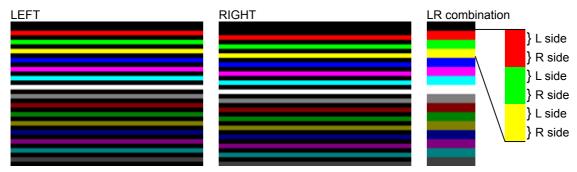
#### [0] Color Bar V-1

The same vertical color bars are output at the left and right. "LEFT" and "RIGHT" are displayed for left and right, respectively.



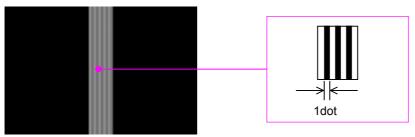
#### [1] Color Bar V-2

The vertical color bar shown in the figures below are output at the left and right.



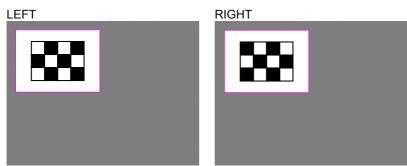
#### [2] Vertical Bar

Vertical bars each consisting of one dot are output. The pattern is the same for the left and right.



#### [3] Checker DOT

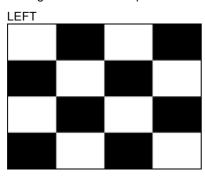
Checkerboard dot by dot patterns are output. The colors for the pattern on the left are reversed from the colors in the pattern on the right.

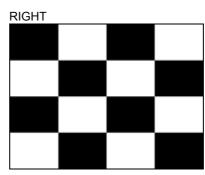


#### [4] Checker BLOCK

Checkerboard block by block patterns are output. The colors for the pattern on the left are reversed from the colors in the pattern on the right.

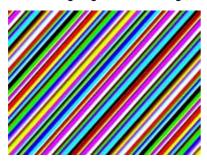
The figures below show patterns where the number of blocks is defined as H=4/V=4.

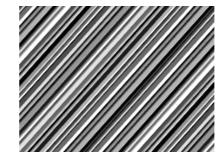




#### [5] Slant Color Bar, [6] Slant Gray Bar

The different width color bar / gray bar in the slant direction. The right and the left output the same picture. The drawing angle can be changed from 0 – 165 degrees.



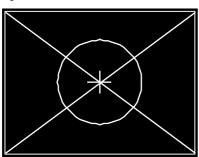


#### [7] Rastar

A raster is output. The pattern is the same for the left and right.

#### [8] □×+○

The  $\square$ ,  $\times$ , + and  $\bigcirc$  of  $\square$  ×  $\overline{ABC}$  pattern are superimposed and output. The pattern is the same for the left and right.



#### [9-1] Window [9-2] Circle

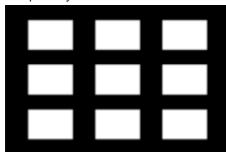
Window / circle pattern is displayed in the designated position. The left and right frame shows the same picture, but color can be set separately.

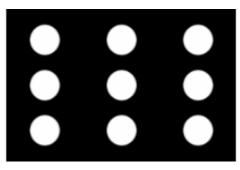




#### [9-3] 9 Window [9-4] 9 Corcle

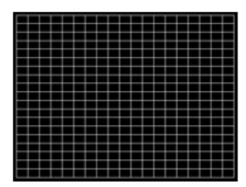
9 Window patterns are displayed in the designated position. The left and right frame shows the same picture, but color can be set separately.





#### [10] Cross Hatch

Cross hatch is output. The left and right frame shows the same picture, but color can be set separately.

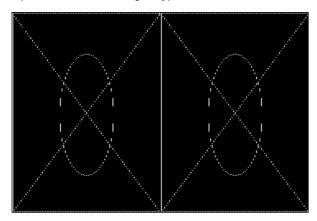


#### \* About Sub Sampling display

In case of Side-by-Side and Top & Bottom, by setting Sub-sampling = OFF, ODD and EVEN line is not taken out and line seems more clear.

Example : Side-by-Side

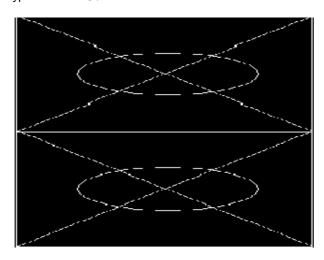
If Sub Sampling is ON, the pattern is displayed according to the setting in the « 3D Extention Data » in the Vender Specific Infoframe. (e.g. : Type =  $\square \times + \bigcirc$ )



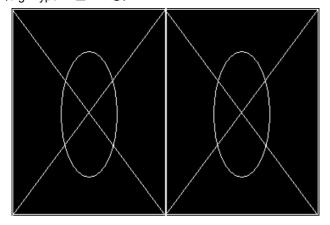
Example: Top & Bottom

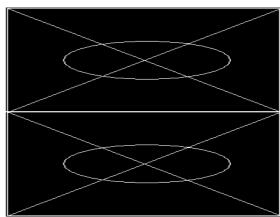
If Sub Samplingn is ON, only Odd lines are drawn in each left and right picture.

(e.g. : Type =  $\square \times + \bigcirc$ )



If Sub Sampling is OFF, all lines are drawn by the size of « Sub Sampling = ON », (e.g. Type =  $\square \times + O$ )



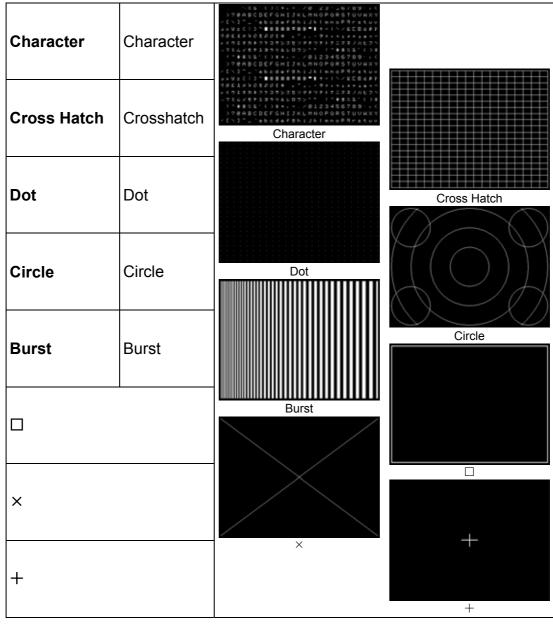


# 6.10 □ × ABC patterns

The following patterns are available as  $\square \times \boxed{\mathsf{ABC}}$  patterns. Select them using the  $\boxed{\square}$  key.

All the patterns can be superimposed onto one another, and displayed.

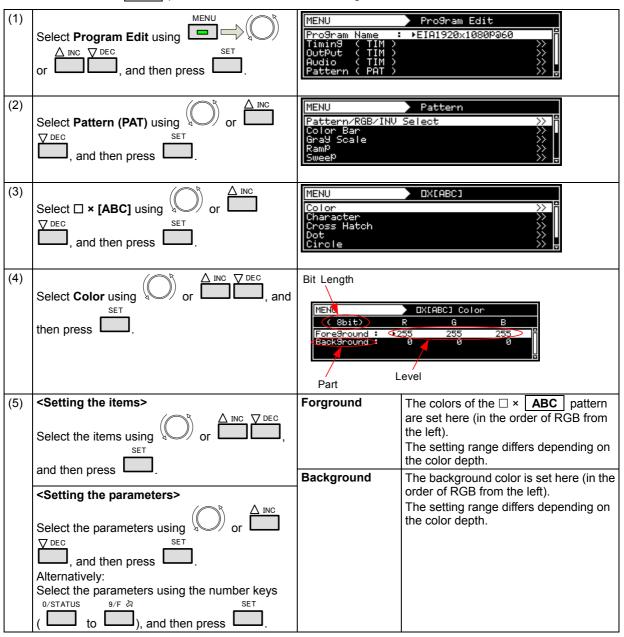
For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."



<sup>\*</sup> There are no items to be set in  $\square$ ,  $\times$  and + patterns.

### 6.10.1 Color settings

The colors of the  $\square \times \boxed{\mathsf{ABC}}$  patterns themselves and their background colors can be set.



#### Setting ranges by color depth

8 BIT	0 to 255
9 BIT	0 to 511
10 BIT	0 to 1023
11 BIT	0 to 2047
12 BIT	0 to 4095
13 BIT	0 to 8191
14 BIT	0 to 16383
15 BIT	0 to 32767
16 BIT	0 to 65535

# 6.10.2 Character patterns

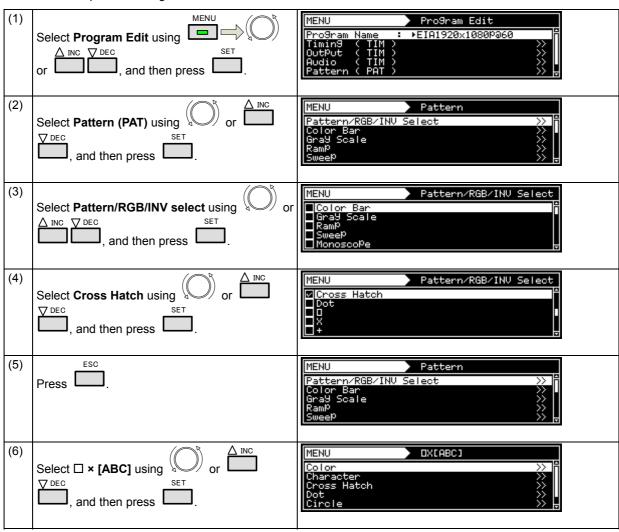
(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU ProSram Edit  ProSram Name : ▶EIA1920x1080P060  TiminS ( TIM )
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep   >>>   >>   >>>   >>   >>>   >>   >>>   >>>   >>>   >>   >>>   >>>   >>>   >>>   >>>   >>   >>   >>   >>   >>   >>   >>>   >>
(3)	Select Pattern/RGB/INV select using or or	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select <b>Character</b> using or $\triangle$ INC $\bigvee$ DEC or and then press .	MENU Pattern/RGB/INV Select  Raster AsPect Checker ImaSe/OPT □ Character □ Pattern/RGB/INV Select □ ImaSe/OPT □ Character
(5)	Press .	MENU Pattern  Pattern/RGB/INU Select  Color Bar Gray Scale  Ramp Sweep   >>>  >>>  >>>  >>>  >>>  >>>  >>>
(6)	Select □ × [ABC] using □ or □ o	MENU DX[ABC]  Color Character Cross Hatch Dot Circle
(7)	Select <b>Character</b> using or $\triangle$ INC $\bigcirc$ DEC or and then press $\square$ .	MENU Character  Format (0-2): ▶Character List Font (0-2): 5×7 Character Code : 48H Cell Size [dot]: H= 14 V= 18
(8)	Select the items using or □ □ OF	For further details, refer to <b>Table of character setting</b> items> below.
	Select the parameters using or Select the parameters using or Select the parameters using or Alternatively:  Select the parameters using the number keys O/STATUS O/S	

#### <Table of character setting items>

(1)	Format (0-2)	The	e format is selected here	
		0	Character List	
			All 1-Character	
		2	Corner&Center	
(2)	Font (0-2)	The	e font size is set here.	
		0	5 × 7	
		1	7 × 9	
		2	16 × 16	
(3)	Character Code	The	e character code is selec	ted here.
		Se	tting range: 20h to FFh	
		Wh	nen characters are select	ed directly, select ABC.
		Fo	r details on the operation	procedure, refer to "2.3 Setting the names."
(4)	Cell Size [dot]	The	e size of each character i	is set here.
		Н		Set the size of the character in the horizontal direction.
		V		Set the size of the character in the vertical direction.

### 6.10.3 Crosshatch patterns

The crosshatch pattern settings are described below.



(7)	Select Cross Hatch using SET or SET and then press	MENU Cross Hatch Format (0/1): ▶from Center InPut Mode (0/1): Number Of Lines Number Of Lines: H= 20 U= 20 Line Width [dot]: H= 1 U= 1
(8)	Select the items using or □□□, and then press □.	For further details, refer to <table crosshatch="" items="" of="" setting=""> below.</table>
	Select the parameters using or Select the parameters using or SET or Alternatively:  Select the parameters using the number keys  O/STATUS  O/STAT	

#### <Table of crosshatch setting items>

(1)	Format (0-2)	Th	e origin point of the patte	rn drawing is set here.		
		0	From Center	The pattern is drawn using the screen center as the origin point.		
		1	From Top-Left	The pattern is drawn using the top left of the screen as the origin point.		
(2)	InputMode (0/1)	Th	e input mode is selected	here.		
				This sets the number of lines to be displayed on the screen.		
		1	Interval (dot)	This sets the interval between the blocks.		
(3)-a	(3)-a When <b>Number of Lines</b>		The number of lines to be displayed on the screen is set here.			
	has been selected as the Input Mode setting	Nι	ımber Of Lines: H =	Set the number of lines in the horizontal direction.		
		Number Of Lines: V =		Set the number of lines in the vertical direction.		
(3)-b	When Interval (dot) has	Th	e interval between the blo	ocks is set here.		
	been selected as the Input Mode setting	Interval [dot]: H =		Set the number of lines in the horizontal direction.		
		Int	erval [dot): V =	Set the number of lines in the vertical direction.		
(4)	Line Width [dot]	The line width is set here.				
		Liı	ne Width [dot]: H =	Set the number of lines in the horizontal direction.		
		Liı	ne Width [dot]: V =	Set the number of lines in the vertical direction.		

# 6.10.4 Dot patterns

The dot pattern settings are described below.

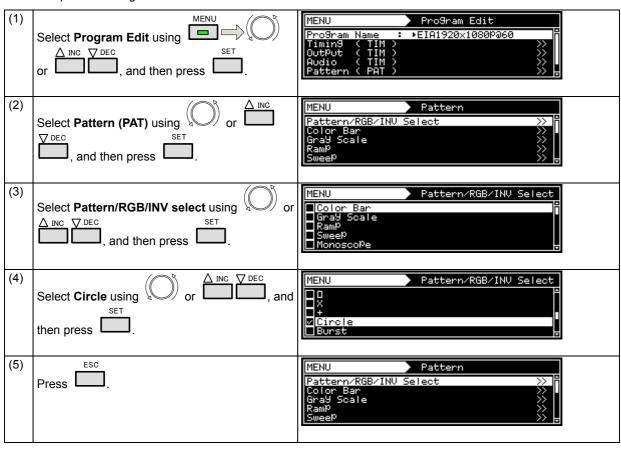
(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INU Select  Color Bar  Gray Scale  Ramp  Sweep  >>>
(3)	Select Pattern/RGB/INV select using or or, and then press	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select <b>Dot</b> using or or or or or, and then press.	MENU Pattern/RGB/INV Select  Cross Hatch Dot X +
(5)	Press .	MENU Pattern  Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep   Pattern  Pat
(6)	Select □ × [ABC] using or □ or	MENU DX[ABC]  Color Character Cross Hatch Dot Circle  DX[ABC]
(7)	Select <b>Dot</b> using or	MENU Dot  Format (0/1): ▶from Center InPut Mode (0/1): Number Of Lines Number Of Lines: H= 25 V= 25 Size [dot]: 1 ShaPe (0/1): Square
(8)	<setting items="" the=""> Select the items using SET or Select the item</setting>	For further details, refer to <b><table dot="" items="" of="" setting=""></table></b> below.
	and then press <setting parameters="" the=""></setting>	
	Select the parameters using or	
	Alternatively: Select the parameters using the number keys  O/STATUS  O/STAT	

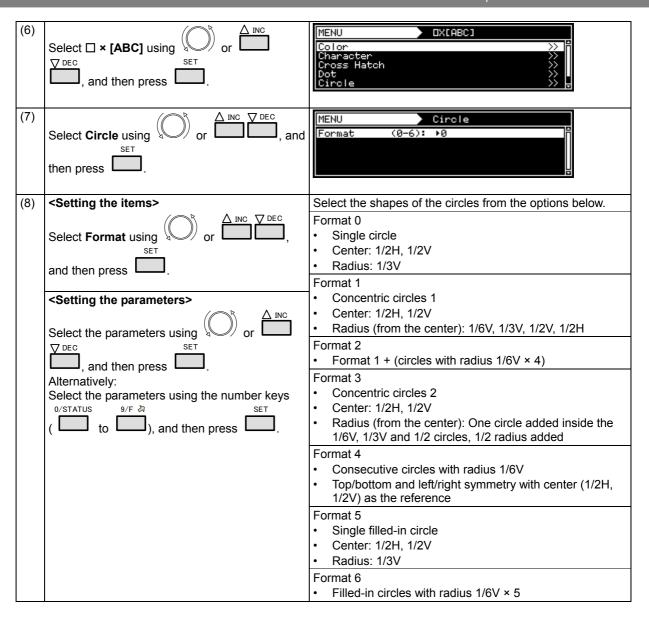
#### <Table of dot setting items>

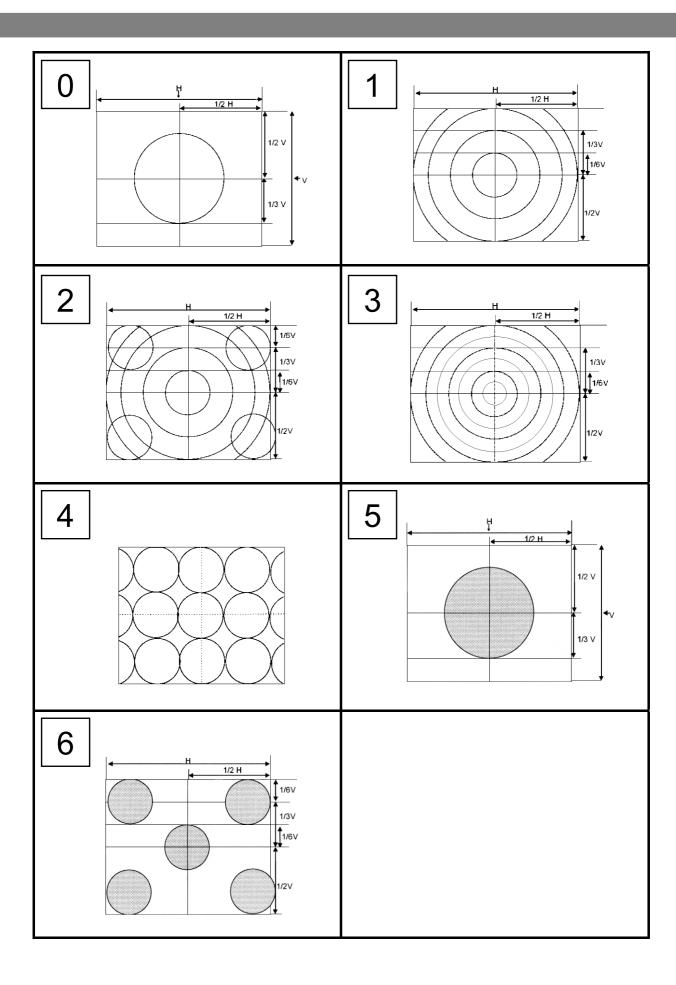
(1)	Format (0-2)	Th	e origin point of the patte	rn drawing is set here.
		0	From Center	The pattern is drawn using the screen center as the origin point.
		1	From Top-Left	The pattern is drawn using the top left of the screen as the origin point.
(2)	InputMode (0/1)	Th	e input mode is selected	here.
		0	Number Of Line	This sets the number of lines to be displayed on the screen.
			Interval (dot)	This sets the interval between the dots.
(3)-a When <b>Number of Lines</b>		The number of lines to be displayed on the screen is set here.		
	has been selected as the Input Mode setting	Number Of Lines: H =		Set the number of lines in the horizontal direction.
		Number Of Lines: V =		Set the number of lines in the vertical direction.
(4)-b	When Interval (dot) has	Th	e interval between the blo	ocks is set here.
	been selected as the Input Mode setting		erval [dot]: H =	Set the number of lines in the horizontal direction.
		Interval [dot): V =		Set the number of lines in the vertical direction.
(5)	Size [dot]	Th	e size is set here. Setting	range: 1 to 15 [Dot]
(6)	Shape	The shape is set here.		
		0	Circle	The dots are drawn in the form of circles.
			Square	The dots are drawn in the form of squares.

### 6.10.5 Circle patterns

The circle pattern settings are described below.

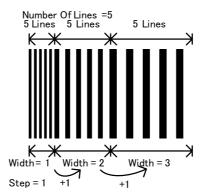




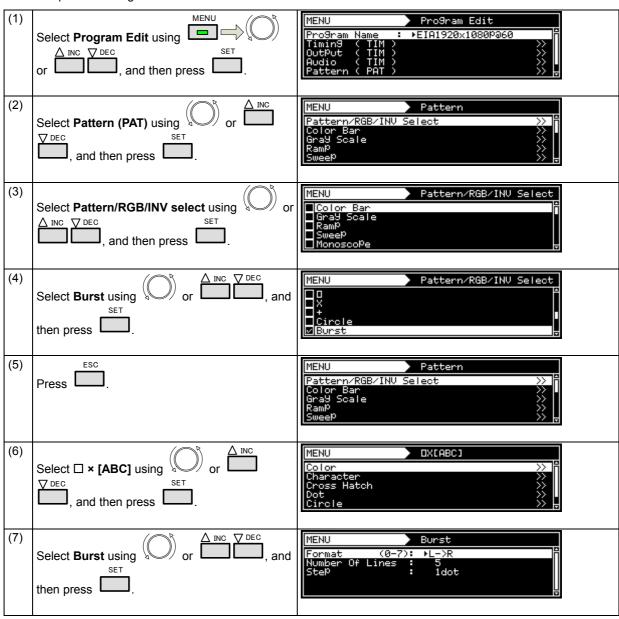


### 6.10.6 Burst patterns

In burst patterns, the line width increases gradually.



The burst pattern settings are described below.



(8)	<setting items="" the=""></setting>	For further details, refer to <table burst="" items="" of="" setting=""></table>
	Select the items using or	below.
	and then press .	
	<setting parameters="" the=""></setting>	
	Select the parameters using $\bigcirc$ or $\bigcirc$ inc	
	DEC , and then press	
	Alternatively:	
	Select the parameters using the number keys	
	( $0$ /STATUS $0$ /F $\otimes$ ), and then press $\square$ .	

### <Table of burst setting items>

(1)	Format (0-2)	Th	e origin point of the patte	ern drawing is set here.
			0 L->R The line width increases from left to rig	
		1	L<-R	The line width increases from right to left.
		2	L<-C->R	The line width increases from the center to the left edge and from the center to the right edge.
		3	L->C<-R	The line width increases from the left edge to the center and from the right edge to the center.
				The line width increases from top to bottom.
				The line width increases from bottom to top.
		6	T<-C->B	The line width increases from the center to the top edge and from the center to the bottom edge.
		7	T->C<-B	The line width increases from the top edge to the center and from the bottom edge to the center.
(2)	Number of Line	Aft an	The number of lines set here are repeatedly drawn with the same thickness. After the set number of lines have been drawn, the thickness is increased by an amount equivalent to the <b>Step setting</b> , and this is repeated. Setting range: 1 to 99	
(3)	Step		The step is set here. Setting range: 0 to 99 [dot]	

# 6.11 Window patterns

Mono-color rectangles can be displayed as the window patterns.

The window patterns can also be used to check moving images using the action function described later (refer to the action settings).

# 6.11.1 Types of window patterns

When window has been selected using the pattern key, for instance, window patterns can be selected from among the types listed below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."

0	1 Window	1 window displayed	
		, ,	
1	4 Window	4 windows displayed	
2	9 Window	9 windows displayed	
3	16 Window	16 windows displayed	
4	25 Window	25 windows displayed	
5	64 Window	64 windows displayed	
6	3 Window In V Row	3 windows in a vertical row displayed	
7	3 Window In H Row	3 windows in a horizontal row displayed	
8	User Pos Center	Window displayed at the position of the user's choice  * The coordinates of the window center are specified as the origin point of the display.	Position-1:H Position-1:V SizeH
9	User Pos Corner	Window displayed at the position of the user's choice  * The coordinates of the top left of the window are specified as the origin point of the display.	Position-1:H SizeH Position-1:V Size:V

# 6.11.2 Window pattern settings

The types of window patterns can be selected using the procedure below.

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Program Edit  Program Name : ►EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select Pattern (PAT) using or	MENU Pattern  Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep  >>
(3)	Select Pattern/RGB/INV select using or or, and then press	MENU Pattern/RGB/INV Select  Color Bar Gray Scale Ramp Sweep Monoscope
(4)	Select <b>Window</b> using or	MENU Pattern/RGB/INV Select  ☑ Window □ Cursor □ Name/List ☑ R/Pr ☑ G/V
(5)	Press Esc.	MENU Pattern  Pattern/RGB/INV Select  Color Bar  Gray Scale  Ramp  Sweep
(6)	Select <b>Window</b> using or	MENU
(7)	Selecting the items>  Select the items using or □□□,  and then press □.  Setting the parameters>	For further details on the parameters, refer to <b>Table of window setting items</b> > below.
	Select the parameters using or SET, and then press .  Alternatively: Select the parameters using the number keys  O/STATUS  9/F &  ( to ), and then press .	

# <Table of window setting items>

(1)	Format (0-9)	The window display format is set here.			
		0	1 Window	One window is displayed at the center of the screen.	
		1	4 Window	The screen is divided into four areas, and each of the four windows is displayed in the center of its respective area.  The window size is set using the area divided into the four areas as 100%.	
		2	9 Window	The screen is divided into four areas, and each of the nine windows is displayed in the center of its respective area.	
		3	16 Window	The screen is divided into four areas, and each of the sixteen windows is displayed in the center of its respective area.	
		4	25 Window	The screen is divided into four areas, and each of the thirty-two windows is displayed in the center of its respective area.	
		5	64 Window	The screen is divided into four areas, and each of the sixty-four windows is displayed in the center of its respective area.	
		6	3 Window In V Row	The screen is divided vertically into three areas, and each of the three windows is displayed in the center of its respective area.	
		7	3 Window In H Row	The screen is divided horizontally into three areas, and each of the three windows is displayed in the center of its respective area.	
		8	User Pos Center	The window can be displayed at any position. The coordinates of the window center are specified as the origin point of the window display.  Position 1:H	
				Position 1:V SizeV	
		9	User Pos Corner	The window can be displayed at any position. The coordinates of the top left of the window are specified as the origin point of the window display.  Position-1:H SizeH	
				Positiom 1: V Size: V	
(2)	Input Mode	Th	The input mode for the window size and display position is specified here.		
		0	%	The size and position are set as a percentage of the entire screen.	
		1	dot	The size and position are set in 1-dot increments.	
(3)	Size	The window size is set here. The setting procedure differs depending on the <b>Input Mode setting</b> .			

(4)	Position-1 Position-2	<b>format 9 (User Pos Corner)</b> has been selected. Window is displayed at the reverse mirror positions	Position-1:H SizeH SizeH Position-1:H					
		Position-2:V Size:V  Position-2:H SizeH SizeH SizeH Position-2:H	Size: V  Position—2: V  Size: V					
		H = The window center position or horizon position is set here.	The state of the s					
		·	V = The window center position or vertical direction of its start					
(5)	Color R G B	The window colors and level are set here.  Bit Length  Setting ranges by color depth						
			8 BIT	0 to 255				
		MENU Window : 8bit	MENU Window : (8bit) 9 BIT 0 to 511					
		Format (0-9): >1 Window InPut Mode (0/1): %	InPut Mode (0/1): 2 IU BII   U to 10/23					
		Size [%]: H= 20.0 V= 20.0 Position-1 [%]: H= 0.0 V= 0.0	Size [½]: H= 20.0 V= 20.0   11 BIT 0 to 2047					
		12 BIT   0 to 4095						
		·	The setting range differs depending on the color					
		depth. 14 BIT <b>0 to 16383</b>						
			15 BIT <b>0 to 32767</b>					
			16 BIT	0 to 65535				

## 6.12 Cursor patterns

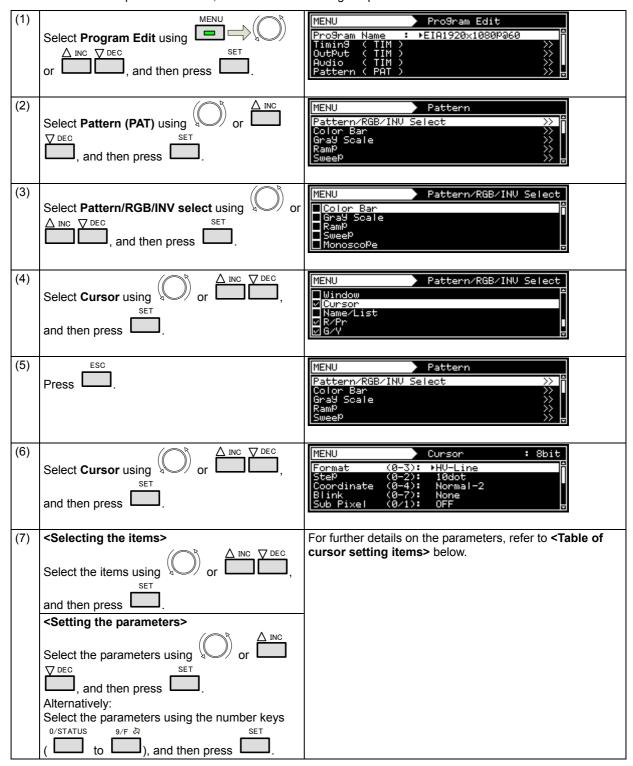
A cursor can be displayed on screens which show pattern displays.

The cursor can be moved to any point, and its position on the screen can be displayed.

#### 6.12.1 Cursor settings

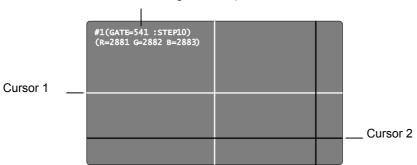
The cursor can be selected by following the operation procedure below.

For further details on pattern selection, refer to "2.1.3 Selecting the pattern data."



## <Example of what is displayed>

Coordinates of target cursor (#1 = cursor 1, #2 = cursor 2 when Cursor 2 is ON)



#### <Table of cursor setting items>

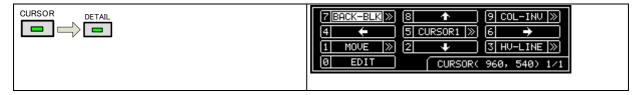
(1)	Format (0-3)	The shape	of the cursor is	set here.
		0	5 × 5 Cross	The cursor is displayed as a 5-pixel × 5-pixel cross.
		1	HV-Line	The cursor is displayed as a cross whose horizontal and vertical dimensions extend to the edges of the screen.
		2	V-Line	The cursor is displayed as a vertical line.
		3	Dot	The cursor is displayed as a 1-pixel × 1-pixel.
(2)	Step	-		ment when the cursor is to be moved is set here.
		0 to 2	1 dot/10 dot/100 dot	
(3)	Coordinate		nate display met	
		0	OFF	No coordinates are displayed.
		1	Normal-1	The horizontal and vertical coordinates and step are displayed in 1-pixel increments.
		2	Normal-2	The horizontal and vertical coordinates are displayed in sub-pixel increments, and the step is displayed in 1-pixel increments.
		3	Reverse-1	Normal-1 is inverted at the top and bottom, and displayed.
		4	Reverse-2	Normal-2 is inverted at the top and bottom, and displayed.
(4)	Blink	Whether th	e cursor is to bli	nk and the blink interval are set here.

		0	None	The cursor does not blir	nk.	
		1 to 7	1 V /2 V /4 V /8 V /16 V /32 V /64 V	The cursor blinks for ea period) to 64 V and ther repeated.		
(5)	Sub Pixel	increments	e cursor is to be is set here. in sub-pixel incre	moved in 1-pixel increme	ents or su	ıb-pixel
		Right			В	Left
		0	OFF	The cursor is moved in		
		1	ON	The cursor is moved in		
(6)	Overlay	Whether the the background	e cursor is to be ound color set us	displayed on top of the to sing the Color Back R G E	est patter 3 item is s	n or on top of set here.
		0	OFF	The cursor is displayed color set by the Color B		
		1	ON	The cursor is displayed	on top of	the test pattern.
(7)	Intersection	The shape	of the intersection	on is set here.		
		0	Normal	The intersection is filled	in as a c	ross.
		1	Space	The cursor is not displar intersection and sub-pix original background is d	els below	it, but the
(8)	Cursor 2	The display	of the second of	cursor (cursor 2) is set he	re.	
		0	OFF	The cursor is not display	yed.	
		1	ON	The cursor is displayed.	-	
(9)	Color Cursor 1 R G B		1 color and leve range differs de	el are set here. Epending on the color	Setting color d	ranges by epth
		depth.			8 BIT	0 to 255
					9 BIT	0 to 511
					10 BIT	0 to 1023
					11 BIT	0 to 2047
					12 BIT	0 to 4095
					13 BIT	0 to 8191
					14 BIT	0 to 16383
					15 BIT	0 to 32767
					16 BIT	0 to 65535
(10)	Color Cursor 2 R, G, B		2 color and leve procedure is the	el are set here. e same as for the <b>Color (</b>	Cursor 1	R G B item.
(11)	Color Back R G B	The backgr selected as background	ound color and I the <b>Overlay</b> ited to so that this set	evel are set here. However methods the test pattern ting is canceled. The color of	er, when n become	On has been s the

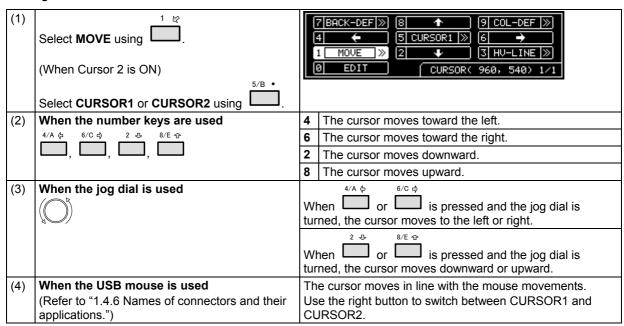
#### 6.12.2 Cursor operations

The cursor operations which can be performed include moving the cursor and changing the cursor level.

#### Operation screen display

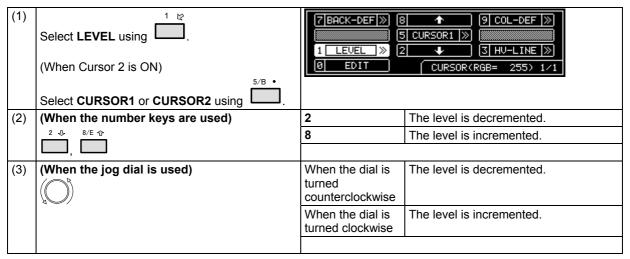


#### Moving the cursor



#### Changing the cursor level

These steps change the Color Cursor 1 R, G, B and Color Cursor 2 R, G, B settings.



#### Changing the background level

These steps change the  ${\bf Color\ Back\ R,\ G,\ B}$  settings.

(1)	Select <b>LEVEL-BK</b> using	7 BACK-DEF >> 8 1 LEVEL-BK >> 2 0 EDIT	CURSOR1 >>
(2)	(When the number keys are used)	2	The level is decremented.
	2 & 8/E &	8	The level is incremented.
(3)	(When the jog dial is used)	When the dial is turned counter-clockwise	The level is decremented.
		When the dial is turned clockwise	The level is incremented.

#### Changing the cursor shape

These steps change the **Format** setting.

3 20	5x5CROSS	Cross consisting of 5 × 5 pixels
	HV-LINE	Cross which covers the entire screen
	V-LINE	Vertical line
	DOT	1 × 1 pixel

#### Changing the background color

7/D ß	BACK-DEF	Color Back R, G, B settings
	BACK-W	White
	BACK-R	Red
	BACK-G	Green
	BACK-B	Blue
	BACK-BLK	Black

#### Inverting the cursor color

9/F &	COL-DEF	Normal
	COL-INV	Inverted

## 6.13 Name/List

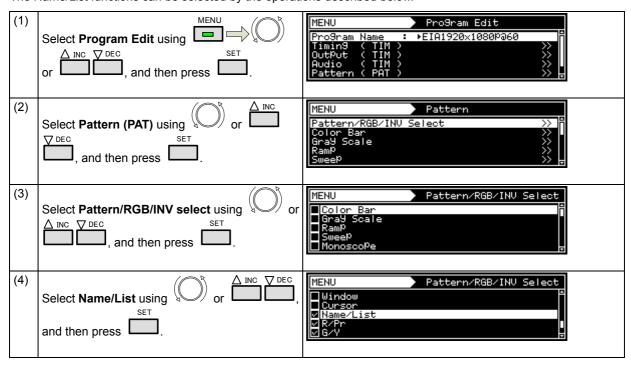
Information such as the setting data and execution results as well as the images (number of colors restricted) are contained in the Name/List patterns, and they can be superimposed onto other patterns.

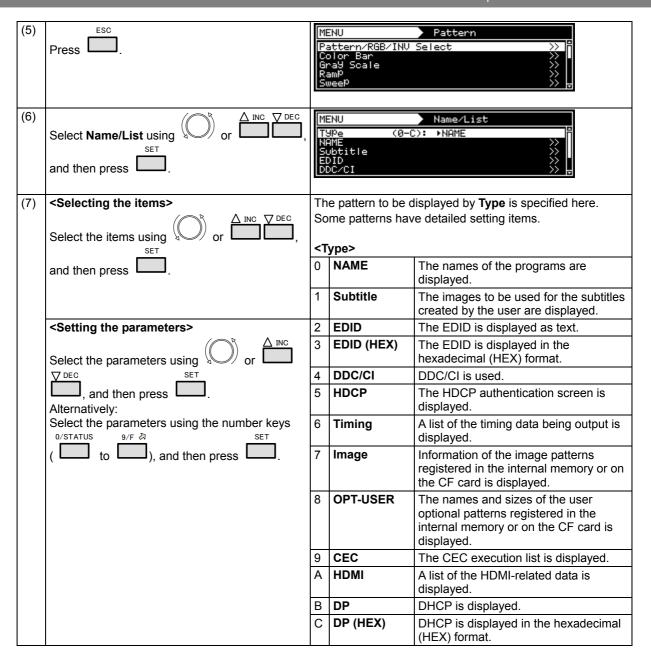
#### <Name/List types>

NAME/LIST	NAME	Program name display	For details, refer to "6.13.2 Name."
	EDID	EDID setting data display	For further details, refer to "6.13.3 EDID."
	DDC/CI	DDC-CI setting data display	For further details, refer to "6.13.4 DDC/CI."
	HDCP	HDCP authentication screen display.	For further details, refer to 6.13.5 HDCP (High-bandwidth Digital Content Protection)."
	CEC	CEC setting data display	For further details, refer to "4.2.4 CEC function."
	HDMI	HMDI setting data display	For further details, refer to "6.13.6 HDMI list."
	TIMING	Timing data (parameter, etc.) display	For further details, refer to "6.13.7 Timing data list."
	IMAGE	Image pattern list display	For further details, refer to "6.13.8 Image pattern list."
	OPT-USER	User optional pattern list display	For further details, refer to "6.13.9 OPT-USER pattern list."
	DP	Display port setting information display (link rate, number of lanes, link training results, DPCD)	For details, refer to "4.12.3 Displaying the DisplayPort setting information."
	SUBTITLE	Display of images to be used for the subtitles created by the user	For details, refer to "6.13.10 Subtitle."
	HDMI ARC	Audio Return Channel data display	For details, refer to "4.2.11 Audio Return Channel."

## 6.13.1 Name/List display

The Name/List functions can be selected by the operations described below.





## 6.13.2 Name

The display method can be set when Name has been selected as the Type setting.

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU
(2)	Select Pattern (PAT) using or or or or or or and then press.	MENU Pattern  Pattern/RGB/INV Select  Color Bar Gray Scale  Ramp Sweep  >>>  Sweep
(3)	Select Pattern/RGB/INV select using or or, and then press	MENU Pattern/RGB/INV Select    Color Bar
(4)	Select Name/List using or	MENU Pattern/RGB/INV Select  Window Cursor Name/List  R/Pr  G/Y
(5)	Press .	MENU Pattern  Pattern/RGB/INV Select >>   Color Bar Gray Scale >>   Ramp >>   Sweep >>
(6)	Select Name/List using or	MENU Name/List  Type (0-C): ▶NAME  NAME  Subtitle  EDID  DDC/CI  Name/List
(7)	Select Name using or	MENU Name  Format (0-3): ▶Pro9ram Name Position (0-6): ToP-Left Font (0-2): 7×9  Overscan [%]: H= 10 V= 10  Pattern Name : Character List
(8)	Selecting the items> Select the items using or □□□□, and then press  Setting the parameters>	For further details, refer to <b>Table of name setting items</b> below.
	Select the parameters using or	

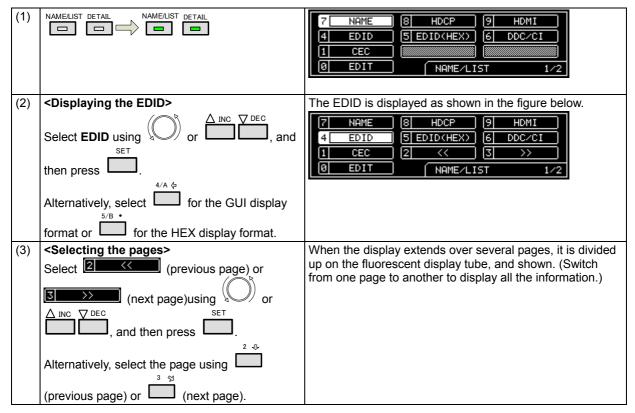
## <Table of name setting items>

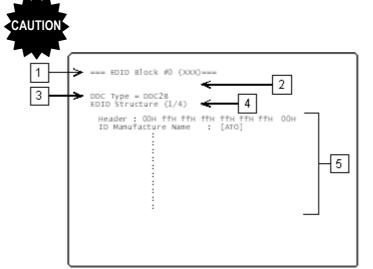
(1)	Format (0-3)	Th	e display contents of Nar	me are set here.	
		0	Program Name	The program names are displayed.	
		1	Pattern Name	The pattern names are displayed.	
		2	Program & Pattern Name	The program names and pattern names are displayed at the same time.	
		3	Program Name, Freq	The program names and video timing signal frequencies are displayed at the same time.	
(2)	Position	Th	e display position of the r	name is set here.	
		0	Center	The name is displayed at the screen center.	
		1	Top-Left	The name is displayed at the top left of the screen.	
		2	Bottom-Left	The name is displayed at the bottom left of the screen.	
		3	Top-Right	The name is displayed at the top right of the screen.	
		4	Bottom-Right	The name is displayed at the bottom right of the screen.	
		5	Top-Center	The name is displayed at the upper center of the screen.	
		6	Bottom Center	The name is displayed at the lower center of the screen.	
(3)	Font	Th	e font size is set here.		
		0	5 × 7	5 × 7 is set as the font size.	
		1	7 × 9	7 × 9 is set as the font size.	
		2	16 × 16	16 × 16 is set as the font size.	
<u>                                     </u>			e display position can be Iden to simulate an overs	adjusted in such a way that the display is not scanning monitor.	
		Н :	=	Set the horizontal overscanning ratio.	
V = Set the			Set the vertical overscanning ratio.		
(5)	Pattern Name		The pattern names are edited here. For details on the editing procedure, refer to "2.3 Setting the names."		

#### 6.13.3 EDID

The EDID of the connected display can be displayed on the screen.

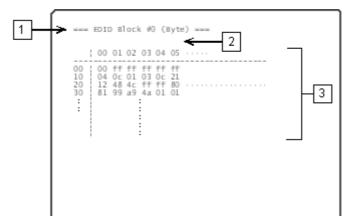
#### a) EDID display method





#### With the GUI display format

- 1) Block number of EDID
- 2) Error display when an error has occurred
- 3) DDC type4) EDID block now displayed
- 5) Content of EDID



#### With the HEX display format

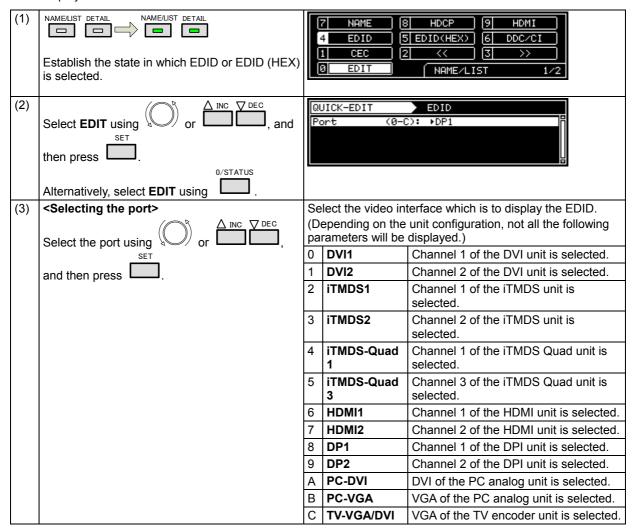
- 1) Block number of EDID
- 2) Error display when an error has occurred
- 3) Content of EDID



The maximum number of blocks read for EDID is 8 (one basic block + 7 expanded blocks). (When HDMI is selected)

#### b) Port selection method

The EDID is displayed by only one video interface at a time, and it is necessary to set the video interface which is to display the EDID.

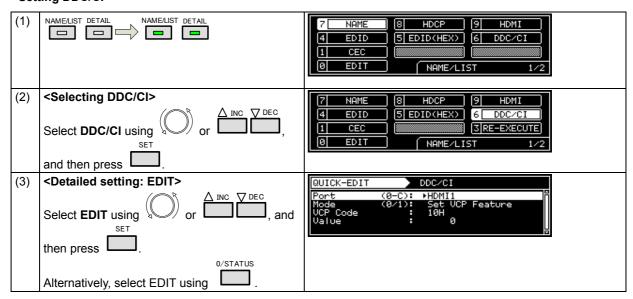


#### 6.13.4 DDC/CI

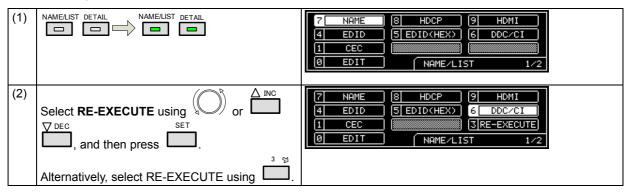
Using the DVI and VGA connectors, the DDC/CI commands can be sent and received, and shown on the display.

Two modes are available for DVI/CI: the **Get** (**Get VCP Feature**) mode in which the setting data is loaded from the information set in the display at the connection destination, and the **Set** (**Set VCP Feature**) mode in which the data of the user's choice is set.

#### <Setting DDC/CI>



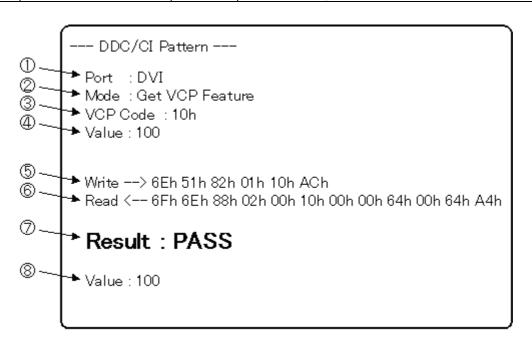
#### <Re-executing DDC/CI>



#### <Table of DDC/CI setting items>

(1)	Port (0-C)	The po	rt for using DDC/CI	is set here.
		0	DVI1	DDC/CI is used by the DVI1 port.
		1	DVI2	DDC/CI is used by the DVI2 port.
		2	iTMDS1	DDC/CI is used by channel 1 of the iTMDS unit.
		3	iTMDS2	DDC/CI is used by channel 2 of the iTMDS unit.
		4	iTMDS-Quad1	DDC/CI is used by channel 1 of the iTMDS Quad unit.
		5	iTMDS-Quad3	DDC/CI is used by channel 3 of the iTMDS Quad unit.
		6	HDMI1	DDC/CI is used by the HDMI1 port.
		7	HDMI2	DDC/CI is used by the HDMI2 port.
		8	DP1	DDC/CI is used by the DisplayPort1.
		9	DP2	DDC/CI is used by the DisplayPort2.
		Α	PC-DVI	DDC/CI is used by the DVI port of the PC analog unit.
		В	PC-VGA	DDC/CI is used by the VGA port of the PC analog unit.
		С	TV-VGA/DVI	DDC/CI is used by the DVI port of the TV encoder unit.

(2)	Mode (0/1)	The operat	ing mode is set here.		
		0	Get VCP Feature	The status of the connection destination is checked.	
		1	Set VCP Feature	The control commands are sent to the connection destination.	
(3)	VCP Code	00H-FFH	The VCP code is set.		
(4)	Value	0-65535	A value is set only when <b>Set VCP Feature</b> has been selected as the Mode setting.		



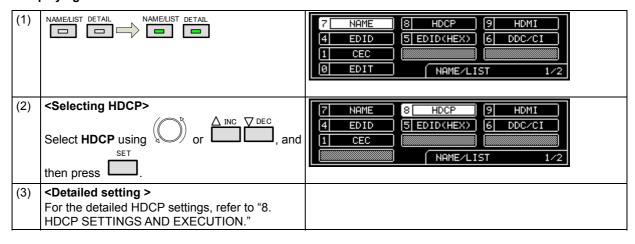
1	Port	Output port from which DDC/CI is transferred DVI or Dsub	Items to be set by the generator
2	Mode	DDC/CI transfer mode Get VCP Feature: The status of the connection destination is received. Set VCP Feature: Control commands are sent to the connection destination.	
3	VCP Code	Transfer command (set using the hexadecimal format)	
4	Value	(Displayed only when Set VCP Feature has been selected as the Mode setting.) Parameter value to be transferred from the generator to the connection destination	
5	Write	Data sent from the generator	Items which display the
6	Read	Data received by the generator	receiving or sending
7	Result	Transfer result PASS: Data was transferred successfully. NG: Transfer failed.	results
8	Value	(Displayed only when Get VCP Feature has been selected as the Mode setting.) Parameter value received by the VG-870 or 871 from the connection destination	

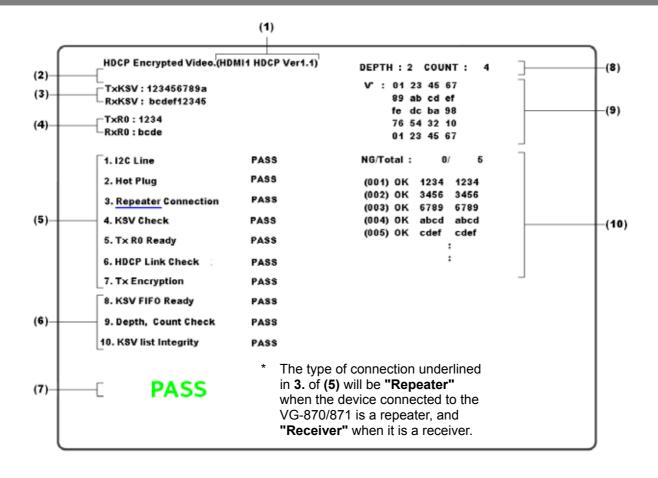
## 6.13.5 HDCP (High-bandwidth Digital Content Protection)

The HDCP authentication results can be shown on the display.

(For details on the HDCP settings and execution refer to "8. HDCP SETTINGS AND EXECUTION")

#### <Displaying the HDCP authentication screen>





Details of the information shown on the screen are given below.

#### <HDCP authentication screen display data>

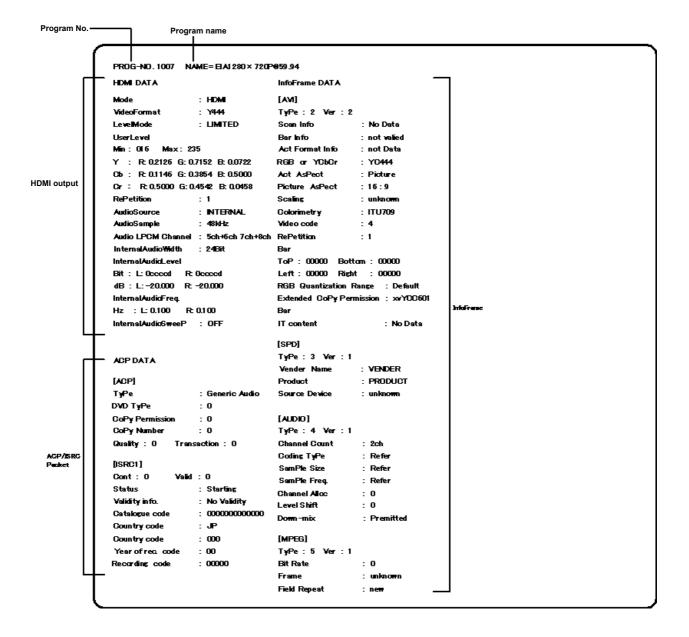
(1)	This indicates the port selected in <b>c) Display Select</b> of <b>"8.2.1 Execution procedure"</b> (When the HDCP authentication is successful, the HDCP version is displayed beside the port; when it has failed, an error message is displayed beside the port.)				
(2)	The EDID reading results are indicated here.  (The results are displayed only when AUTO has been selected as the HDMI or DVI setting in "4.2.2 HDMI setting procedure" or when "EDID Check" has been selected as the Version setting in "8.1 HDCP settings.")				
(3)	The Key Selection Vectors are displayed here among the HDCP keys. "TxKSV" is the key selection vector of the transmitter; "RxKSV" is the key selection vector of the receiver.				
(4)	The Synchronization Verification Values calculated by the initial authentication are displayed here. "TxR0" is the value calculated for the transmitter; "RxR0" is the value calculated for the receiver.				
(5)	The authentication status of the initial authentication is displayed here. An item with "PASS" denotes an				
(6)*	The authentication status of the second authentication for a repeater is displayed here.	item that has been successfully authenticated.			
(7)	If all the authentications have been carried out successfully, "PASS (green)" appears; otherwise, "NG (red)" appears.				
(8)*	The DEPTH (number of stages) and COUNT (total number of connections) of the devices connected beyond the receiver which in turn is connected to the VG-870B/871B/873/874 are shown here.				
(9)*	The value (V') shown here is for verifying the adequacy of the KSV list of the devices connected beyond of the receiver which in turn is connected to the VG-870B/871B/873/874.				
(10)	Shown here are the Synchronization Verification Values (Ri, Ri') for che Ri is the value calculated for the transmitter; Ri' is the value calculated when the transmitter and receiver values match. (If OK resulted from a	for the receiver. "OK" is displayed			

- \* This information is displayed only when the device connected to the VG-870B/871B/873/874 is a repeater.
- \* Items (6) and (8) to (10) are not displayed for DisplayPort output.

#### **6.13.6** HDMI list

The HDMI list display can be selected by pressing the NAME/LIST pattern key while the output from the HDMI connector is connected to the display. Information on the signals which are input to the display from the HDMI connector is shown on the display.

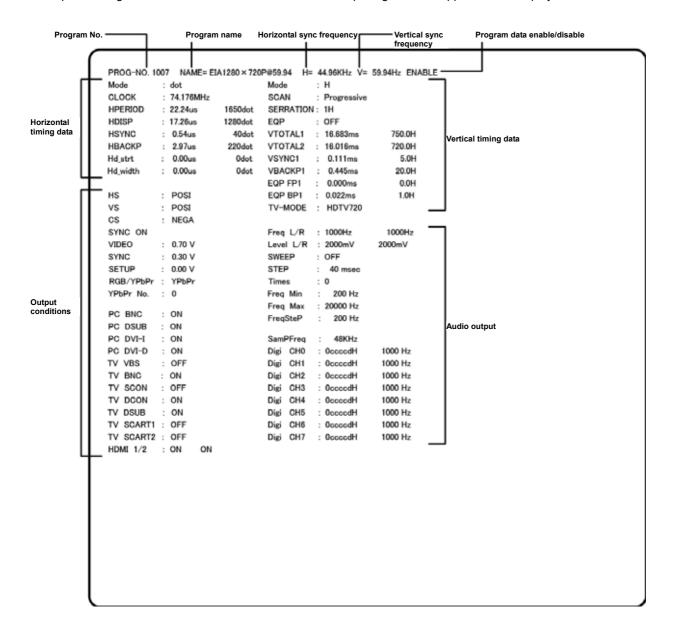
Example: HDMI list when EIA 1280 × 720 and 59.94 Hz output signals are supplied to the display



#### 6.13.7 Timing data list

The timing data list display can be selected by pressing the NAME/LIST pattern key while the generator output is connected to the display. The timing data (such as the parameters) of the signals which are input to the display from the output connector is shown on the display.

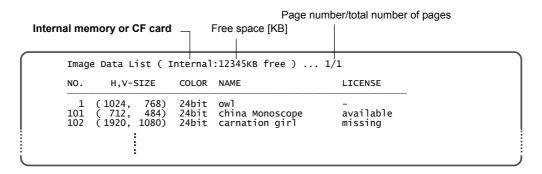
Example: Timing list when EIA 1280 × 720 and 59.94 Hz output signals are supplied to the display



#### 6.13.8 Image pattern list

When the image list display is selected by pressing the NAME/LIST key while the generator output is connected to the display, the information on the image patterns registered in the internal memory or on the CF card is shown on the display.

If the CF card has been inserted, the information on the CF card is displayed; otherwise, the information in the internal memory is displayed.



NO.: Image number

H, V-SIZE: Image size (width [dots], height [dots])

COLOR: Number of bits per dot

NAME: Image name

LICENSE: - No license required (standard)

availableThe license has been registered so the image list can be used.(option)missingThe license has not been registered so the image list cannot be used by this generator.(option)

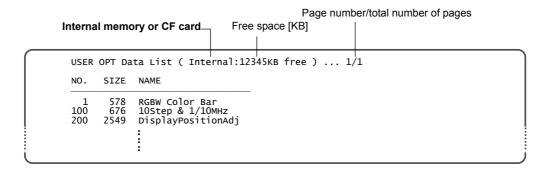
If the total number of pages exceeds 2, the pages can be selected using



#### 6.13.9 OPT-USER pattern list

When the OPT-USER pattern list display is selected by pressing the NAME/LIST key while the generator output is connected to the display, the names and sizes of the user optional patterns registered in the internal memory or on the CF card are shown on the display.

If the CF card has been inserted, the names and sizes of the patterns on the CF card are displayed; otherwise, the names and sizes of the patterns in the internal memory are displayed.



NO.: Pattern number
SIZE: Pattern size [bytes]
NAME: Pattern name

If the total number of pages exceeds 2, the pages can be selected using 2



#### 6.13.10 Subtitle

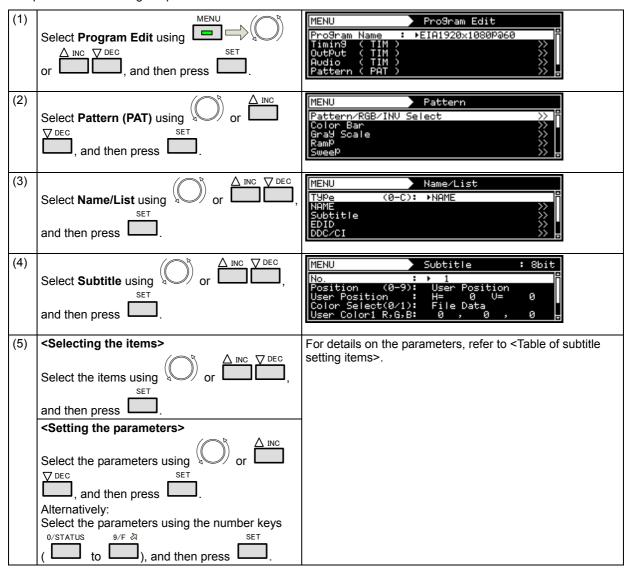
The image patterns (\*) created by the user are displayed.

Scrolling is possible using the action settings. (Refer to "7.5 Subtitle scrolling.")



Unlike the images in "6.9 Image/OPT," the images which can be displayed here are subject to restrictions on the number of colors and other aspects.
 They are created and registered using the Windows software program (SP-8870) which is provided with the VG generator. For details, refer to the operating instructions of the SP-8870.

The procedure for setting the parameters is outlined below.

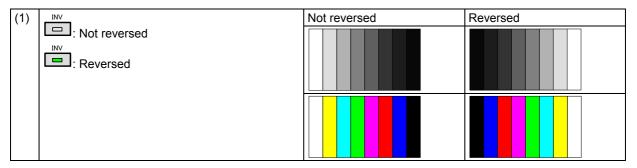


## <Table of subtitle setting items>

(1)	No.		e number of the image pa	attern to be displayed is	set here.		
(2)	Position (0-9)	_		ern is to be displayed is s	ot hara		
(2)	rosition (0-9)	0	The position where the pattern is to be displayed is set here.  0 <b>User Position</b> Setting position of User Position in (3)				
		1	Top-Left	Top left	POSITION	III (3)	
		<u> </u>	Middle-Left	'			
		2		Center left			
		3	Bottom-Left	Bottom left			
		4	Top-Center	Center top			
		5	Center	Center			
		6	Bottom-Center	Center bottom			
		7	Top-Right	Top right			
		8	Middle-Right	Center right			
		9	Bottom-Right	Bottom right			
(3)	User Position	he Th Po	e desired display position re. e setting takes effect whe esition setting. etting range: 0 to 9999				
(4)	Color Select (0/1)	The display colors are set here.					
		0	File Data	The colors of the image displayed in their original		lata are	
		1	User Color	The settings for User C	olor1-3 in	(5) are used.	
(5)	User Color1 to 3 R,G,B	Th	e desired display colors a e setting takes effect whe	en User Color has been	Color Depth	Setting range	
		se	selected as the Color Select setting.			0 - 255	
		l			9BIT	0 - 511	
			The setting range differs depending on the color			0 - 1023	
		depth.		0 - 2047			
					12BIT	0 - 4095	
					13BIT	0 - 8191	
					14BIT	0 - 16383	
					15BIT	0 - 32767	
					16BIT	0 - 65535	

## 6.14 Video black/white reversal

The video levels can be reversed.



## 6.15 Simple animation

Simple animation sequences can be displayed by drawing a multiple number of images in the drawing area and moving the display start coordinates. The display method is described here uses an example of a simple animation sequence consisting of  $640 \times 480$  images in nine frames.

#### 6.15.1 Creating and registering the images

(1) Create the images.

The  $640 \times 480$  images in nine frames are created as a  $1920 \times 1440$  image which is three frames wide and three frames high. (See Fig. 6-15-1.)

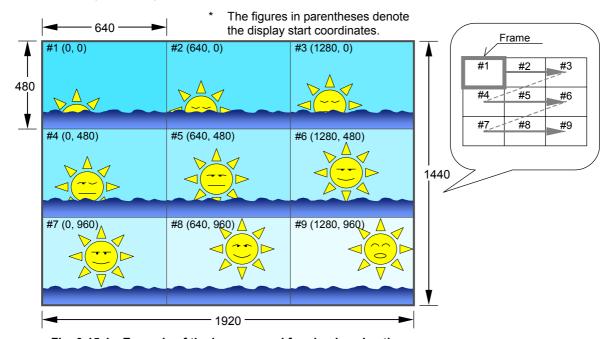


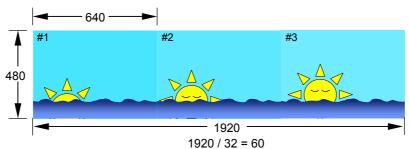
Fig. 6-15-1 Example of the images used for simple animation

- \* The display shows images #1 to #9 with a 640 × 480 frame size in the sequence of #1, #2 and so on up to #9.
- (2) Register the created images as image data (No.1 to 200) on the CF card using the Windows software (SP-8870) provided.



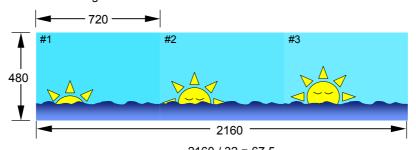
When creating simple animation images, create images whose resolution can be divided by 32 dots when pasting images side by side.

If the images have a resolution which cannot be divided by 32 dots, they will not be output properly.



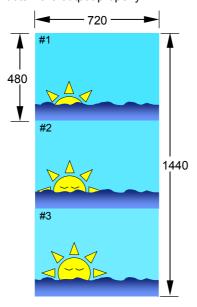
Since the resolution can be divided by 32, images with this resolution are output properly.

If the above image has a resolution of 720 × 480



2160 / 32 = 67.5
Since the resolution cannot be divided by 32, images with this resolution are not output properly.

Images which have been pasted one on top of the other—even images which cannot be divided by 32 dots—are output properly.



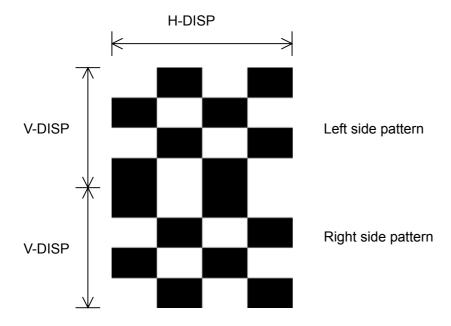
These images are not pasted side by side so they are output properly.

#### <Frame Sequential 3D patterns>

Patterns are drawn as shown in the figure below when **Frame Sequential** has been selected as **the Output Mode setting** among the 3D pattern (OPT No.101) settings in section 6.9.6.

This pattern is displayed at the left and right alternatively, and the **Frame Sequential** operation is performed by setting H=1 and V=2 as the values for the "Repeat" parameter among the simple animation settings in section 6.15.2.

The Interval1 parameter also takes effect at this time.



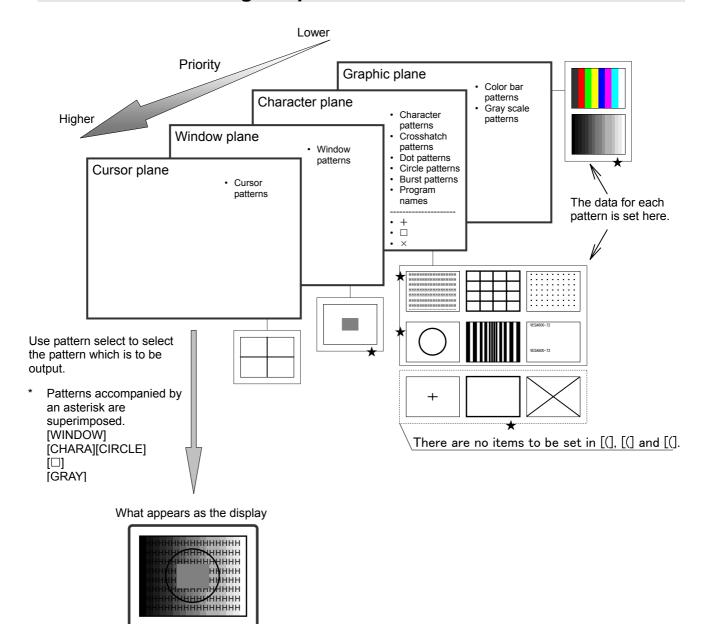
## 6.15.2 Simple animation settings

(1)	Insert the CF card containing the registered images.	
	* At this point, check that has not been pressed.	
(2)	Select the <b>program No.</b> using or	DIRECT-ALL No: 2 CF SAMPLETP  0002:SimPle Animation 0004:Color Bar 100/100-H  OUTPUT: [bit]
(3)	<setting action=""> Select G-SCROLL using   ACTION and B/E ↔  O/STATUS  and then select EDIT using</setting>	7 GC-SCROL   8 G-SCROLL   9 C-SCROLL   4 W-SCROLL   5 W-FLICKR   6 W-LEVEL   1 W-L. SEQ   2 M-BLUR
(4)	Select <b>Graphic Plane</b> using or or or DEC, and then press	QUICK-EDIT Action  Graphic Plane - SCROLL >> Character Plane - SCROLL >>
(5)	Selecting the items> Select the items using or □□□, and then press □.	QUICK-EDIT GraPhic Plane Scroll  ( ColorBar/GrayScale/Ramp/Ima9e/ > Scroll (0/1): FON Direction (0-8): Simple Animation Mode (0-4): User Interval 1: 100  Set only the items below.
	<setting parameters="" the=""></setting>	Scroll ON (1)
		Direction Simple Animation (8)
	Select the parameters using or or	Interval1 1 V to 255 V
	, and then press .	Repeat H = 3, V = 3  * This setting is for a 3 × 3 9-frame animation
	Alternatively:	* For details of the parameter settings, refer to "7.3
	Select the parameters using the number keys	Graphic plane scrolling actions."
	( $\stackrel{0/\text{STATUS}}{\square}$ to $\stackrel{9/\text{F}}{\square}$ ), and then press $\stackrel{\text{SET}}{\square}$ .	* Do not change any parameters not listed in the above table from their initial values.



# **ACTION SETTINGS**

## 7.1 Concerning the planes



## 7.2 Window actions

## 7.2.1 Scrolling

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."

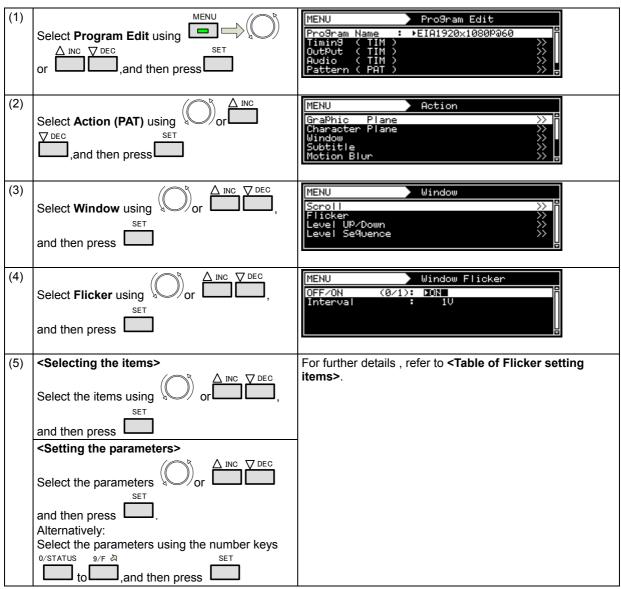
(1)	Select <b>Program Edit</b> using SET Or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select <b>Action (PAT)</b> using SET or	MENU Action  Graphic Plane  Character Plane  Window  Subtitle  Motion Blur   Action
(3)	Select <b>Window</b> using or	MENU Window  Scroll Flicker Level UP/Down Level Sequence  Scroll Flicker Flick
(4)	Select Scroll/Flicker using or	MENU Window Scroll  OFF/ON (0/1): DON  Direction (0-A): Ri9ht  Mode (0-4): User  Interval 1 : 1V  2 : 0V
(5)	Selecting the items> Select the items using or □□□□, and then press □.	For further details, refer to <table items="" of="" scroll="" setting="">.</table>
	Select the parameters using or ☐	

## <Table of scroll setting items>

(1)	OFF/ON (0/1)	Or	On or Off is set for window scrolling here.		
		0	OFF	Window scrolling is set to Off.	
		1	ON	Window scrolling is set to On.	
(2)	Direction (0-A)	Th	e direction of scrolling is	set here.	
		0	Left	The window is scrolled toward the left.	
		1	Right	The window is scrolled toward the right.	
		2	Up	The window is scrolled upward.	
		3	Down	The window is scrolled downward.	
		4	Left Up	The window is scrolled toward the top left.	
		5	Left Down	The window is scrolled toward the bottom left.	
		6	Right Up	The window is scrolled toward the top right.	
		7	Right Down	The window is scrolled toward the bottom right.	
		8	Left<->Right	The window is scrolled toward the left and right.	
		9	Up<->Down	The window is scrolled toward the top and bottom.	
		Α	Random	The window is scrolled at random.	
(3)	Mode (0-4)	Th	e interval (execution inter	rval) mode for window scrolling is set here.	
		0	User	The window is scrolled as per the <b>Interval 1-4 setting</b> .	
		1	60i->60i	The interval (execution interval) is set to 1 V.	
		2	24p-> 60i 2-3PullDown	The interval (execution interval) is set to 2 V and 3 V, and repeated.	
		3	25p->50i	The interval (execution interval) is set to 2 V.	
		4	30p->60i	The interval (execution interval) is set to 2 V.	
(4)	Interval (Interval 1)	The interval (execution interval) is set here. Setting range: 1 V to 255 V This setting takes effect only when <b>User</b> has been selected as the <b>Mode</b> setting.			
(5)	Step (Step 1)	Th		er interval (execution interval) is set here.	
		Н	Setting range: 1 dot to 2 This setting takes effect <b>Direction setting</b> .	55 dots only when <b>Left</b> or <b>Right</b> has been selected as the	
		V	Setting range: 1H to 258 This setting takes effect <b>Direction setting</b> .	5H only when <b>Up</b> or <b>Down</b> has been selected as the	
The fo	ollowing items are selected or	nly wh	en <b>User</b> has been select	red as the <b>Mode setting</b> .	
(6)	Interval 2 to 4	The interval (execution interval) is set here. Setting range: 0 V to 255 V When a setting other than "0" has been selected, the conditions which have been set are repeated in sequence starting with Interval 1.  Example: Interval 1 → Interval 2 → Interval 3 → Interval 1 → ••••			
(7)	Step 2 to 4			er interval (execution interval) is set here. orresponding to the <b>Interval 2-4</b> setting is set.	
		Н	Setting range: 0 dot to 2 This setting takes effect <b>Direction setting</b> .	55 dots only when <b>Left</b> or <b>Right</b> has been selected as the	
		V	Setting range: 0H to 255 This setting takes effect <b>Direction setting</b> .	5H only when <b>Up</b> or <b>Down</b> has been selected as the	

## 7.2.2 Flickering

For details on the action selection procedure, refer to "2.1.4 Selecting the actions"

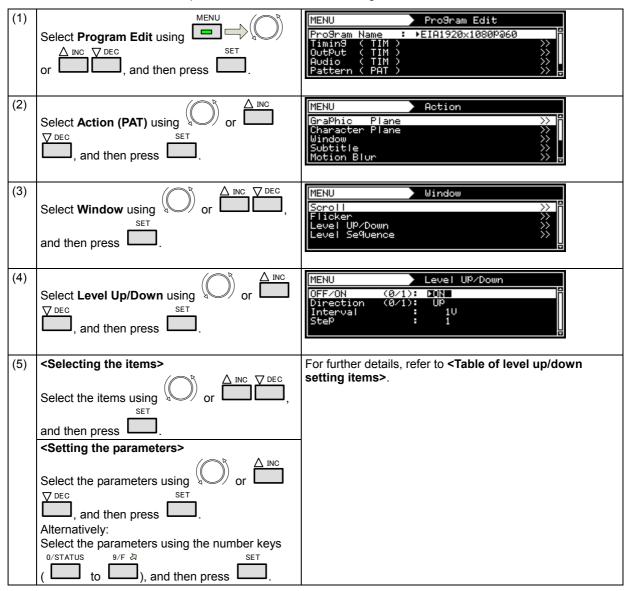


#### <Table of Flicker setting items>

(1)	OFF/ON (0/1)	On or Off is set for window flicker		
		0 <b>OFF</b>		Flicker is set to OFF
		1	ON	Flicker is set to ON
(2)	Interval	The interval (execution interval) is set here.		
		Setting range: 1 V to 255 V		

#### 7.2.3 Level up/down actions

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."



#### <Table of level up/down setting items>

(1)	OFF/ON (0/1)	On or Off is set for level up/down here.		
		0	OFF	Level up/down is set to Off.
		1	ON	Level up/down is set to On.
(2)	Direction (0/1)	Whether the level is to be increased or reduced is set here		e increased or reduced is set here.
		0	Up	The level is increased.
		1	Down	The level is reduced.
(3)	Interval	The interval (execution interval) is set here. Setting range: 1 V to 255 V		
(4)	Step	The amount of increase or reduction per interval (execution interval) is set here. Setting range: 1 to 255		

## 7.2.4 Level sequence action

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080P@60  Timin9 ( TIM )
(2)	Select <b>Action (PAT)</b> using or or or set of the set of	MENU Action  Graphic Plane  Character Plane  Window  Subtitle  Motion Blur   Action
(3)	Select <b>Window</b> using or or or or or or or or and then press	MENU Window  Scroll Flicker Level UP/Down Level Sequence
(4)	Select Level Sequence using or	MENU Level Sequence : 8bit  OFF/ON (0/1): DON  Number: 4  1: 255 255 255 8V  2: 240 255 240 24V  (R) (G) (B) (Time)
(5)	Selecting the items> Select the items using or □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □	For further details, refer to <table items="" level="" of="" sequence="" setting="">.</table>
	Select the parameters using or Select the parameters using or SET or Select the parameters using or SET or Select the parameters using the number keys O/STATUS O/ST	

## <Table of level sequence setting items>

(1)	OFF/ON (0/1)	On or Off is set for	level sequence here.
		0 <b>OFF</b>	Level sequence is set to Off.
		1 <b>ON</b>	Level sequence is set to On.
(2)	Number	The number of lev	el sequences is set here. Setting range: 1 to 16
		The sequences ar	e repeated in order for the number of times set here.
(3)	1 to 16	The RGB levels a	nd interval (execution interval) in each sequence are set
		here.	
		(R)	Set the R, G and B levels.
		(G)	8-bit setting range:0 to 255
		(B)	9-bit setting range:0 to 511
			10-bit setting range:0 to 1023
			11-bit setting range:0 to 2047
			12-bit setting range:0 to 4095
			13-bit setting range:0 to 8191
			14-bit setting range:0 to 16383
			15-bit setting range:0 to 32767
			16-bit setting range:0 to 65535
		(Time)	The interval (execution interval) is set here.
			Setting range: 1 V to 9999 V

# 7.3 Graphic plane scrolling actions

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Program Edit  Program Name : ▶EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select <b>Action (PAT)</b> using or or or or or or and then press.	MENU Action  Graphic Plane  Character Plane  Window  Subtitle  Motion Blur   Action
(3)	Select <b>Graphic Plane</b> using or or or or and then press.	MENU GraPhic Plane Scroll  < ColorBar/GraYScale/RamP/ImaSe/ > Scroll (0/1): FON  Direction (0-7): Left Mode (0-4): User Interval 1: 1V
(4)	<selecting items="" the=""> Select the items using or □□□, and then press □.</selecting>	For further details, refer to <table graphic="" items="" of="" plane="" setting="">.</table>
	Select the parameters using or Select the parameters using or Select the parameters using or Alternatively:  Select the parameters using the number keys O/STATUS O/S	

## <Table of graphic plane setting items>

(4)	lo "" (2/4)				
(1)	Scroll (0/1)	_	or Off is set for scrolling		
		0	OFF	Scrolling is set to Off.	
		1	ON	Scrolling is set to On.	
(2)	Direction (0-8)	Th	e direction of scrolling is		
		0	Left	The window is scrolled toward the left.	
		1	Right	The window is scrolled toward the right.	
		2	Up	The window is scrolled upward.	
		3	Down	The window is scrolled downward.	
		4	Left Up	The window is scrolled toward the top left.	
		5	Left Down	The window is scrolled toward the bottom left.	
		6	Right Up	The window is scrolled toward the top right.	
		7	Right Down	The window is scrolled toward the bottom right.	
		8	Simple Animation	Simple animation	
				For details, refer to "6.15.2 Simple animation settings."	
(3)	Mode (0-4)	Th	e interval (execution inter	val) mode for scrolling is set here.	
		0	User	The window is scrolled as per the <b>Interval 1-4 setting</b> .	
		1	60i->60i	The interval (execution interval) is set to 1 V.	
		2	24p->60i 2-3PullDown	The interval (execution interval) is set to 2 V and 3 V, and repeated.	
		3	25p->50i	The interval (execution interval) is set to 2 V.	
		4	30p->60i	The interval (execution interval) is set to 2 V.	
(4)	Interval (Interval 1)	Th	e interval (execution inter	rval) is set here. Setting range: 1 V to 255 V	
			is setting takes effect only tting.	y when <b>User</b> has been selected as the <b>Mode</b>	
(5)	Step (Step 1)	Th	e amount of movement p	er interval (execution interval) is set here.	
		H Setting range: 1 dot to 4095 dots This setting takes effect only when Left or Right has been selected Direction setting.			
		V	Setting range: 1H to 409 This setting takes effect Direction setting.	only when <b>Up</b> or <b>Down</b> has been selected as the	
The fo	ollowing items are selected or	ıly wh	nen <b>User</b> has been select	ed as the <b>Mode setting</b> .	
(6)	Interval 2 to 4	Th	e interval (execution inter	rval) is set here. Setting range: 0 V to 255 V	
		be	en set are repeated in se	0" has been selected, the conditions which have quence starting with Interval 1.  rval 2 → Interval 3 → Interval 1 → •••	
(7)	Step 2 to 4		•	er interval (execution interval) is set here.	
(')	2.0p 2 to 4			orresponding to the <b>Interval 2-4</b> setting is set.	
		Н	Setting range: 0 dot to 4		
			This setting takes effect	only when <b>Left</b> or <b>Right</b> has been selected as the	
		1/	Direction setting.	DELI.	
		V	Setting range: 0H to 409 This setting takes effect <b>Direction setting</b> .	only when <b>Up</b> or <b>Down</b> has been selected as the	
(8)	Repeat	Th		e width and height dimensions to be used for the	
(-)	- 1	simple animation is specified here.			
		For details, refer to "6.15.2 Simple animation settings."			
		Н	Setting range: 1 to 16		
				arranged horizontally is specified here.	
		٧	Setting range: 1 to 64	proposed vertically is appointed by	
<u> </u>			True number of images a	arranged vertically is specified here.	

# 7.4 Character plane scrolling actions

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."

(1)	Select <b>Program Edit</b> using SET or SET, and then press SET.	MENU
(2)	Select <b>Action (PAT)</b> using or	MENU Action  Graphic Plane  Character Plane  Window  Subtitle  Motion Blur
(3)	Select Character Plane using or	MENU Character Plane Scroll  (Chara/Cross/Dot/D/X/+/Circle/Burst/)  Scroll (0/1): FUN  Direction (0-7): Left  Mode (0-4): User  Interval 1: 1V
(4)	Selecting the items>  Select the items using or □□□,  and then press □.	For further details, refer to <b><table b="" character="" of="" plane<=""> <b>setting items&gt;</b>.</table></b>
	Select the parameters using or Select the parameters using or Select the parameters using or Select the parameters using the number keys olstatus of the parameters using the parameter	

#### <Table of character plane setting items>

(1)	Scroll (0/1)	Or	On or Off is set for scrolling here.		
		0	OFF	Scrolling is set to Off.	
		1	ON	Scrolling is set to On.	
(2)	Direction (0-7)	Th	e direction of scrolling is	set here.	
		0	Left	The window is scrolled toward the left.	
		1	Right	The window is scrolled toward the right.	
		2	Up	The window is scrolled upward.	
		3	Down	The window is scrolled downward.	
		4	Left Up	The window is scrolled toward the top left.	
		5	Left Down	The window is scrolled toward the bottom left.	
		6	Right Up	The window is scrolled toward the top right.	
		7	Right Down	The window is scrolled toward the bottom right.	
(3)	Mode (0-4)	Th	e interval (execution inte	rval) mode for scrolling is set here.	
		0	User	The window is scrolled as per the <b>Interval 1-4 setting</b> .	
		1	60i->60i	The interval (execution interval) is set to 1 V.	
		2	24p-> 60i 2-3PullDown	The interval (execution interval) is set to 2 V and 3 V, and repeated.	
		3	25p->50i	The interval (execution interval) is set to 2 V.	
		4	30p->60i	The interval (execution interval) is set to 2 V.	
(4)	Interval (Interval 1)	Th	The interval (execution interval) is set here. Setting range: 1 V to 255 V This setting takes effect only when <b>User</b> has been selected as the <b>Mode</b> setting.		
(5)	Step (Step 1)	Th	e amount of movement p	per interval (execution interval) is set here.	
		Н	Setting range: 1 dot to 4 This setting takes effect <b>Direction setting</b> .	095 dots only when <b>Left</b> or <b>Right</b> has been selected as the	
		V	Setting range: 1H to 409 This setting takes effect Direction setting.	95H only when <b>Up</b> or <b>Down</b> has been selected as the	
The fo	ollowing items are selected o			<u> </u>	
(6)	Interval 2 to 4	Wh	The interval (execution interval) is set here. Setting range: 0 V to 255 V When a setting other than "0" has been selected, the conditions which have been set are repeated in sequence starting with Interval 1.  Example: Interval 1 → Interval 2 → Interval 3 → Interval 1 → ••••		
(7)	Step 2 to 4		The amount of movement per interval (execution interval) is set here. The amount of movement corresponding to the <b>Interval 2-4</b> setting is set.		
		Н	H Setting range: 0 dot to 4095 dots This setting takes effect only when <b>Left</b> or <b>Right</b> has been selected as the <b>Direction setting</b> .		
		V			

## 7.5 Subtitle scrolling

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080Pa60  Timin9 ( TIM )
(2)	Select <b>Action (PAT)</b> using or	MENU Action  Graphic Plane  Character Plane  Window  Subtitle  Motion Blur   Action
(3)	Select <b>Subtitle</b> using or or or or or and then press	MENU Subtitle Scroll  Scroll (0/1): DON CONTROL CONTRO
(4)	Selecting the items>  Select the items using or □□□□,  and then press □.	For details, refer to the <table items="" of="" scroll="" setting="" subtitle="">.</table>
	Select the parameters using or Select the parameters using or SET, and then press Alternatively:  Select the parameters using the number keys  O/STATUS  O/S	

#### <Table of subtitle scroll setting items>

(1)	Scroll(0/1)	Scrolling is set to On or Off here.				
(')	OCION(O/1)	0	OFF	Scrolling is set to Off.		
		1	ON	Scrolling is set to On.		
(2)	Direction(0-7)					
(2) Bricetion(e-r)		0	The scrolling direction is set here.  0 Left The subtitles are scrolled to the left.			
		1	Right	The subtitles are scrolled to the right.		
		2	Up	The subtitles are scrolled to the right.  The subtitles are scrolled to the top.		
		3	Down	The subtitles are scrolled to the bottom.		
		4	Left Up	The subtitles are scrolled to the bottom.  The subtitles are scrolled to the top left.		
		5	Left Down	The subtitles are scrolled to the bottom left.		
		6	Right Up	The subtitles are scrolled to the bottom left.  The subtitles are scrolled to the top right.		
		7	Right Down	The subtitles are scrolled to the top right.  The subtitles are scrolled to the bottom right.		
(3)	Mode(0-4)		<u> </u>	ution interval) mode is specified here.		
(3)	Wode(0-4)	0	User	Scrolling is executed in compliance with the		
		U	Usei	Interval 1, 2, 3 or 4 setting.		
		1	60i->60i	1V serves as the interval (execution interval).		
		2	24p->60i 2-3PullDown	2V and 3V are repeated as the interval (execution interval).		
		3	25p->50i	2V serves as the interval (execution interval).		
		4	30p->60i	2V serves as the interval (execution interval).		
(4)	Interval(Interval 1)	Th	e interval (execution inter	val) is set here.		
		Th	tting range: 1 V to 255 V is item can be set only wl all other modes, a fixed v	hen <b>User</b> has been selected as the <b>Mode</b> setting.		
(5)	Step(Step 1)		The amount of movement per interval (execution interval) is set here.			
,	,		Setting range: 1 dot to 4 This item takes effect or <b>Direction</b> setting. Setting range: 1 H to 40	095 dots ally when <b>Left</b> or <b>Right</b> has been selected as the		
			This item takes effect or <b>Direction</b> setting.	nly when <b>Up</b> or <b>Down</b> has been selected as the		
The ite	ems listed below are set only	y when	User has been selected	as the <b>Mode</b> setting.		
(6)	Interval 2 - 4	The interval (execution interval) is set here.  Setting range: 0 V to 255 V  When a setting other than "0" has been set, the set conditions are repeated in sequence starting from Interval 1.  Example: Interval 1 → Interval 2 → Interval 3 → Interval 1 →				
(7)	Step 2 - 4			er interval (execution interval) is set here.		
		set	This is the amount of movement (step) which corresponds to the <b>Interval 2-4</b> settings.			
			H Setting range: 0 dot to 255 dots This item takes effect only when <b>Left</b> or <b>Right</b> has been selected as the <b>Direction</b> setting.			
		V	V Setting range: 0 H to 255 H This item takes effect only when <b>Up</b> or <b>Down</b> has been selected as the <b>Direction</b> setting.			

#### 7.6 0.25- and 0.125-dot scrolling actions (option)

0.25-dot and 0.125-dot scrolling can be set.

This function is optional. (The 0.25 dot and 0.125 dot are separate options.)

For further details, contact your dealer or an ASTRODESIGN sales representative.

#### Concerning 0.25- and 0.125-dot scrolling

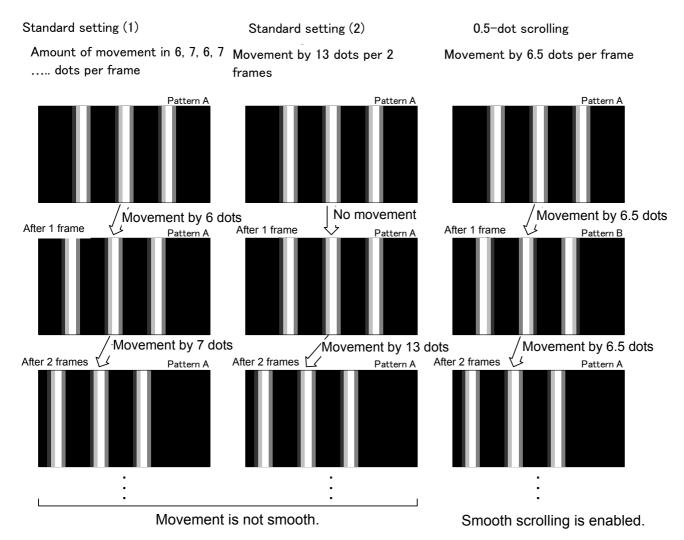
The setting increments used by the regular scrolling function are interval 1 V increments (frames for progressive scanning and fields for interlaced scanning), and 1-dot increments are used for the movement amounts.

In the case of the VG-870B or 871B, smoother scrolling can be achieved by providing four patterns with a 0.25-dot shift in between (or two patterns with a 0.5-dot shift and 8 patterns with a 0.125-dot shift).

#### <Example>

When scrolling horizontally for one round at a 1920 × 1080p @ 60p timing of approximately 5 seconds About 13 dots are required per 2 V and about 6.5 dots (\*1) per 1 V.

\*1: Amount of movement per 1 V =  $1920/(60*5) = 6.4 \approx 6.5 \text{ [dot]}$ 



Pattern A: Pattern serving as the reference

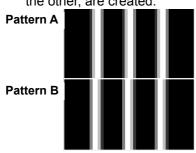
Pattern B: Pattern produced when pattern A is moved by 0.5 dot.



The 0.25-/0.125-dot scrolling function takes effect only for the image data created by the user.

#### With 0.5-dot scrolling

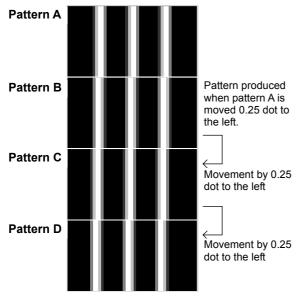
Two patterns, with one offset by 0.5 dot from the other, are created.



Pattern produced when pattern A is moved 0.5 dot to the left.

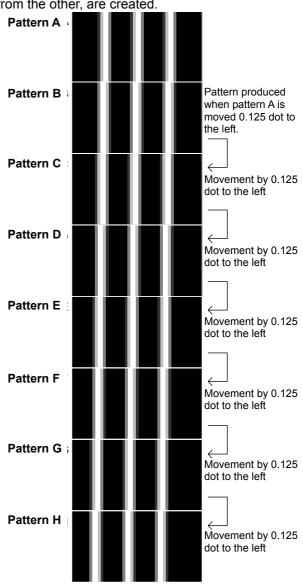
#### With 0.25-dot scrolling

Four patterns, each offset by 0.25 dot from the others, are created.



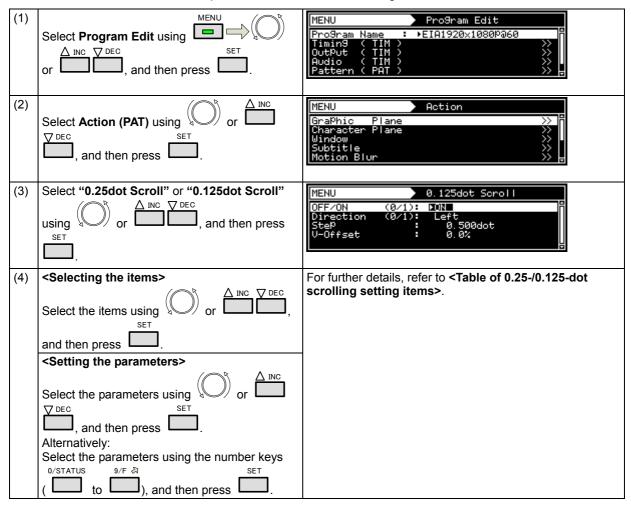
#### With 0.125-dot scrolling

Eight patterns, with one offset by 0.125 dot from the other, are created.



#### • 0.25-/0.125-dot scrolling settings

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."



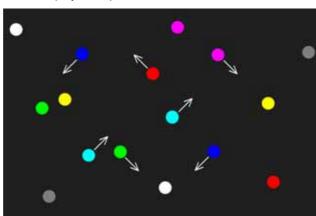
#### <Table of 0.25-dot and 0.125-dot scroll setting items>

(1)	OFF/ON (0/1)	On or Off for 0.25-/0.125-dot scrolling is set here.		
		0 0	OFF	0.25-/0.125-dot scrolling is set to Off.
		1 (	ON	0.25-/0.125-dot scrolling is set to On.
(2)	Direction (0/1)	The direction of 0.25-/0.125-dot scrolling is set here.		
		0 <b>L</b>	_eft	Scrolling moves to the left.
		1 F	Right	Scrolling moves to the right.
(3)	Step	The amount of movement per 1 V is set here. 0.25-dot scroll setting range: 0.00 dot to 254.75 dots (0.25 dot Step) 0.125-dot scroll setting range: 0.000 dot to 254.750 dot (0.125 dot Step)		
(4)	V-offset	The value of V-offset is set here. Setting range: 0.0 to 100.0% (0.1% Step)		

#### 7.7 Motion blur

Motion blur is a function for displaying up to 16 of the specified patterns described later, and moving them.

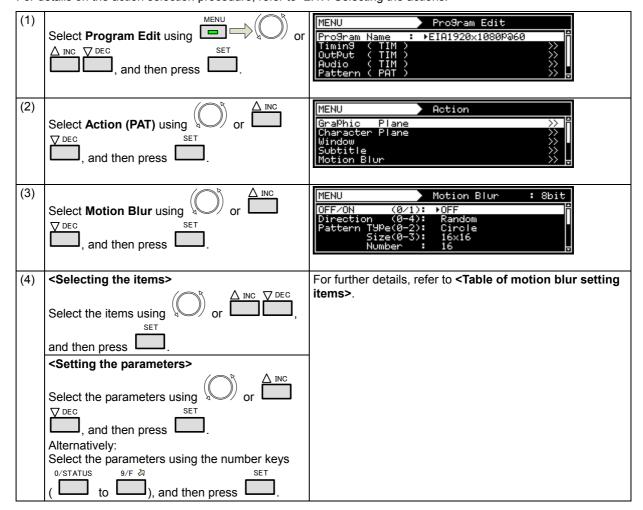
The patterns to be displayed using this function can be combined with other patterns (excluding the cursor), and displayed.



<Display example: Random movement direction>

The setting procedure is described below.

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."



#### <Table of motion blur setting items>

(1)	OFF/ON (0/1)	This is used to set the motion blur function to ON or OFF.				
		0	OFF			
		1	ON			
(2)	Direction (0-4)	Th	e direction of the pattern	movement is specified here.		
		0	Top-L<->Bottom-R	Top left ⇔ bottom right		
		1	Left<->Right	Left ⇔ right		
		2	2:Up<->Down	Up ⇔ down		
		3	Random Random			
		4	Left<->Right Pair	Left ⇔ right pair		
		パターン 1~16 ペア 1 1 2 2 3 4 3 5 6 4 7 8 5 9 10 6 11 12 7 13 14 8 15 16				
(3)	Pattern Type (0-2)	Th	The shape of the pattern is specified here.			
		0	Circle	Circle		
		1	Square	Square		
		2	USER Character User character			
				Setting range: <b>E0h - FFh</b>		
(4)	Pattern Size (0-3)	Th	e size of the pattern is sp	ecified here. (dot)		
		0	8 × 8			
		1	16 × 16			
		2	32 × 32			
		3	64 × 64			
(5)	Pattern Number		e number of patterns to b tting range: <b>1 - 16</b>	e displayed is set here.		
		*	16 is specified as the nu	ight Pair setting is available only when 2, 4, 8, or mber of patterns. Even if a number other than 2, he number of patterns will be 2, 4, 8, or 16.		
(6)	Distance	When the Direction >Left<->Right Pair setting is selected, the pattern interval for each pair is specified here.  Setting range: 0 to 255 [dot]  Pair 1 Pattern  Distance 1				
(7)	Step InpMode (0/1)	Th	The <b>step</b> setting method is specified here.			
( )		0				
		1	Separate	The patterns are set individually.		
(8)	Step (/1 V)	The amount of pattern movement per 1 V (progressive scanning: frame; interlaced scanning: field) is set here.  Setting range: 1 - 255 [dot]				
		* When the Direction >Left<->Right Pair setting is selected, the amount of movement is specified for each pair (pair 1 to 8).				

(9)	Color InpMode (0/1)	The <b>color</b> setting method is specified here.		
		0 <b>AII</b>		All the patterns are set together.
		1 Separate		The patterns are set individually.
(10)	Color	The pattern of	olors (R/G/B le	evels) are set here.
		The colors fo	r patterns 9 to	16 are the same as for patterns 1 to 8.
		The setting ra	ange differs de	pending on the pattern drawing bit length (Color
		Color Depth	Setting rang	<u>ge</u>
		8bit :	0 - 255	
		9bit :	0 - 511	
		10bit :	0 - 1023	
		11bit :	0 - 2047	
		12bit :	0 - 4095	
		13bit :	0 - 8191	
		14bit :	0 - 16383	
		15bit :	0 - 32767	
		16bit :	0 - 65535	
(11)	OPT Back R,G,B	optional patte	ern No.71 (Mot	/B levels) is specified here when the internal ion Blur Line) has been selected. ne as for the <b>Color setting item</b> .
(12)	Area H/V	The pattern movement range is set as a percentage of H/V-Timing Disp.  Setting range: 0 - 100 [%]		

#### <Concerning the internal optional pattern No.71>

Internal optional pattern No.71 (Motion Blur Line) is the pattern which displays the aspects of the Motion Blur>Step setting and other setting items.

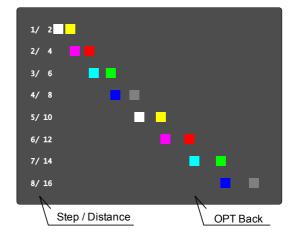
What is displayed depends on the Direction setting.

The background color can be set using OPT Back.

(1) When the Direction>Top-L<->Bottom-R, Left<->Right, or Up<->Down setting is selected The Step and its path line are displayed in white.

 (2) When the Direction>Left<->Right setting is selected

The Step and Distance are displayed in white.



(3) When the Direction>Random setting is selected The frames are displayed in white.

## 7.8 Scroll Sequence

Sequence numbers for each plane can set up to 16 by scroll sequence setting.

(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ▶EIA1920x1080P@60  Timin9 ( TIM )
(2)	Select <b>Action</b> using $\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	MENU Action  Motion Blur  Ø.5/0.25dot Scroll  Scroll Sequence  Lip Sync  Black Insertion
(3)	Select Scroll Sequence using or or or or or and then press.	MENU Scroll Sequence  OFF/ON Graphic (0/1): ▶OFF Character(0/1): OFF Window (0/1): OFF Return Mode (0-2): One Sequence Number : 1
(4)	Select the items using or	For further details, refer to .
	0/STATUS 9/F & SET ( to ), and then press .	

#### <The table of Scroll Sequence setting items>

(1)	OFF/ON (0/1)	On or Off is not	for graphic/character/window plane here.		
(1)	OFFION (U/1)	0 OFF	ior graphic/character/wirdow plane here.		
(0)			the first the second of the second		
(2)	Return Mode (0/2)		store the scroll position is set here.		
		0 One Seque			
		1 All Sequen	·		
		2 Random	The scroll position is not restored. In this mode, the Parameter settings (scroll direction, execution interval, movement amount and execution time) are changed at random for each sequence regardless of the values which have been set. However, with the exception of Direction, the set values are used as the upper limits.		
(3)	Number	Set the number	Set the number of sequence		
Parai	 neter	octaing range.	-10		
(1)	Direction	The direction of	scrolling is set here		
( - /		0 <b>L</b>	The window is scrolled toward the left.		
		1 R	The window is scrolled toward the right.		
		2 <b>U</b>	The window is scrolled upward.		
		3 <b>D</b>	The window is scrolled downward.		
		4 L-U	The window is scrolled toward the top left.		
		5 <b>L-D</b>	The window is scrolled toward the bottom left.		
		6 <b>R-U</b>	The window is scrolled toward the top right.		
		7 <b>R-D</b>	The window is scrolled toward the bottom right.		
(2)	Interval	The interval (ex	ecution interval) is set here.		
(3)	H Step	The amount of interval)movem	The amount of horizontal movement per interval (execution interval)movement is set here.  Setting range: 1 - 255 dot		
(4)	V Step	The amount of is set here.	The amount of vertical movement per interval (execution interval) movement		
(5)	Time	Execution time per sequence is set here. Setting range: 1 - 999 V			

### 7.9 LipSync

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."

The lip sync function can be used to set a difference between the audio phase and video phase.

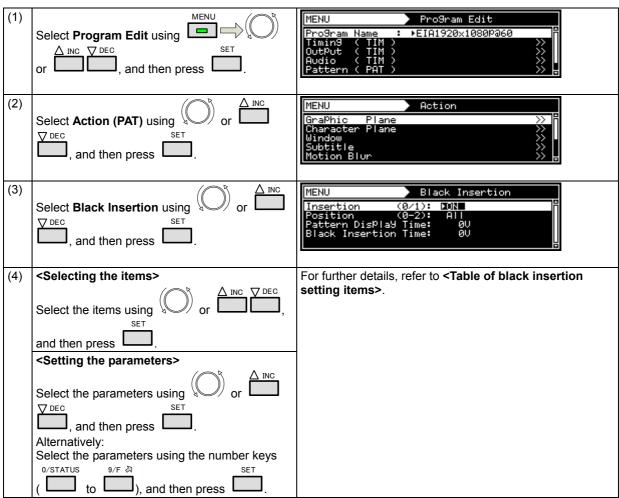
(1)	Select <b>Program Edit</b> using SET or SET, and then press.	MENU Pro9ram Edit  Pro9ram Name : ►EIA1920x1080Pa60  Timin9 (TIM ) >> OutPut (TIM ) >> Audio (TIM ) >> Pattern (PAT ) >>  P
(2)	Select <b>Action (PAT)</b> using SET or SET or And then press .	MENU Action  Graphic Plane  Character Plane  Window  Subtitle  Motion Blur   Action
(3)	Select <b>Lip Sync</b> using or	MENU LiP Sync  OFF/ON (0/1): ▶OFF  Mode (0/1): Delay  Audio Delay Time: + Øms  ON Time : 120V  OFF Time : 120V
(4)	Select the items using or	For further details, refer to <table items="" lip="" of="" setting="" sync="">.</table>

#### <Table of LipSync setting items>

(1)	OFF/ON(0/1)	Whether to set the <b>lip sync</b> function on or off is set here.				
		0	OFF	Disabled		
		1	ON	Enabled		
(2)	Mode(0/1)	Th	e method used to set the	delay amount (time) is set here.		
		0	Delay	The delay amount of the user's choice is set.		
		1	EDID	The delay amount accords with the EDID of the connection destination. (Automatically set)		
(3)	When Delay has been	Th	e delay amount is set he	re as a time.		
	selected as the Mode		ENU Lip S9r			
	setting Audio Delay Time	M	FF/ON (0/1): OFF ode (0/1): Delay			
	Addio Boldy Timo	Ö	Jdio Delay Time: (++) (0 N Time : 255V FF Time : 120V	ms )		
			- Time . 1200			
		Sign Time				
		Sign: Indicates behind or ahead.				
		0	+	The audio is behind the video.		
		1		The audio is ahead of the video.		
			ne: The time is set here.			
	When EDID has been		e port used to read the E			
	selected as the Mode setting		peration is initiated using neerned.	the amount of delay defined in the EDID		
	EDID Port		ENU Lip	Sync		
		01	FF/ON (0/1): OFF	i i i i i i i i i i i i i i i i i i i		
		E	ode (0/1): EDI DID Port (0/1): ▶HDM	111		
			N Time : 255 FF Time : 120	ju V		
		0	HDMI1	The EDID is read from HDMI1.		
		1	HDMI2	The EDID is read from HDMI2.		
(4)	On Time			raster (white) display time are set using the me increments). <b>Setting range: 1 V to 255 V</b>		
(5)	OFF Time	The audio no output time and display OFF (black) time are set using the vertical sync signal (in 1-frame increments). Setting range: 1 V to 255 V				

#### 7.10 Black insertion action

For details on the action selection procedure, refer to "2.1.4 Selecting the actions."



#### <Table of black insertion setting items>

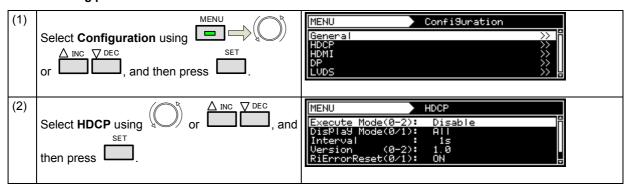
(1)	Insertion (0/1)	Or	On or Off for black insertion is set here.		
		0	OFF	Black insertion is set to Off.	
		1	ON	Black insertion is set to On.	
(2)	Position (0-2)	Th	e black insertion position	is set here.	
		0	All	The entire screen is subject to the black insertion.	
		1	Left Half	The left half of the screen is subject to the black insertion.	
		2	Right Half	The right half of the screen is subject to the black insertion.	
(3)	Pattern Display Time	The time during which the pattern is to be displayed is set here. Setting range: 0 V to 255 V			
(4)	Black Insertion Time	The time during which black insertion is to take effect is set here. Setting range: 0 V to 255 V			

# 8

## HDCP SETTINGS AND EXECUTION

### 8.1 HDCP settings

#### <HDCP setting procedure>



#### <List of HDCP setting items>

(1)	Execute Mode (0-2)	The HDCP execution mode is set here.		
		0	Disable	HDCP execution is disabled.
		1	Enable	HDCP execution is enabled.
		2	Program	HDCP execution enable/disable is set for each
				program.
(2)	Display Mode (0-1)	Th	e HDCP authentication s	creen display mode is set here.
		0	All	All the authentication values are displayed.
		1	NG Only	The authentication values are displayed only with an NG result.
(3)	Interval	Th	e interval for performing	the authentication is set here.
		1s	-10s	An interval from 1 second to 10 seconds is set.
(4)	Version (0-2)	The HDCP version is set here.		
		0	1.0	HDCP version 1.0 is used for execution.
		1	1.1/1.2	Operation is performed using HDCP version 1.1 or 1.2. *1
		2	EDID Check	The version is determined after checking EDID.
(5)	RiErrorReset (0/1)		hether to proceed with re- DCP authentication is set	-authentication when errors have occurred in here.
		0	OFF	Re-authentication is not undertaken when errors have occurred.
		1	ON	Re-authentication is undertaken when errors have occurred.
(6)	FIFO Ready (0-8)		e limit on the time to wait nnected device is a repea	until "FIFO Ready" is returned when the ater is set here.
		0	OFF	30 seconds
		1- 7	600 ms to 4,200 ms	Increased in increments of 600 ms
		8	5,000 ms	5 seconds

<sup>\*1 &</sup>quot;Advance Cipher/Enhanced Link Verification" is not supported.

#### 8.2 HDCP execution

#### 8.2.1 Execution procedure

One of the following three steps can be taken for HDCP execution.

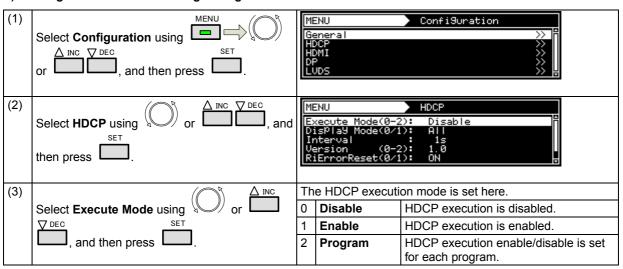
- a) Set enable or disable using the On/Off customize key.
- b) Set enable or disable using Configuration.
- c) Set enable or disable for each program.

#### a) Setting enable or disable using the On/Off customize key

This setting is canceled when the program is changed, and b) Setting enable or disable always using Configuration and c) Setting enable or disable for each program take effect.

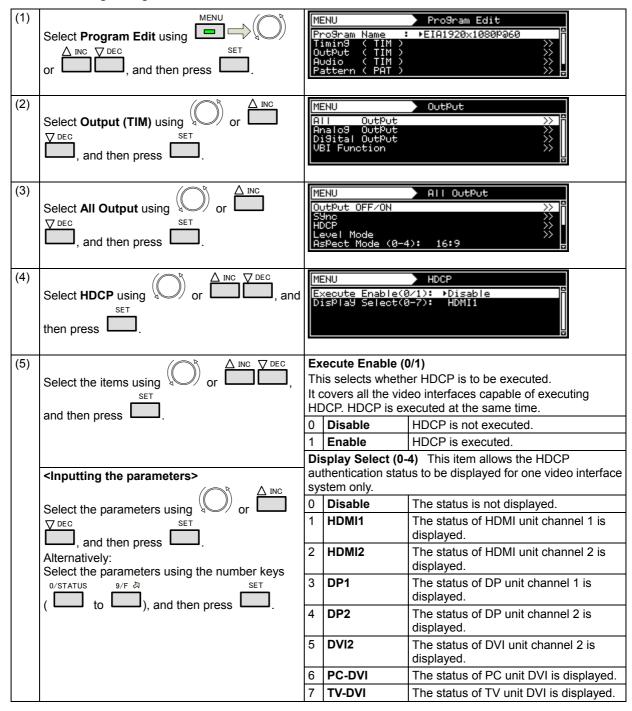
(1)	I.HDCP	HDCP is executed, and the authentication results screen		
		appears above the test pattern.		

#### b) Setting enable or disable using Configuration



#### c) Setting enable or disable for each program

This setting takes effect when "Program" was selected as the HDCP mode setting in b) Setting enable or disable using Configuration.



#### 8.2.2 Screen displays during HDCP execution

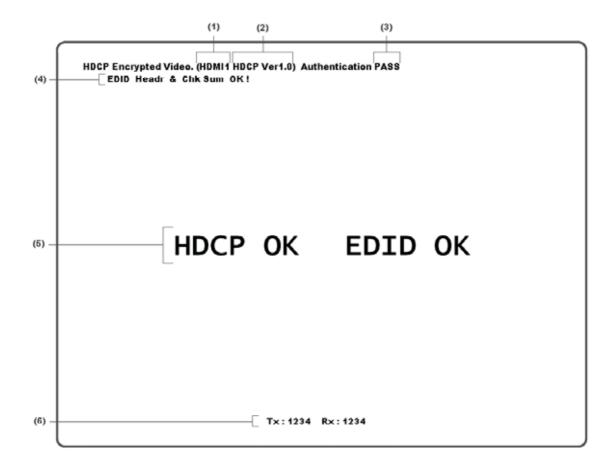
The authentication results and other information are displayed on the screen during HDCP execution. (If other patterns are already displayed, the information will be displayed on top of the patterns.)

(1) When HDCP is selected as the NAME/LIST pattern.

For details on the screen and what is displayed on the screen, refer to "6.13.5 HDCP (High-bandwidth Digital Content Protection)."

#### (2) At all other times

A screen such as the one shown below is displayed.



Details of the information shown on the screen are given below.

representative or your distributor.

#### <HDCP authentication screen (simplified version) display data>

This indicates the port selected in c) Display Select of "8.2.1 Execution procedure." (2) The HDCP version is displayed here. (When the HDCP authentication has failed, an error message is displayed.) The HDCP authentication results are displayed here. (If authentication is successful, "PASS" appears; it is has failed, "NG" appears.) (4) The check results for the EDID header and checksum are indicated here. (These results are displayed only when "AUTO" has been selected as the HDMI or DVI setting in "4.2.2 HDMI setting procedure" or when "EDID Check" has been selected as the Version setting in "8.1 HDCP settings.") (5) The HDCP authentication status (OK or NG) as well as the check result (OK or NG) for the EDID header and checksum are indicated here. (These results are displayed only when "AUTO" has been selected as the HDMI or DVI setting in "4.2.2 HDMI setting procedure" or when "EDID Check" has been selected as the Version setting in "8.1 HDCP The Synchronization Verification Values for checking the adequacy of the link are displayed here. "Tx" is the value calculated for the transmitter; "Rx" is the value calculated for the receiver. The display is updated each time HDCP authentication and encryption are completed. When the HDCP display is DisplayPort, this item is not displayed. In the HDCP/EDID/CEC collective display mode, the following check results are displayed in addition to the above displays. DVI-1: OK DVI-2: OK TvVGA1: OK Display example (DVI + TV + HDMI) For details of the HDCP/EDID/CEC collective display mode settings, refer to section "8.2.3 HDCP/EDID/CEC collective display."

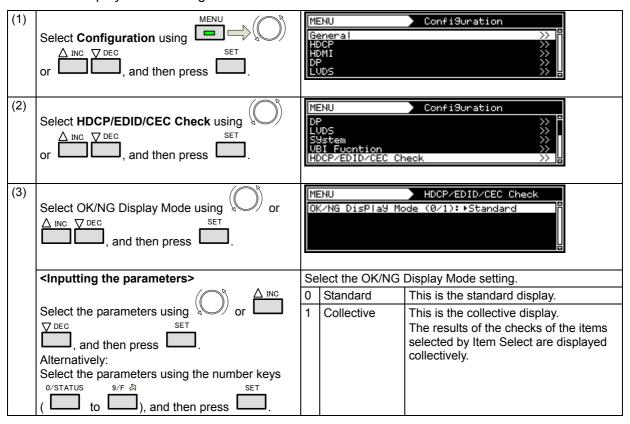
The requisite license must be registered. To purchase the license, consult with an Astrodesign sales

#### 8.2.3 HDCP/EDID/CEC collective display

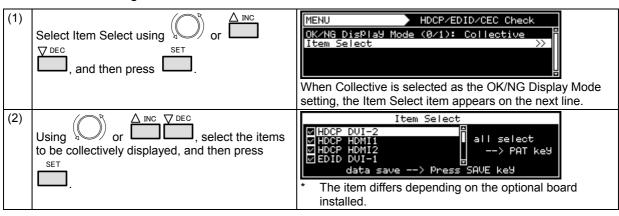
\* The requisite license must be registered. To purchase the license, consult with an Astrodesign sales representative or your distributor.

When OFF has been selected as the List display setting, the results of the HDCP/EDID/CEC check can be displayed together with HDCP ON.

#### ■ OK/NG Display Mode settings



#### ■ Item Select settings



#### <OK or NG judgement>

#### (1) HDCP

With HDCP ON, it is checked that HDCP authentication is performed correctly.

#### (2) EDID

The EDID is read at specific intervals, and an "OK" verdict is given when:

- The EDID can be read correctly.
- · The EDID header and checksum are correct.

#### (3) CEC

An OK verdict is given if the CEC commands are sent in succession to the HDMI ports and ACK responses are given correctly when the program is run.

For the CEC send data and parameters, refer to the CEC List data among the program data.

The CEC check is performed if the following conditions are met:

- · Transmission must be established as the Mode setting.
- · The Tx Destination must be other than Fh.

If the above conditions are not met, the CEC check is not performed. (No displays will be shown.)

#### <Items which can be displayed collectively>

The items listed in the table below can be displayed collectively.

Output unit	HDCP		EDID		CEC	
PC analog unit VM-1811	PcDVI1 PcDVI2 PcDVI3		PcDVI1 PcDVI2 PcDVI3	PcVGA1 PcVGA2 PcVGA3		
TV encoder unit VM-1812, VM-1812-B			TvVGA1 TvVGA2 TvVGA3			
DVI unit VM-1814		DVI-2 DVI-4 DVI-6	DVI-1 DVI-3 DVI-5	DVI-2 DVI-4 DVI-6		
HDMI unit VM-1817, HDMI1.4a	HDMI1 HDMI3 HDMI5	HDMI2 HDMI4 HDMI6	HDMI1 HDMI3 HDMI5	HDMI2 HDMI4 HDMI6	HDMI1 HDMI3 HDMI5	HDMI2 HDMI4 HDMI6
Display port unit VM-1820	DP-1 DP-3 DP-5	DP-2 DP-4 DP-6	DP-1 DP-3 DP-5	DP-2 DP-4 DP-6		

<sup>\*</sup> The settings of the second and third boards are established for the second and third lines of the columns.

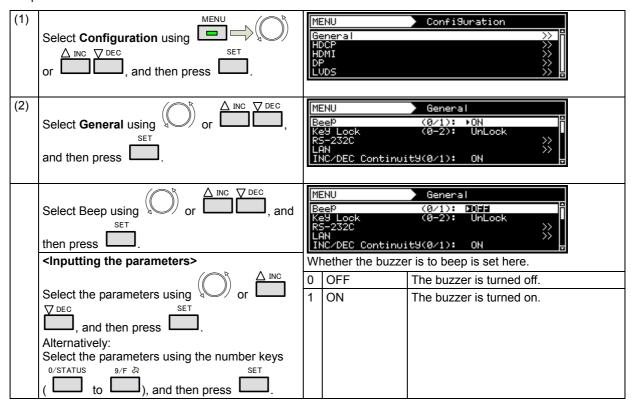


## VG-870B/871B SYSTEM SETTINGS

#### 9.1 System settings

#### 9.1.1 Beep setting

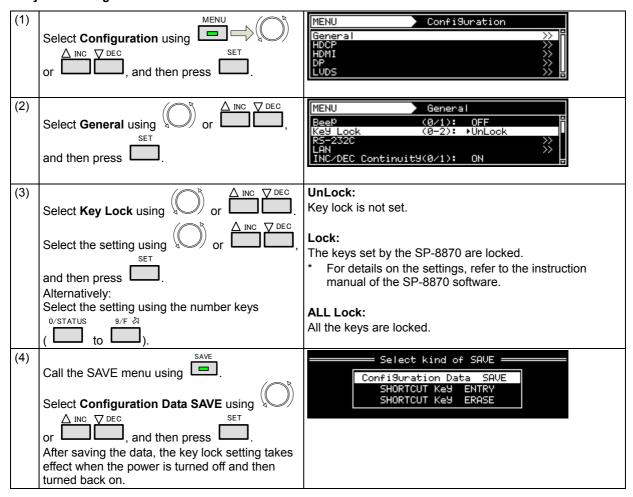
The buzzer which sounds when any of the keys on the front panel of the VG-870B (or on the RB-1870 or RB-1871) are pressed can be turned on or off.



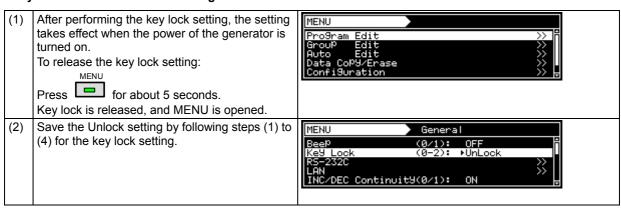
#### 9.1.2 Key lock setting

The keys on the VG-870B/871B main unit can be locked.

#### <Key lock setting>



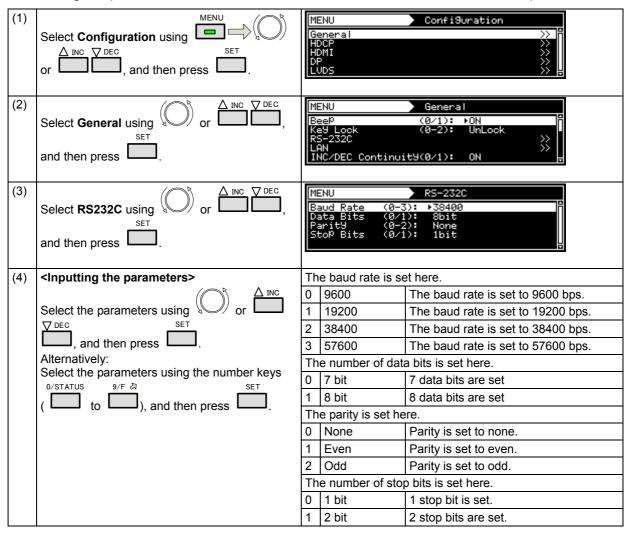
#### <Key lock release and unlock settings>



\* Unless the Unlock setting is saved, the key lock setting will take effect when the generator is turned on.

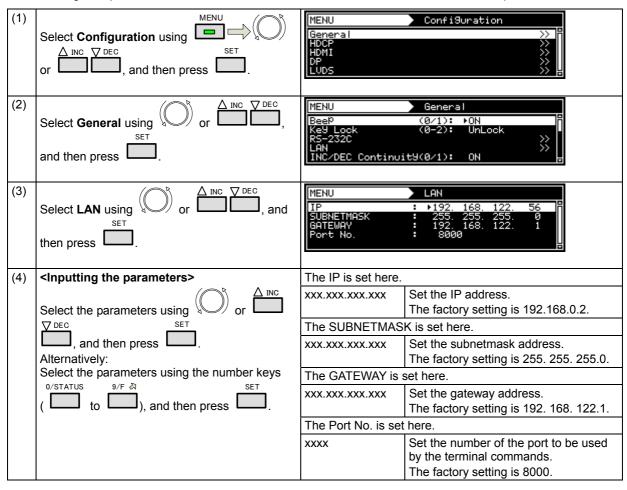
#### 9.1.3 RS-232C settings

These settings are performed when connection to the terminal is to established from the RS-232C port.



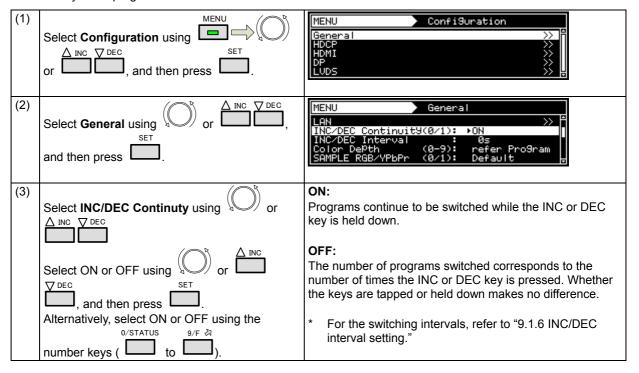
#### 9.1.4 LAN settings

These settings are performed when connection to the terminal is to established from the LAN port.



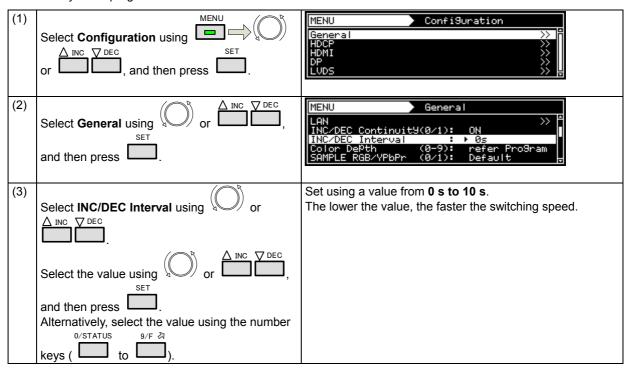
#### 9.1.5 INC/DEC continuity setting

The INC/DEC key continuity function enables the holding down of the INC and DEC keys to be enabled or disabled. It works only when programs are selected and executed.



#### 9.1.6 INC/DEC interval setting

This function enables the minimum interval for switching programs to be set using the INC and DEC key. It works only when programs are selected and executed.



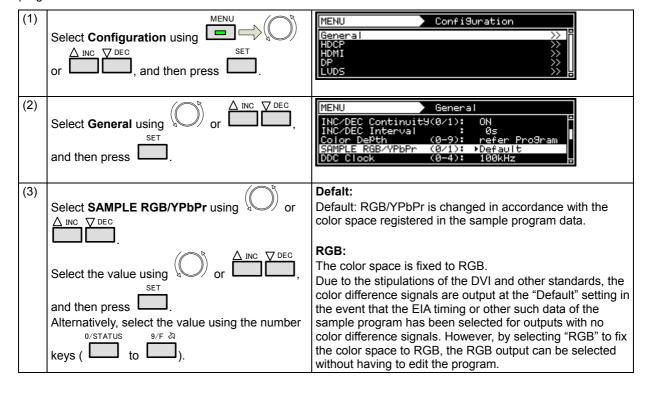
#### 9.1.7 Color depth setting

This setting makes it possible to specify whether the number of color gray scale bits for the output images is to accord with the program data or whether it is to be fixed.

For details, refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

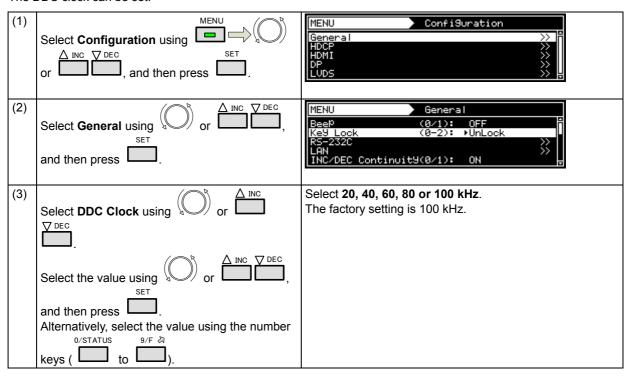
#### 9.1.8 SAMPLE RGB/YPbPr setting

This setting makes it possible to specify whether the color space of the output images is to accord with the sample program data or whether it is to be fixed to RGB.



#### 9.1.9 DDC clock setting

The DDC clock can be set.



#### 9.1.10 Trigger mode settings

#### Concerning the trigger function

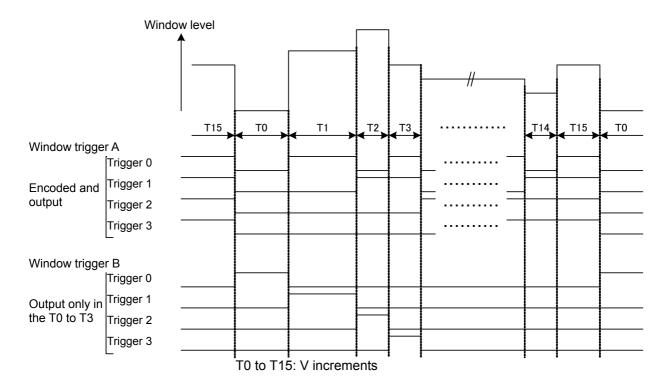
The trigger function outputs trigger in accordance with the patterns in order to evaluate the moving picture response speed and other factors.

The following kinds of triggers are provided.

- (1) Window triggers (TriggerA/TriggerB)
- (2) VSync trigger
- (3) Scroll triggers (optional)
- (4) Simple moving image frame trigger (optional)

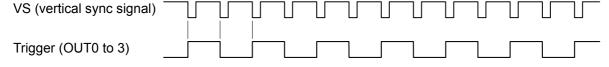
#### (1) Window triggers (TriggerA/TriggerB)

When, on the window patterns, 4 or 16 levels are set and displayed for the format, these triggers are output in accordance with the level sequence.



#### (2) VSync trigger

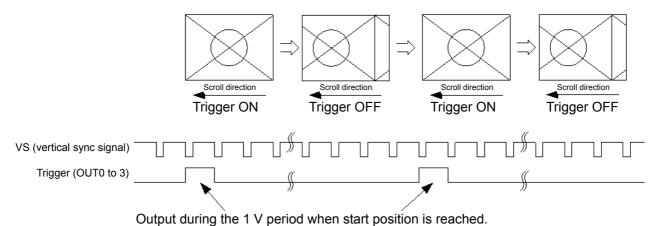
High and low levels are output alternately in line with VSync.



#### (3) Scroll triggers (optional)

When a pattern is being scrolled, the trigger is output when the pattern has reached the start position.

#### When scrolling toward the left

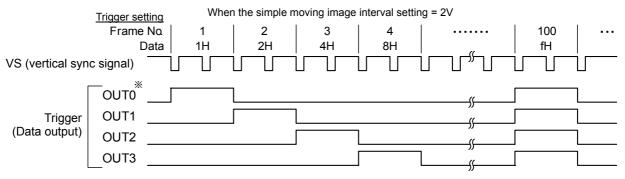




- When there is a multiple number of scroll ON planes (graphic, character and window planes), trigger ON is not output when the scroll settings (step and direction) differ.
- When there is one scroll ON plane, the trigger ON is output in accordance with the settings
  of that plane.
- Trigger ON can be output with both horizontal direction (left/right) scrolling and vertical direction (up/down) scrolling, but when scrolling in both of these directions (top right, for instance), it is output in accordance with the horizontal direction.

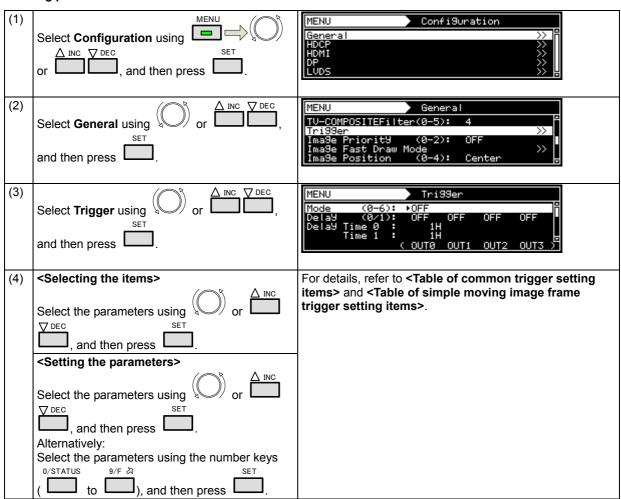
#### (4) Simple moving image frame trigger (optional)

The trigger is output at the specified frames while simple moving images are displayed.



<sup>\*</sup> The VSync trigger is output for OUT0 when Frame + VSync Trigger has been set as the mode.

#### <Setting procedure>



#### <Table of common trigger setting items>

Mode (0-6)	Th	trigger functions are selected here.			
	* Frame Trigger, Frame+VSync Trigger and Scroll Trigger are optional functions. For further details, contact your dealer or an ASTRODESIGN sales representative.				
	0	OFF	At this setting, the trigger output is set to OFF.		
	1	TriggerA	Window trigger A is output.  The trigger is encoded in line with the set time, and output.		
	2	TriggerB	Window trigger B is output.  The trigger is output in line with the set time only for the T0-T3 duration.		
	3	VSync Trigger	The VSync trigger is output.  The trigger is encoded in line with the VSync signal, and output.		
	4	Frame Trigger *	The simple moving image frame triggers are output. The triggers are output at the specified frames.		
	5	Frame+VSync Trigger *	The simple moving image frame triggers (OUT1-3) and VSync trigger (OUT0) are output.		
	6	Scroll Trigger *	The scroll trigger is output. The trigger is output in line with the scroll setting.		

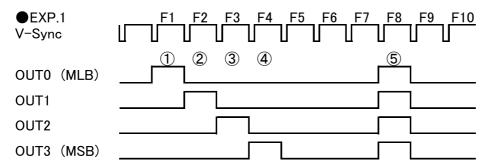
				_		
Delay (0/1)	ON or OFF for the trigger delay function is selected for each output (OUT0-3).			Delay OFF		
	0 OFF The triggers are not delayed.		The triggers are not delayed.			
	1	ON	The trigger outputs are delayed in accordance with the <b>Delay Time</b> .	ON		
Delay Time 0,	The delay time is set here. Setting range: 1 to 4096 [H]			<u>Delay Time</u>		
1						
Time Sel (0/1)	/1) The delay time settings (Delay Time 0 and 1)			are selected here for each output (OUT0 to 3).		
	0 Delay Time 0					
	1	Delay	Time 1			
Pulse (0/1)	ON or OFF for the pulse function is selected for each output (OUT0-3).			Pulse OFF		
	0	OFF	The triggers are output using a width in V increments. (Refer to "Concerning the trigger function" described previously.)	ON Triggers other than VSync trigger *		
	1	ON	The triggers are output using the designated width (in H increments) of the <b>Pulse Width</b> .	Pulse Width		
Pulse Width 0, 1				When the pulse function is OFF, high-level and low-level VSync triggers are output alternately in line with VSync; when it is ON, they are output with each VSync signal.		
Width Sel	The pulse width settings (Pulse Width 0 and 1) a			1) are selected here for each output (OUT0 to 3).		
(0/1)	0 Pulse Width 0					
1		1 Pulse Width 1				
Polarity (0/1)	The polarity of the trigger output is selected for each output (OUT0-3).					
'` '	0 Nega Reversed					
	1	Posi	Positive (high)			
L						

#### <Table of simple moving image frame trigger setting items>

\* The simple moving image frame trigger is an option so it will not be displayed unless the license has been registered.

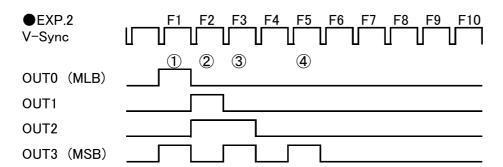
Number Of Frames	The number of frames for outputting the trigger is set here.			
	Setting range: 0 to 16			
Frame No.	The numbers of the frames (max. 16) for outputting the trigger are set here.  Any number which exceeds the number of simple moving image being displayed will be ignored.  Setting range: 1 to 1024			
Data	The data to be output to the specified frames is set here. (Data bits 0-3: OUT0-3) Setting range: 0x0 to 0xF			

#### <Example of simple animation frame trigger settings>



Number Of Fram: 5

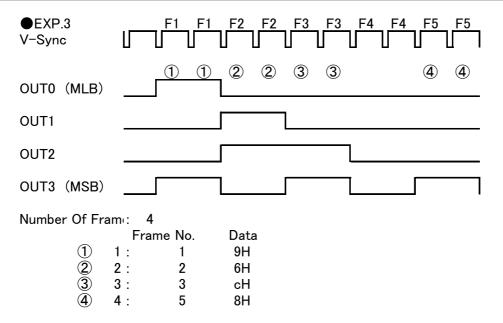
	Fra	Data	
1	1:	1	1H
2	2:	2	2H
3	3:	3	4H
4	4:	4	8H
<b>(5</b> )	5:	8	fH



Number Of Fram: 4

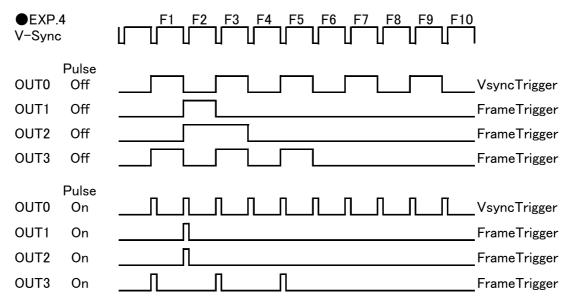
Frame No. Data

 1 : 1 9H
 2 : 2 6H
 3 3 : 3 cH
 4 : 5 8H



OWhen "2" has been selected as the setting for the interval parameter of simple animation below GraphicPlaneScr: Direction: SimpleAnimation

: Interval 1 : 2 (A value of 1 applies for EXP.1 and EXP.2.)



OUnder the settings below, VsyncTrigger is output to OUT1 and FrameTrigger to the other ports.

Mode : Frame+Vsync Trigger

- OWhen Pulse has been set to On, VsyncTrigger pulses are output at both the rising and falling edges.
- OWhen Pulse has been set to On, FrameTrigger pulses are output at the rising edges only.
- OA pulse is output only to the initial edge when it spans a multiple number of frames as with OUT2.

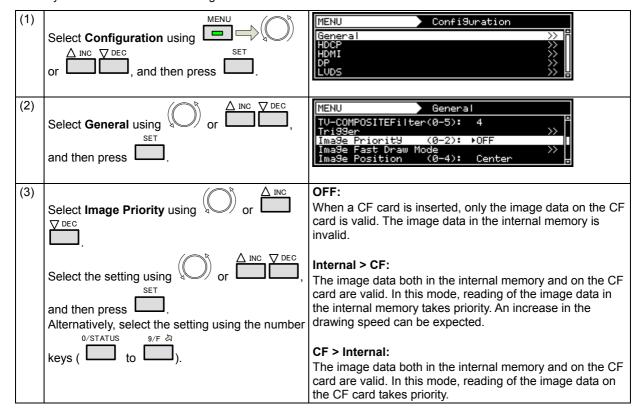
#### 9.1.11 Image - priority settings

Image priority settings can be performed.

Data can be saved both in the internal memory of the VG-870B/871B or on CF cards.

This setting is used to set the priority when both sets of image data have been made valid.

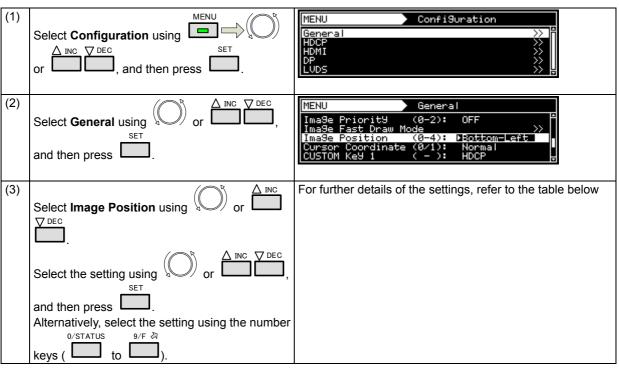
Utilizing it brings some advantages such as increasing the drawing speed and dispersing the data to the internal memory and CF card and then calling the saved data.



# 9.1.12 Image Position Setting

Image Position setting can change Bitmap's display position.

\* Bitmap resolution can be set only when its resolution is lower than display resolution.

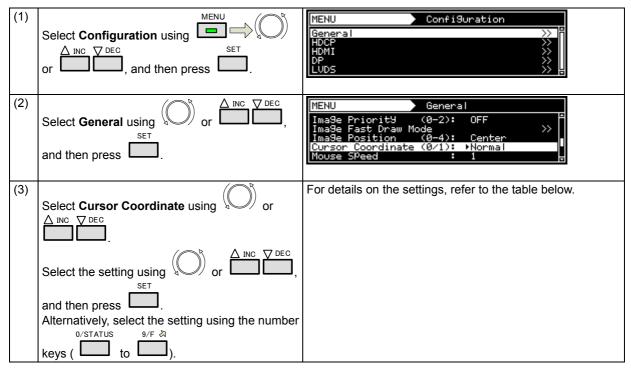


Key	LCD display	Description	
0	Center	The image is displayed at the center of the screen.	
1	Top-Left The image is displayed at the top left.of the screen		
2	Bottom-Left	The image is displayed at the bottom left.of the screen	
3	Top-Right The image is displayed at the top right.of the screen		
4	Bottom-Right	The image is displayed at the bottom right.of the screen	

# 9.1.13 Cursor coordinate setting

The position for displaying the cursor coordinates can be set.

For details on the On or Off setting procedure for the coordinate display, refer to "6.12.1 Cursor settings."

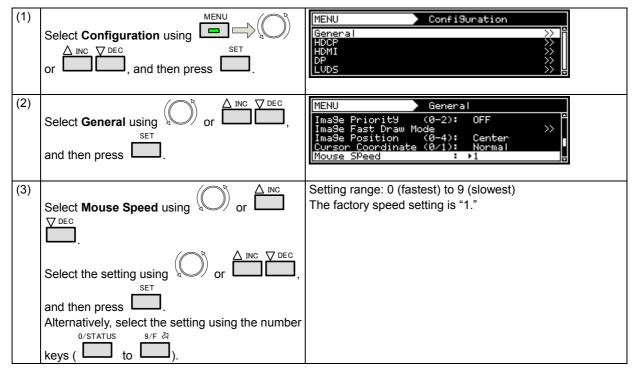


	Key	LCD display	Description	
	0	Normal	mal The coordinates are displayed at the top left or top right of the display.	
1 Move with Curs The display appears near the actual cursor coordinates and moves as the curs		The display appears near the actual cursor coordinates and moves as the cursor moves.		

# 9.1.14 Mouse speed setting

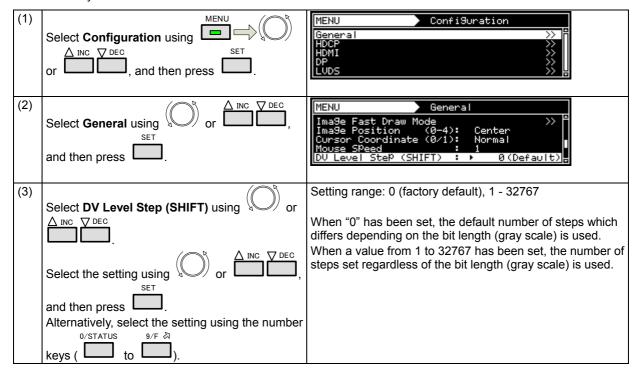
The movement speed at which to move the cursor using the USB mouse can be set.

For details on the On or Off setting procedure for the coordinate display, refer to "6.12.1 Cursor settings."



#### 9.1.15 Digital Video Level Step setting

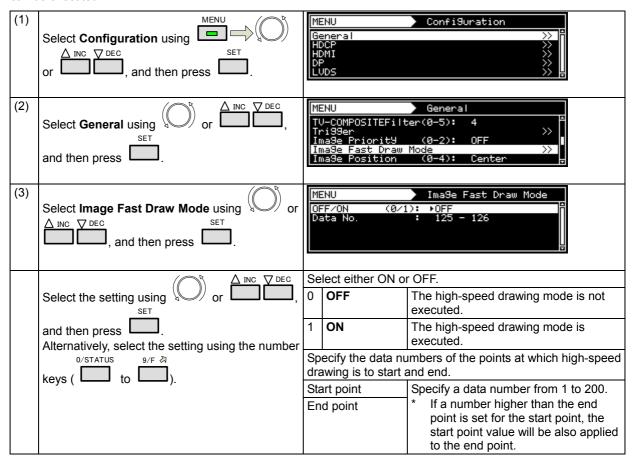
By performing operations to change the digital level of the video signals (refer to section "4.1.8 Setting the digital level"), the number of steps to be taken when the setting speed is increased by pressing the SHIFT key simultaneously is set.



#### 9.1.16 High-speed drawing mode setting

This function allows high-speed drawing to be set.

With the VG-870B/871B, the high-speed drawing mode in which the specified patterns are selected at high speed can be executed.



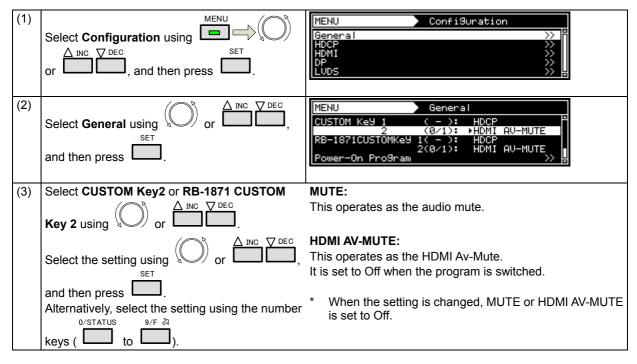


- Video memory capacity restrictions
  - The total volume of data which can be developed in the high-speed pattern selection mode in the image files of the video memory is approximately 174 MB although this figure differs slightly depending on the image data size and other factors. In the high-speed pattern selection mode, it is not possible to load image data exceeding this total.
- When the CF card has been ejected
   When the high-speed pattern selection mode has been set to ON and image
   data has been developed from the card into the video memory, all the image
   data developed from the CF card will be cleared if the card is ejected.

# 9.1.17 CUSTOM Key1, 2, RB-1871 CUSTOM Key

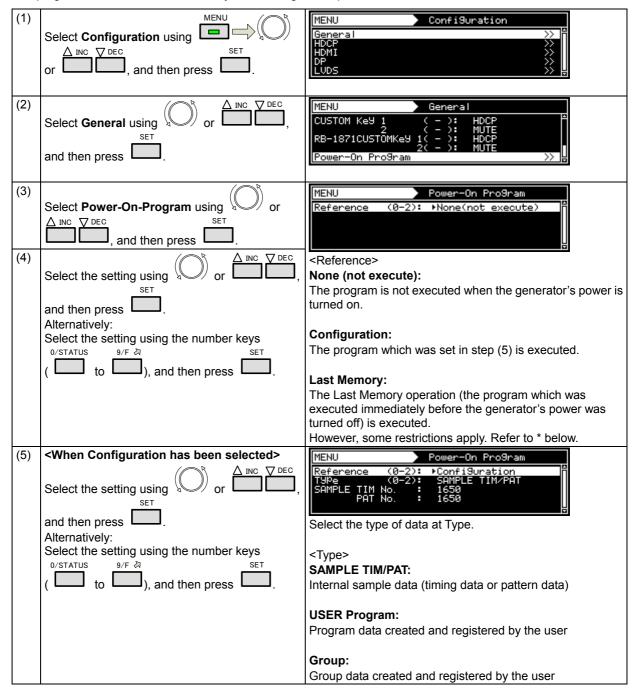
Custom Key2/RB-1871CustomKey2

Either MUTE or HDMI AV-MUTE can be selected.



#### 9.1.18 Operation mode at power-on

The program to be executed immediately after turning on the power of the VG-870B/871B/873/874 can be set.



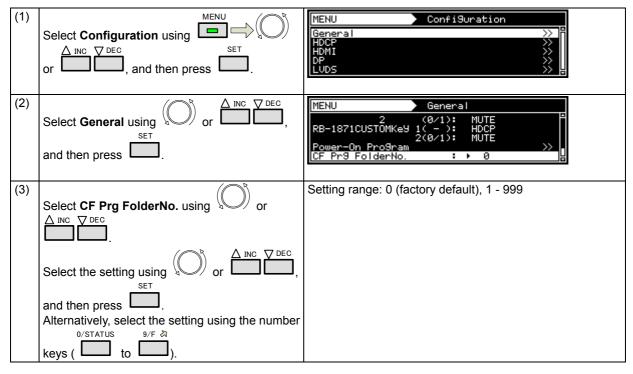
#### \* Last Memory restrictions

- 1. The user program will be executed in cases where the data types differ such as when internal sample data is selected as the timing data and a user program is selected as the pattern data. (Example: When TIM=1001 and PAT=1, TIM=PAT=1 is executed.)
- 2. The timing data number will be executed in cases where user programs are selected as both the timing data and pattern data but their numbers differs. (Example: When TIM=5 and PAT=1, TIM=PAT=5 is executed.)
- 3. When Last Memory is executed using a group, the data at the head of the group will be executed.

# 9.1.19 CF Prg FolderNo. setting

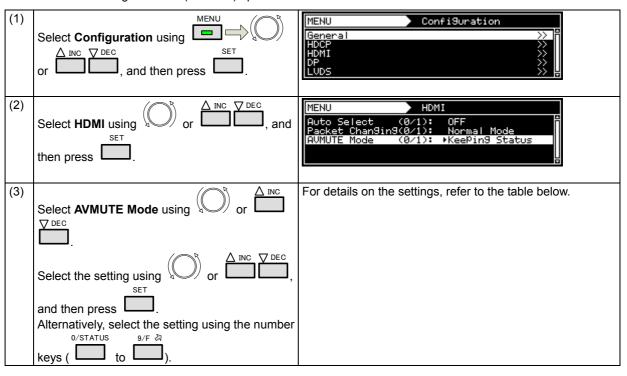
Folders for the program data on the CF cards can be set.

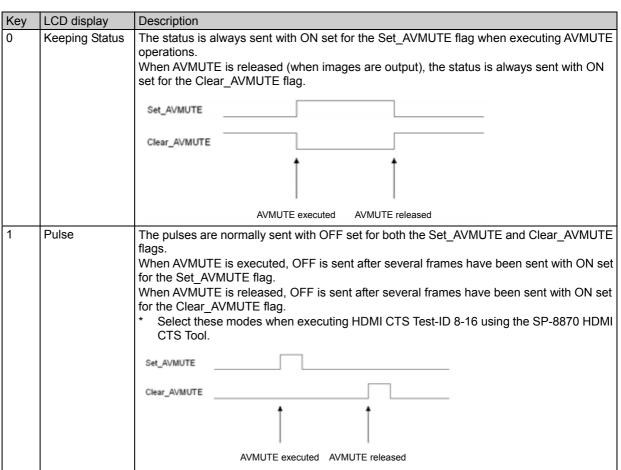
When program data is read and registered, the folders set here are used.



#### 9.1.20 AVMUTE operation mode settings

It is possible to select the method used to set the Set\_AVMUTE and Clear\_AVMUTE flags in the General Control Packet when executing AVMUTE (set/clear) operations.





# 10

# **OTHER FUNCTIONS**

# 10.1 Copying and erasing data

The data stored on the media (internal memory or CF cards) can be copied or erased.



Refrain from ejecting the CF card or turning off the power while data is being copied or erased.

Otherwise the CF card and its data may be damaged.

# 10.1.1 Copying programs

Various methods can be used to copy the program data.

(1)	Select Data Copy/Erase using SET or SET, and then press	MENU Program Edi Group Edi Auto Edi Data Cop9/E Configurati	t >> t >> rase >>
(2)	Select Copy: Program using or or or or and then press	MENU COPY : Pro9 COPY : USER COPY : USER COPY : Ima9 COPY : Subt	Character >>  OPT Pattern >>
(3)	Select the programs using or	MENU TYPe (0-4 Source Destination	: 1 > CF-Card
	Select the programs using the number keys  O/STATUS  O/S	Туре:	Select the type of data copying to be used here.
	( Lad to Lad), and then press Lad.	Source:	Select the copy source number and media here.
		Destination:	Select the copy destination number and media here.

#### <Pre><Pre>rogram categories for Type>

#### 1-Program:

Select this when copying stored programs in their original form.

#### 1-SAMPLE TIM&PAT->Program:

When combining the timing data and pattern data among the internal sample data to create a program

#### 1-TIM (Timing/Output/Audio):

When the timing data of the source program is to be combined with the pattern data of the destination program and the existing program is to be overwritten by the program thus created

This setting cannot be used when the program does not exist at the destination end.

#### 1-PAT (Pattern/Action):

When the pattern data of the source program is to be combined with the timing data of the destination program and the existing program is to be overwritten by the program thus created

This setting cannot be used when the program does not exist at the destination end.

#### Multiple-Program:

Select this when copying a multiple number of programs.

<Differences in the settings from one type of data to another>

#### 1-Program

Select the source and destination program numbers and the media.

#### 1-SAMPLE TIM&PAT->Program

Select the timing or pattern data number among the internal sample data to serve as the source, and select the program number and media to serve as the destination.

#### 1-TIM(Timing/Output/Audio)

Select the program number and media for the timing data to be used as the source, and select the program number and media for the pattern data to be used as the destination.

#### 1-PAT(Pattern/Action)

Select the program number and media for the pattern data to be used as the source, and select the program number and media for the timing data to be used as the destination.

#### 1-Multiple-Program

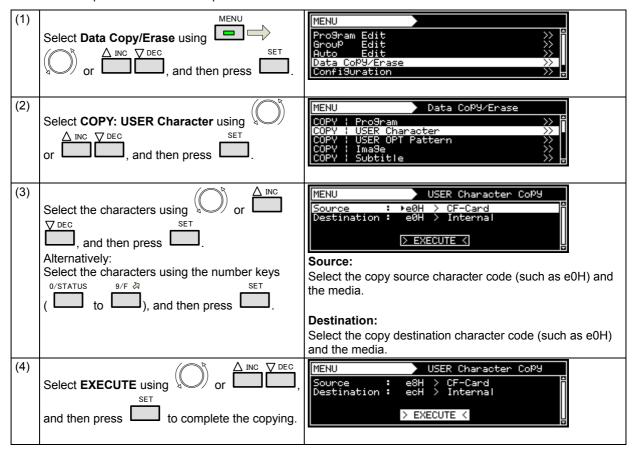
Select the range of the source and destination program numbers and the media.





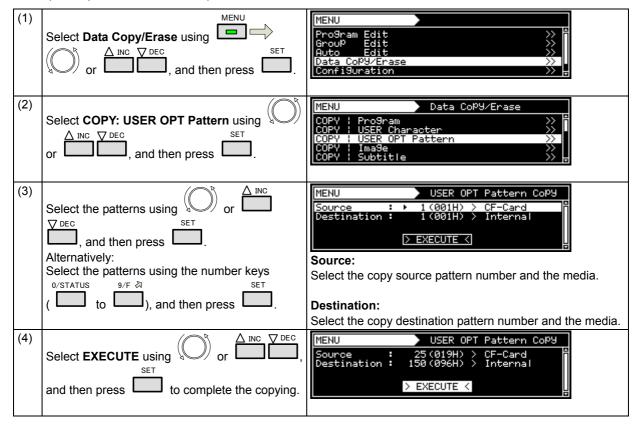
# 10.1.2 Copying user characters

User character pattern data can be copied.



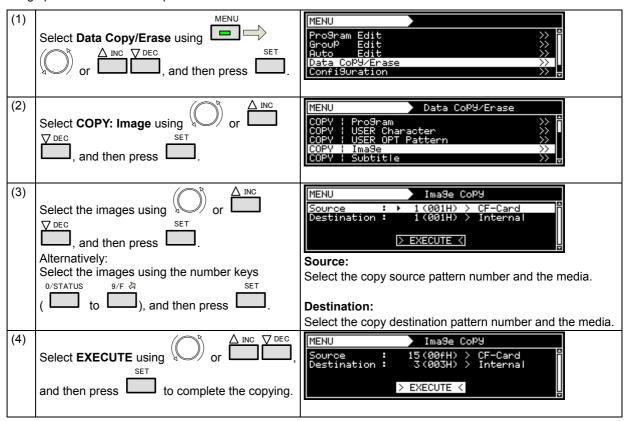
#### 10.1.3 Copying user optional patterns

User optional pattern data can be copied.



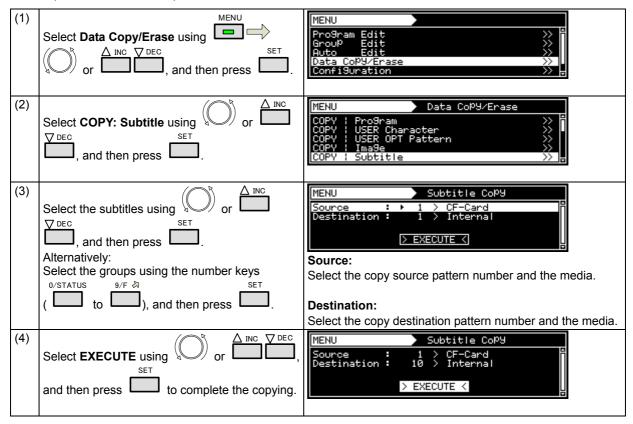
#### 10.1.4 Copying images

Image pattern data can be copied.



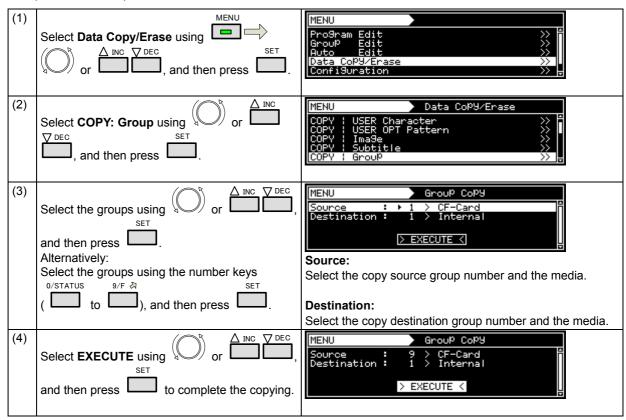
# 10.1.5 Copying subtitle

Subtitle pattern data can be copied.



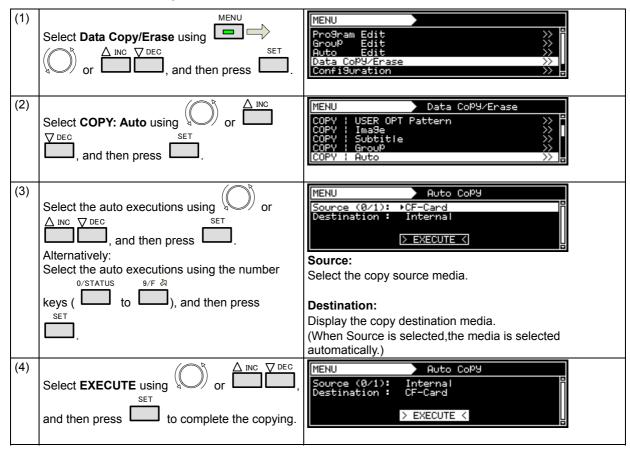
# 10.1.6 Copying groups

Group data can be copied.



# 10.1.7 Copying auto executions

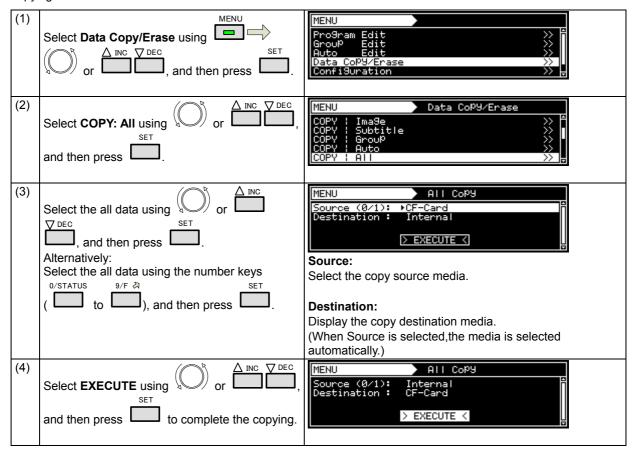
Auto execution data can be copied.



#### 10.1.8 Copying all data

All the data can be copied together.

The time taken to copy the data differs according to how much data is to be copied. It is not possible to cancel copying once it has been initiated.



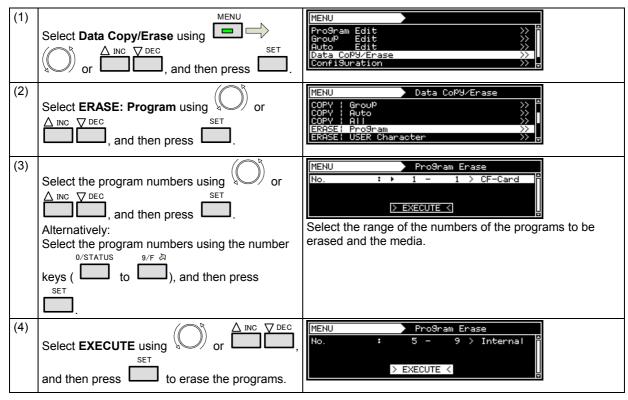


Refrain from ejecting the CF card or turning off the power while data is being copied.

Otherwise the CF card and its data may be damaged.

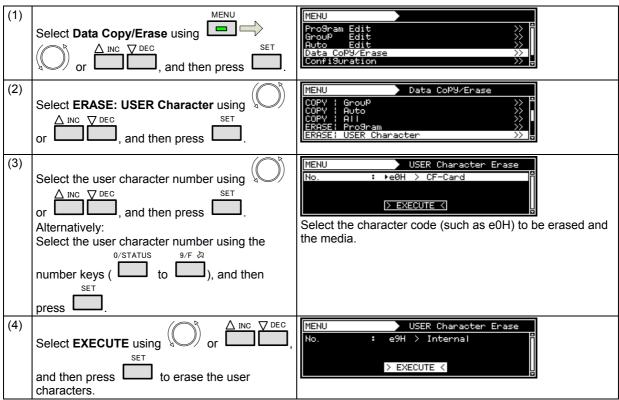
# 10.1.9 Erasing programs

Program data can be erased. At the same time, multiple numbers of programs can be erased.



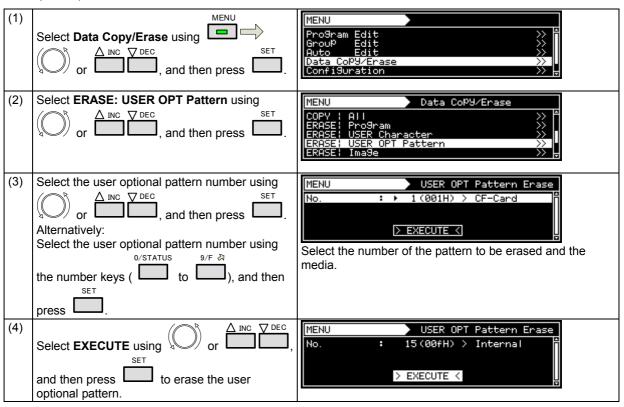
#### 10.1.10 Erasing user characters

User character pattern data can be erased.



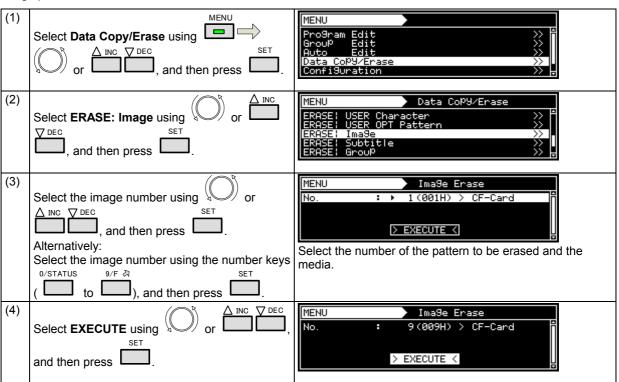
#### 10.1.11 Erasing user optional patterns

User optional pattern data can be erased.



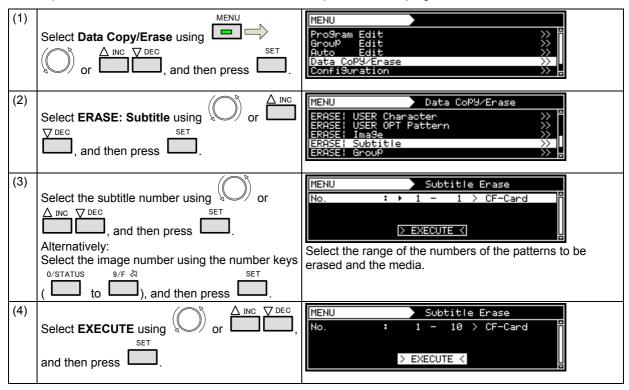
#### 10.1.12 Erasing images

Image pattern data can be erased.



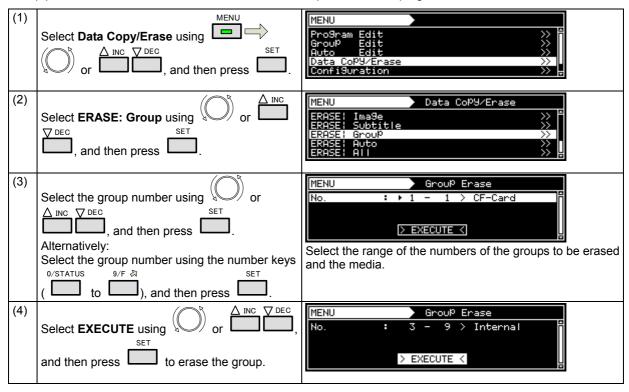
### 10.1.13 Erasing subtitle

Subtitle pattern data can be erased. At the same time, multiple numbers of programs can be erased.



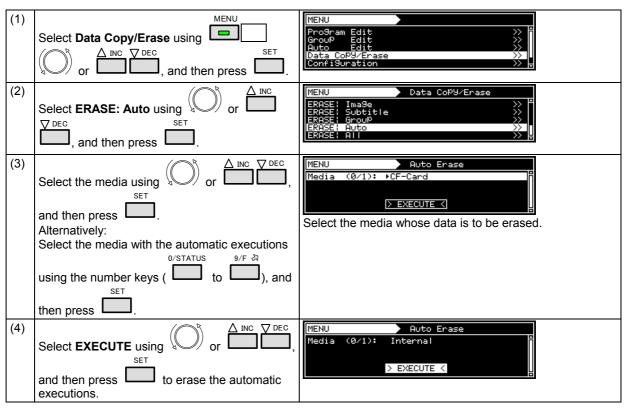
#### 10.1.14 Erasing groups

Group pattern data can be erased. At the same time, multiple numbers of programs can be erased.



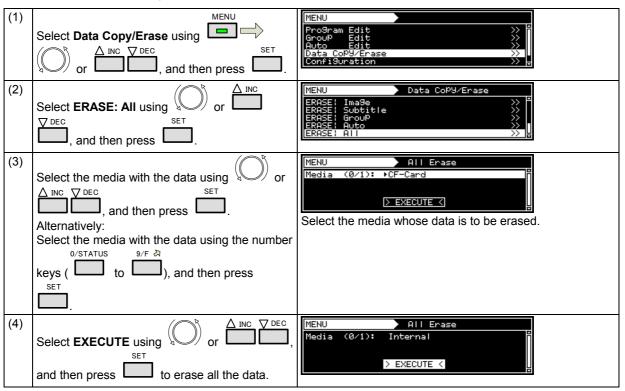
#### 10.1.15 Erasing automatic executions

Automatic execution data can be erased.



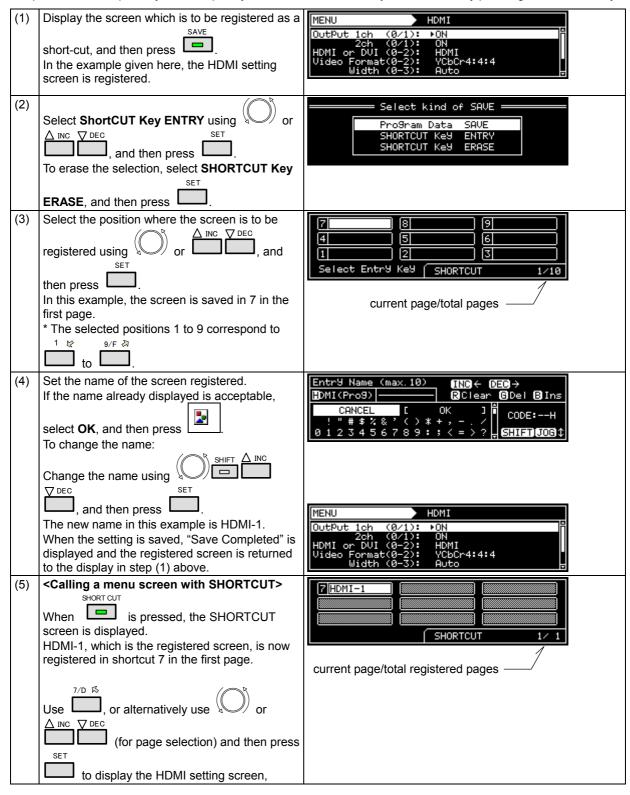
#### 10.1.16 Erasing all data

All the data can be erased together.



# 10.2 Short-cut keys

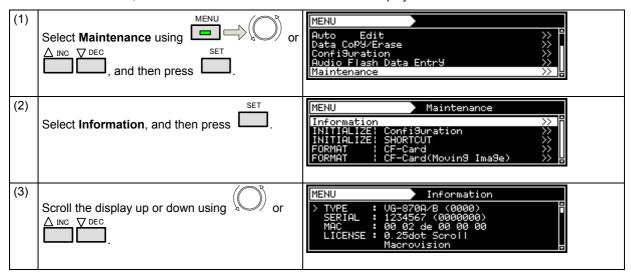
It is possible to set up to any of 90 frequently used screens so that they can be called by pressing the short-cut keys.



\* The shortcut key function may be disabled on some display screens. Use ESC to return to the previous screen, and press the keys again.

# 10.3 Information

The main unit's version, serial number and other information can be displayed.



#### <Table of items displays>

Example of display	Description		
TYPE: VG-870A/B (0000)	Product name		
SERIAL: 1234567 (0000000)	Serial number		
MAC: 00 02 de 00 00 00	MAC address		
LICENSE: 0.25dot Scroll	Usable option functions		
Macrovision			
H/W Ver.: 00 S00-00	Hardware version		
FPGA M01.00/S01.00/R01.00			
F/W Ver.: 01.00 (0000)	Firmware version		
- UNIT	(Listed below is the output unit information.)		
SLOT0) TYPE: VM-1811	Unit type		
SERIAL: 0000000	Serial number		
H/W Ver.: 01(V) S00-00 FPGA01.00	Hardware version		
F/W Ver.: 01.00	Firmware version		
TX1 Ver.: CG V 1.1.1	DisplayPort Transmitter Ver. (DP unit only)		
USER ADJUSTMENT VALUE: R/G/B = +0/+0/+0	User adjustment values (dependent on type of unit)		
	* The 'SLOT' number is given in the sequence $(0 \rightarrow 1)$		
	$\rightarrow$ 2) conting from the lowest slot on the rear panel of		
	the main unit.		
	The 'INT' number is given for the internal moving image module		
	inago modulo		
- DISK SPACE	(Listed below is the amount of the device's memory		
	which has been used.)		
FSystem Used Available Mounted	Mounted c: Internal memory		
rom0a 13360 (12%) 96998 c:	d: CF card		

# 10.4 Data initialization

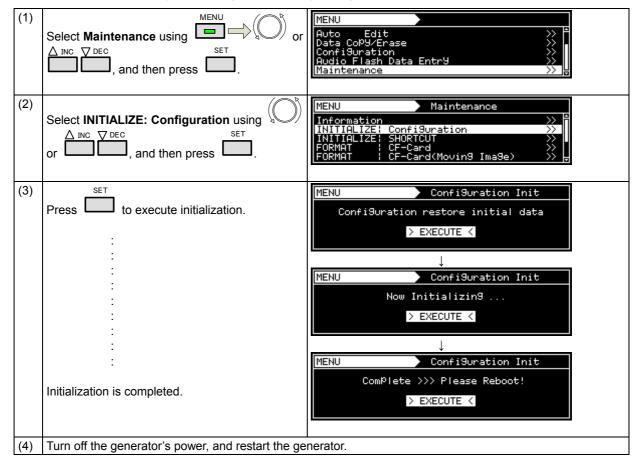
Initialization restores the system settings and short-cut data to the factory settings.



- Performing this operation initialized all the data stored in the internal memory.
- · The generator must be re-started after initialization.

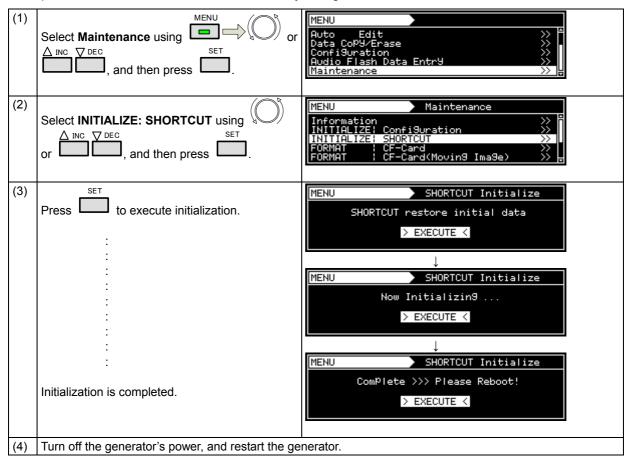
#### 10.4.1 Initializing the system settings

This operation restores the system settings to the factory settings.



# 10.4.2 Initializing the short-cut data

This operation restores the short-cut data to the factory settings.

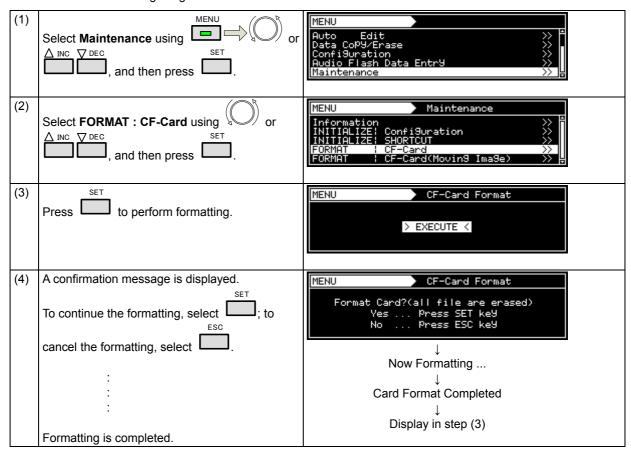


# 10.5 Formatting

#### 10.5.1 Formatting the CF card

Described below is the procedure used to format the CF card.

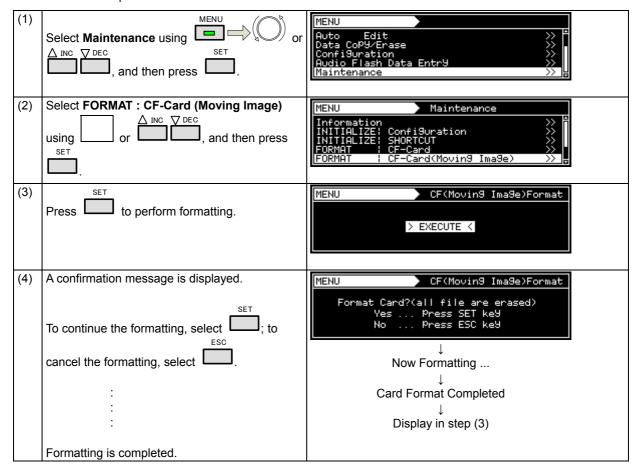
\* For further details on the CF card used for the moving image module, refer to "10.5.2 Formatting CF cards for exclusive use of moving images."



# 10.5.2 Formatting CF cards for exclusive use of moving images

Described below is the procedure used to format the CF card used exclusively for moving images.

\* The CF card for exclusive use of moving images is used with the moving image module, and its ejection slot is located on the side panel of the VG main unit. (For further details, refer to "1.4.7 VG-870B/871B/873/874 side panel") Bear in mind that the file system for these cards is different from the system for the CF cards used by the slot on the front panel of the VG main unit.



#### 10.5.3 Internal memory formatting and data installation

The procedure for formatting the internal memory is described below.



Formatting the internal memory will delete the data required for the generator's operation so the steps for data installation and firmware version updating must be taken after the memory has been formatted.

#### <Procedure>

- 1. Have the required data ready.
- 2. Format the internal memory.
- 3. Turn the power on from the off status.
- 4. Install the data.
- 5. Turn off the power.
- 6. Update the firmware version.
- ..... Refer to **<Firmware version updating** procedure>.

..... Refer to <Data installation procedure>.

..... Refer to <Formatting procedure>.

7. Power off the power, and then turn it back on.

#### <Required data>

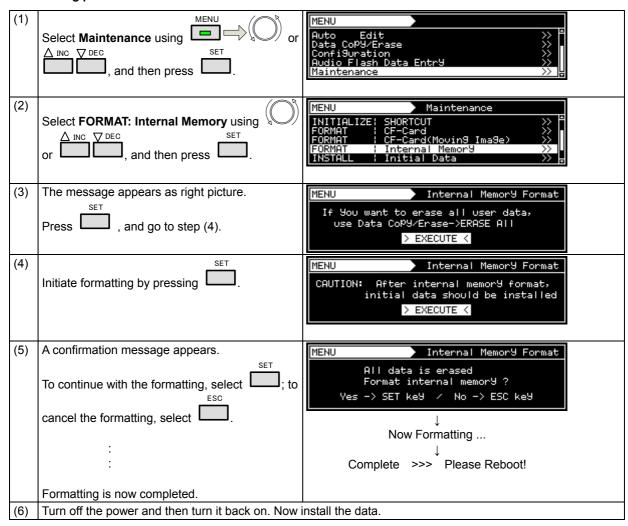
- The required data is the data in the **InitialData** folder on the SP-8870 software installation disk which is provided with the VG generator so copy it onto a CF card.
- If the firmware version has been updated at some point after the generator was purchased, the data
  of the updated version will be required.

Further action must be taken in either of the following events.

Make inquiries with your dealer or an ASTRODESIGN sales representative.

- When APDC patterns (optional) are being used APDC patterns are not included in the data provided with the SP-8870 software.
- When the VG generator is not a standard model The data provided with the SP-8870 software is for a standard model.

#### <Formatting procedure>



#### <Data installation procedure>

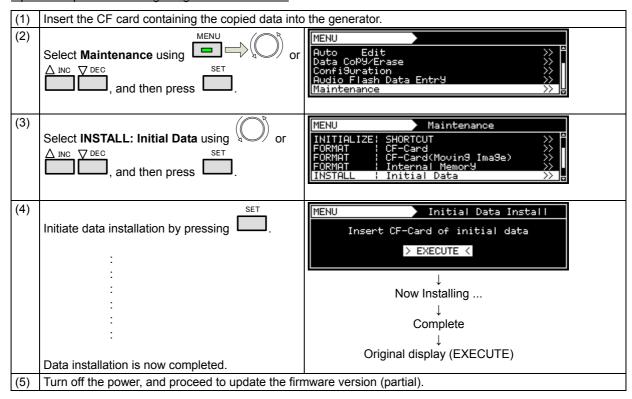
#### Preparing the data

Copy the data on the SP-8870 software installation disk (in drive A:) which is provided with the VG generator onto a CF card (in drive B:).

There is a multiple number of files on this disk.

A:¥InitialData¥VG870¥ \*.\* → B:¥ \*.\*

#### Operations performed using the generator's controls



#### <Firmware version updating procedure>

Use the data which was prepared at the data installation stage.

If the firmware version has been updated at some point after the generator was purchased, copy the data of the updated version onto a CF card.

- (1) Insert the CF card into the generator.
- (2) While holding down \_\_\_\_\_, turn on the power.

ESC

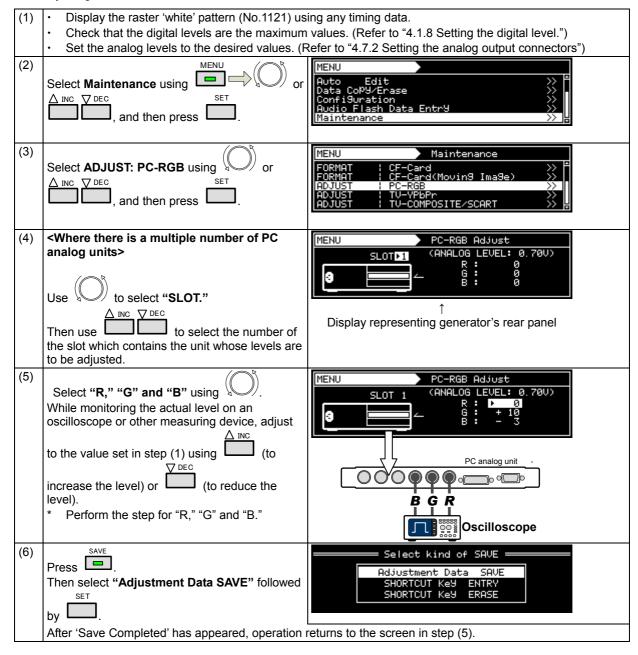
- (3) The 'FPGA/Firmware Version Up' message appears. Wait a few moments.
- (4) When the 'End. Please reboot!!' message has appeared, and the buzzer has sounded, turn off the power and reboot.

# 10.6 Adjustments

#### 10.6.1 Adjusting the RGB video levels of the PC analog unit

The RGB video levels of the PC analog unit will be adjusted in this section.

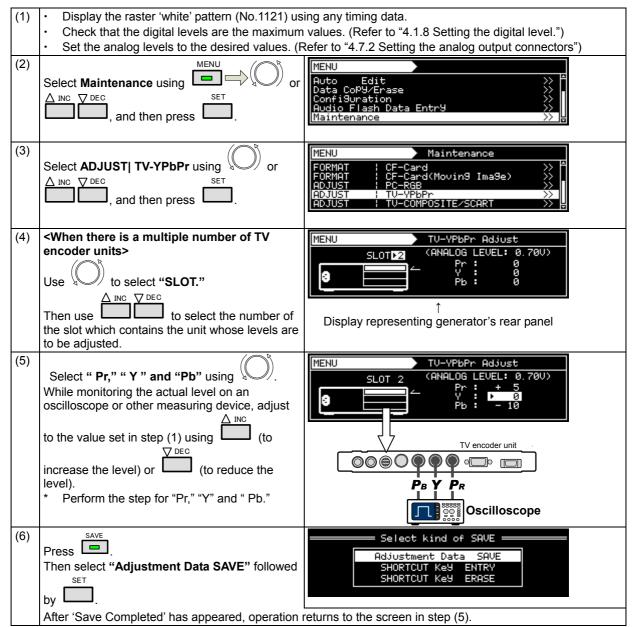
After adjusting the levels, save the data.



#### 10.6.2 Adjusting the YPbPr video levels of the TV encoder unit

The YPbPr video levels of the TV encoder unit will be adjusted in this section.

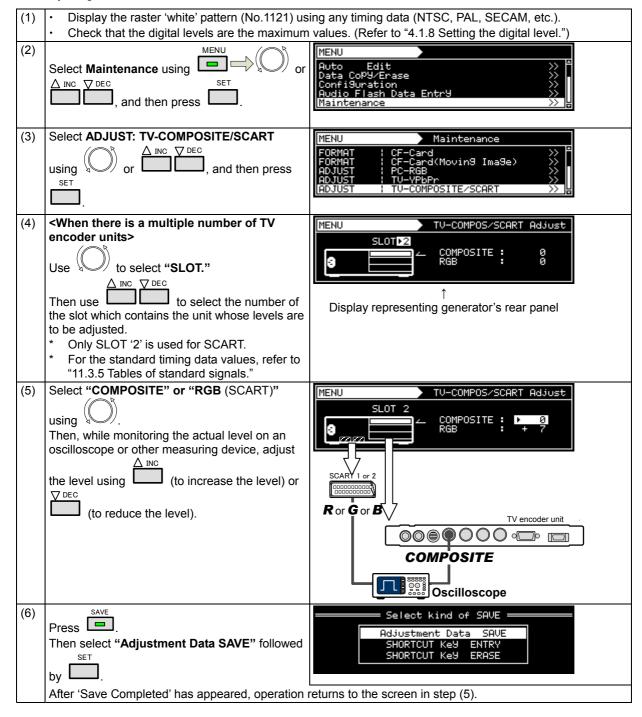
After adjusting the levels, save the data.



# 10.6.3 Adjusting the COMPOSITE/SCART video levels of the TV encoder unit

The COMPOSITE/SCART video levels of the TV encoder unit will be adjusted in this section.

After adjusting the levels, save the data.



# 11

## **SPECIFICATIONS**

## 11.1 Main specifications

#### 11.1.1 Common specifications

Dot clock frequencies	VG-870B/873	Analog	8 - 10 bit: 0.100 - 340.000 MHz 11 - 12 bit: 0.100 - 330.000 MHz 13 - 14 bit: 0.100 - 280.000 MHz	
			15 - 16 bit: 0.100 - 240.000 MHz	
		Digital	8 - 10 bit: 0.100 - 340.000 MHz	
			11 - 12 bit: 0.100 - 330.000 MHz	
			13 - 14 bit: 0.100 - 280.000 MHz	
			15 - 16 bit: 0.100 - 240.000 MHz	
			VM-1824 (with Quad output)	
			8 - 10 bit: 296.000 - 660.000 MHz	
			11 - 12 bit: 296.000 - 600.000 MHz	
	VG-871B/874	Analog	8 to 10 bit: 0.100 - 250.000 MHz	
			11 - 12 bit: 0.100 - 250.000 MHz	
			13 - 14 bit: 0.100 - 250.000 MHz	
			15 - 16 bit: 0.100 - 240.000 MHz	
		Digital	8 - 10 bit: 0.100 - 340.000 MHz	
			11 - 12 bit: 0.100 - 330.000 MHz	
			13 - 14 bit: 0.100 - 280.000 MHz	
			15 - 16 bit: 0.100 - 240.000 MHz	
			VM-1824 (with Quad output)	
			8 - 10 bit: 296.000 - 660.000 MHz	
Had a falfa a sa			11 - 12 bit: 296.000 - 600.000 MHz	
	Horizontal frequency		Max. 300 kHz, 8192 dots	
Number of vertical scanning lines		Max. 8192 lines *1		
Video memory		4096 dots × 4096 dots		
Serration pulse (Serration)			OFF, 0.5H, 1H or EXOR selectable	
Scanning		Progressive (non-interlaced), interlaced, segmented frame, interlace (sync)		

<sup>\*1</sup> The maximum number of vertical scanning lines is 2046 for the DisplayPort output.

#### 11.1.2 **HDMI** unit

HDMI1	Connectors			HDMI × 2		
HDMI2	HDMI2 DotCLK			8-bit output	25 to 165 MHz CLK:165 MHz	
				10-bit output	25 to 165 MHz CLK:206.25 MF	
				12-bit output	25 to 150 MHz CLK:225 MHz	
	No. of colors ger	nerated		8, 10 or 12 bits each YCbCr444 and YCb		
	Audio output	HDMI	L-PCM	Sampling frequency: 192 kHz	: 32, 44.1, 48, 88	3.2, 96, 176.4,
			Output frequency: 100 to one-half of sampling frequency (Hz) No. of bits: 16, 20 or 24 bits			
			Options	DSD, Dolby Digital F	io technologies supported lus, Dolby True HD, DTS HD dio), DTS HD (Master Audio),	
		COAX *2		Sampling frequency: 32, 44.1, 48, 88.2, 96, 176.4, 192 kHz		
	Audio input	RCA *1		Input format	Analog L/R	
		COAXIAL Optical *1 I2S IN (option)		Input format	S/PDIF format	
				sampling frequency	Fs = 32 to 192	kHz
				MCLK frequency *3	Fs = 48 kHz system	24.576 MH z
					Fs = 44.1 kHz system	22.5792 MHz
				Input format	Next-generatio technologies su (I2S format)	
				sampling frequency	Fs = 32 to 768	kHz
		HDMI A	RC *2	sampling frequency	Fs = 32 to 192	kHz
	Copy protection			HDCP Ver1.2		
	Additional function	ons		E-EDID Ver1.3 (DDC2B), xvYCC, CEC		

<sup>\*1:</sup> This function is supported only by the VM-1817.

<sup>\*2:</sup> This function is supported only by the VM-1617.
\*2: This function is supported only by the HDMI1.4a Unit(ARC-compatible unit).
\*3: The frequencies for 48 kHz systems are 32 kHz, 48 kHz, 96 kHz, 192 kHz and 768 kHz; the frequency for 44.1 kHz systems are 44.1 kHz, 88.2 kHz and 176.4 kHz.

#### 11.1.3 TV encoder unit

VGA	DotCLK	5 to 165 MHz	
	No. of colors generated	8 bits each for R, G, B	
	Connector	Dsub × 1	
D5	DotCLK	HDTV (1920 × 1080i/1080p/720p), SDTV (720 × 480p/480i)	
	No. of colors generated	8 bits each for R, G, B	
	Connector	D connector × 1 (D5 output supported)	
YPbPr	DotCLK	HDTV (1920 × 1080i/1080p/720p), SDTV (720 × 480p/480i)	
	No. of colors generated	8 bits each for R, G, B	
	Connector	BNC × 3	
COMPOSITE Y/C	DotCLK	NTSC-M/J/443, PAL (B/D/G/H/I)/M/N/Nc/60, SECAM See below note	
	No. of colors generated	8 bits each for R, G, B	
	Connector	COMPOSITE × 1, Y/C (S-VIDEO) × 1	
	Additional functions	Teletext, Closed Caption, V-Chip, Macrovision (options)	
SCART1 SCART2	DotCLK	NTSC-M/J/443, PAL (B/D/G/H/I)/M/N/Nc/60, SECAM See below note	
	No. of colors generated	8 bits each for R, G, B	
	Connector	SCART × 2	
	Additional functions	Teletext, Closed Caption, V-Chip, Macrovision (options)	
AUDIO	Audio output	RCA × 2	
L/R	Output frequency	20 to 20 KHz	
	Output level	0 to 2000 mV	

Note: VM-1812-B does not support PAL-N, PAL-60, SECAM. Option Pattern #77 (SMPTE color bar CVBS) is not supported.

## 11.1.4 PC analog unit

VGA	DotCLK	5 to 300 MHz	
RGB/HS/VS	No. of colors generated	10 bits each for R, G, B	
DVI-I (analog unit)	Video level	300 to 1200 mV (with OnSync ON) 50 to 1200 mV (with OnSync OFF)	
	Sync level	HS/VS: TTL OnSync: 0 to 600 mV (2-level), 0 to ±600 mV (tri-level)	
	Connector	BNC × 3 (RGB), BNC × 2 (HS/VS), Dsub × 1	
CS	Sync level	CS: 300 mV (2-level), 600 mV (tri-level)	
	Connector	BNC × 1	
DVI-I (digital unit)	DotCLK	25 to 165 MHz	
	No. of colors generated	8 bits each for R, G, B	
	Copy protection	HDCP Ver1.0	
	Additional functions	E-EDID Ver1.3 (DDC2B)	
	Connector	DVI-I × 1 (HDCP supported)	

#### 11.1.5 **DVI unit**

DVI1	DotCLK	Single Link	25 to 165 MHz (to 8 bits)		
			25 to 165 MHz (to 16 bits)		
		Dual Link	50 to 330 MHz (to 8 bits)		
	No. of colors gen	erated	16 bits each for R, G, B		
	Copy protection		None		
	Additional function	ns	E-EDID Ver1.3 (DDC2B)		
	Connector		DVI-D × 1 (Dual Link supported)		
DVI2	DotCLK		25 to 165 MHz (to 8 bits)		
	No. of colors generated		8 bits each for R, G, B		
	Copy protection		Copy protection		HDCP Ver1.0
	Additional function	ns	E-EDID Ver1.3 (DDC2B)		
	Connector		DVI-D × 1 (HDCP supported)		

#### 11.1.6 LVDS unit

LVDS1	DotCLK	Single Link	20 to 135 MHz (to 16 bits)
LVDS2		Dual Link	40 to 270 MHz (to 14 bits)
LVDS3			40 to 240 MHz (to 16 bits)
LVDS4		Quad Link	80 to 340 MHz (to 10 bits)
	No. of colors gen	erated	16 bits each for R, G, B
Connector			LVDS × 4

#### 11.1.7 PARALLEL unit

PARALLEL1	DotCLK	Single Link	0.	1 to 100 MHz (to 16 bits)		
PARALLEL2		Dual Link	0.	2 to 200 MHz (to 8 bits)		
	No. of colors gen	erated	16	bits each for R, G, B		
	Output voltage level			utput signal voltage level (S ower supply voltage level (P		
				Positions of the switches	Voltage level [V]	
					1	1.8
					2	2.5
				3	3.3	
				4	5	
	Connector		P	ARALLEL × 2		

#### 11.1.8 **DP unit**

DP1	Version support	Version supported		tandard Ver.1.1a	
DP2	Connectors		DisplayPort × 2		
	DotCLK	Single Mode	16 to 270 MHz However, according	to each setting. (*1)	
		Dual Mode	32 to 340 MHz		
		Split Mode	However, according	to each setting. (*1)	
	Audio output	L-PCM	Sampling frequency: 32/44.1/48/88.2/96 kHz Output frequency: 100 to "half of the sampling frequency" Hz Number of bit: 16/20/24 bit		
	Number of colo	Number of colors		8 or 10 bits each for R, G and B (RGB and YCbCr444 formats)	
	DisplayPort	Link Rate	1.62 GHz/2.7 GHz		
	I/F	Number of Lane	1/2/4 lanes		
	Audio input	Optical	Input format	S/PDIF format	
			Sampling frequency	Fs=32 to 96 kHz	
	Auxiliary chann	Auxiliary channel support		DPCD, EDID, DDC/CI, HDCP	

<sup>\*1</sup> The maximum dot clock for DisplayPort is as follows according to the link rate, number of lanes, drawing mode, bit length, and color format settings.

Item	Item		Maximum dot clock	Maximum dot clock				
Link Rate	Number of lanes	Drawing mode	18 bit	24 bit	30 bit			
			RGB/Y444 6 bit	RGB/Y444 8 bit	RGB/Y444 10 bit			
				Y422 12 bit				
2.7 GHz	1	Single	120 MHz	90 MHz	72 MHz			
(HBR)		Dual/Split	240 MHz	180 MHz	144 MHz			
	2	Single	240 MHz	180 MHz	144 MHz			
		Dual/Split	340 MHz	340 MHz	288 MHz			
	4	Single	270 MHz	270 MHz	270 MHz			
		Dual/Split	340 MHz	340 MHz	340 MHz			
1.62 GHz	1	Single	72 MHz	54 MHz	43.2 MHz			
(RBR)		Dual/Split	144 MHz	108 MHz	86.4 MHz			
	2	Single	144 MHz	108 MHz	86.4 MHz			
		Dual/Split	288 MHz	216 MHz	172.8 MHz			
	4	Single	270 MHz	216 MHz	172.8 MHz			
		Dual/Split	340 MHz	340 MHz	340 MHz			

<sup>\*2</sup> The DisplayPort output can be output at a horizontal timing of only 2-dot units in Single mode, or 4-dot units in Dual or Split mode.

<sup>\*3</sup> When the YCbCr4:2:2 format has been selected, up to 12 bits can be set as the bit length. However, it is not possible to display the gray scale which accords with the bit length (gray scale) which has been set (64-step gray scale with an 8-bit output). Use this parameter to check the Main Stream Attribute parameters rather than using it to assess the image quality.

<sup>\*4</sup> The maximum number of vertical scanning lines is 2046 for the DisplayPort output.

#### 11.1.9 4K2K (iTMDS, iTMDS Quad) unit

DVI1 DVI2	DotCLK	DVI MODE	8bit	25 to 165MHz (SingleLink) 50 to 330MHz (DualLink)
(DVI3) (DVI4)		iTMDS MODE	8-10bit	25 to 165MHz (SingleLink) 50 to 330MHz (DualLink)
			12bit	25 to 150MHz (SingleLink) 50 to 300MHz (DualLink)
		MULTI CH MODE	10-16bit	25 to 165MHz (DualLink) 50 to 330MHz (QuadLink)
		4Kx2K MODE	8-12bit	296 to 660MHz (QuadLink) 592 to 1320MHz (OctalLink) *1
	Number of colors	generated		16 bits each for R, G, B
	Copy protection			HDCP Ver.1.0 (scheduled) *2
	Additional function	n		E-EDID Ver1.3(DDC2B) *3

<sup>\*1</sup> Octal Link takes effect only when two output boards are used. It will not work when only one board is used.

#### Horizontal timing restrictions

Minimum horizontal blanking period
Minimum horizontal display width

168 dots (384 dots in the 2-board mode)
476 dots (996 dots in the 2-board mode)

Setting increments for VM-1824 (VM-1824-A) screen splitting

	Quad		Octal		
	Н	V	Н	V	
Mode 0	2dot	2line	4dot	2line	
Mode 1	4dot	1line	8dot	1line	
Mode 2	2dot	2line	4dot	2line	
Mode 3	4dot	1line	8dot	1line	

	Quad (x4Mode)					
	Н	V				
Mode 0	4dot(8dot)X	1line				
Mode 1	4dot(8dot)X	1line				
Mode 2	4dot(8dot)	1line				
Mode 3	4dot(8dot)X	1line				
Mode 4	4dot(8dot)※	1line				

<sup>\*</sup> When the screen is split-output into 8 using IA-1540, the setting increment is 8-dot.

<sup>\*2</sup> The VM-1824 (DVI Dual Link is equipped) does not support it.

<sup>\*3</sup> This has been supported starting with firmware version Ver.3.30.

## 11.1.10 V-by-One HS unit

V-by-One HS1	DotCLK	Normal	8-10 bit	20 to 75 MHz (1 Lane )
V-by-One HS2		MODE		40 to 150 MHz (2 Lane )
				80 to 300 MHz (4 Lane)
		4k2k (60Hz) MODE x4 MODE	8-10 bit	160 to 600 MHz (8 Lane )
	Video format			RGB、YCbCr4:4:4
	Color resolution			With 8 bits: 256 × RGB (YC) colors With 10 bits: 1024 × RGB (YC) colors
	Level setting		With 8 bits: 256 steps With 10 bits: 1024 steps	

#### • Setting increments for VM-1825 screen splitting

	8L	ane	16Lane			
	Н	V	Н	V		
Mode 0	2dot	2line	4dot	2line		
Mode 1	4dot	1line	8dot	1line		
Mode 2	2dot	2line	4dot	2line		
Mode 3	4dot	1line	8dot	1line		

	8La	ne (x4Mode)
	Н	V
Mode 0	4dot	1line
Mode 1	4dot	1line
Mode 2	4dot	1line
Mode 3	4dot	1line
Mode 4	4dot	1line

#### 11.1.11 SDI unit

The SDI outputs take effect only with the timings are rated below.

SDI1 SDI2	Formats supported	SD-SDI	NTSC PAL		YCbCr:422 10bit	SMPTE-259M
SDI3		HD-SDI	1920x1080p	30/29.97/25/24/23.98	YCbCr:422 10bit	SMPTE-274M
SDI4			1920x1080psf	30/29.97/25/24/23.98		
			1920x1080i	60/59.94/50	=	
			1280x720p	60/59.94/50	YCbCr:422 10bit	SMPTE-296M
				30/29.97/25/24/23.98		
		3G-SDI	1920x1080p	60/59.94/50	YCbCr:422 10bit	SMPTE-425M
		(LEVEL A/B)	1920x1080p	30/29.97/25/24/23.98	YCbCr:422 12bit	
			1920x1080psf	30/29.97/25/24/23.98	YCbCr:444 10bit	
			1920x1080i	60/59.94/50	YCbCr:444 12bit	
			1280x720p	60/59.94/50	RGB 10bit RGB 12bit	
				30/29.97/25	RGB 12011	
		Dual-Link	1920x1080p	60/59.94/50	YCbCr:422 10bit	SMPTE-372M
			1920x1080p	30/29.97/25/24/23.98	YCbCr:422 12bit	
			1920x1080psf	30/29.97/25/24/23.98	YCbCr:444 10bit	
			1920x1080i	60/59.94/50	YCbCr:444 12bit	
					RGB 10bit RGB 12bit	
	Audio output	L-PCM	No. of channel	s <sup>.</sup> 8	NOB 1201	
	riddio odipat	2 . 0	Sampling frequ			
			Output frequer			
			frequency) Hz	, , , , , , , , , , , , , , , , , , , ,		
			No. of bits: 16,			

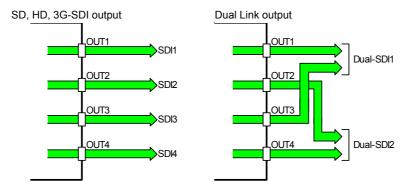
<sup>\*1</sup> The SDI outputs are as shown below.

(1) SD/HD/3G-SDI

The signals are distributed from OUT1, OUT2, OUT3 and OUT4, and output.

(2) Dual-Link

The signals are output from OUT1 and OUT3 as one pair and OUT2 and OUT4 as another pair (to form two distributed outputs).



## 11.1.12 Moving image module

Image memory	4GB (SO-DIMM 2GB × 2)								
Video format	RGB (4:4:4)/10 bit	RGB (4:4:4)/10 bit							
	YPbPr (4:2:2)/10 bit	/PbPr (4:2:2)/10 bit							
Playback time	Full HD (1920 × 1080)/60p	RGB (4:4:4) approx. 8 sec.							
		YPbPr (4:2:2) approx. 12 sec.							

## 11.1.13 External control

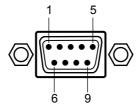
Dedicated remote controllers	RB-1870, RB-1871
Serial control	RS-232C
LAN	10/100BASE-T

#### 11.1.14 General specifications

Supply voltage	AC100 to 240 V
Power line frequency	50/60 Hz
Power consumption	80 VA MAX
Dimensions	430 (W) × 88 (H) × 370 (D) mm (excluding protrusions)
Weight	Approx. 6.85 kg (when 3 output units have been installed)
Operating temperature range	5 to 40°C
Operating humidity range	30 to 80%RH (no condensation)

## 11.2 Connector specifications

#### 11.2.1 RS232C-Connector



Pin no.	I/O	Signal
1	-	NC
2	0	TXD (transmitted data)
3	Ι	RXD (received data)
4	-	Shorted with pin 6
5	-	FG (frame ground)
6	-	Shorted with pin 4
7	I	CTS (clear to send)
8	0	RTS (request to send)
9	-	NC

## 11.2.2 Trigger-Connector



Pin no.	I/O	Signal
1	0	TRIG_OUT3
2	0	TRIG_OUT2
3	0	TRIG_OUT1
4	-	GND
5	0	TRIG_OUT0
6	-	GND
7	I	RESEARVE
8	-	GND

 $^{\star}$   $\;$  The output of trigger is an open-collector output. It is pulled up by 10 k $\!\Omega,$  5 V internally.

## 11.3 Internal data

## 11.3.1 Program data

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog		rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1001	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	EIA640 × 480p@59.94	100%/100% color bars, horizontal direction	Color Bar 100/100-H
1002	31.50	60.00	25.200	640 × 480	Prog	N	Ν	ANALOG	RGB	EIA640 × 480p@60	100%/75% color bars, horizontal direction	Color Bar 100/75-H
1003	31.47	59.94	27.000	720 × 480	Prog	N	N	ANALOG	YPbPr	EIA720 × 480p@59.94	75%/75% color bars, horizontal direction	Color Bar 75/75-H
1004	31.50	60.00	27.027	720 × 480	Prog	Ν	Ν	ANALOG	YPbPr	EIA720 × 480p@60	SMPTE color bars	Color Bar SMPTE
1005	31.47	59.94	27.000	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480pW@59.94	RGBW color bars, vertical direction	Color Bar RGBW-V
1006	31.50	60.00	27.027	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480pW@60	xvYCC 4% color bars	Color Bar xvYCC 4%
1007	44.96	59.94	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA1280 × 720p@59.94	xvYCC 8% color bars	Color Bar xvYCC 8%
1008	45.00	60.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA1280 × 720p@60	xvYCC 12% color bars	Color Bar xvYCC 12%
1009	33.72	59.94	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080i@59.94	100%/100% color bars, horizontal direction 2	Color Bar 100/100-H2
1010	33.75	60.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080i@60		
1011	15.73	59.94	27.000	1440 × 480	Int	N	N	ANALOG	YPbPr	EIA1440 × 480i@59.94		
1012	15.75	60.00	27.028	1440 × 480	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 480i@60		
1013	15.73	59.94	27.000	1440 × 480	Int	N	N	ANALOG	YPbPr	EIA1440 × 480iW@59.94		
1014	15.75	60.00	27.028	1440 × 480	Int	N	N	ANALOG	YPbPr	EIA1440 × 480iW@60		
1015	15.73	60.05	27.000	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240p@59.94		
1016	15.75	60.12	27.028	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240p@60		
1017	15.73	59.83	27.000	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240p@59.94		
1018	15.75	59.89	27.028	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240p@60		
1019	15.73	60.05	27.000	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240pW@59.94		
1020	15.75	60.12	27.028	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240pW@60		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	rnc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1021	15.73	59.83	27.000	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240pW@59.94		
1022	15.75	59.89	27.028	1440 × 240	Prog	N	N	ANALOG	YPbPr	EIA1440 × 240pW@60		
1023	15.73	59.94	54.000	2880 × 480	Int	N	N	ANALOG	YPbPr	EIA2880 × 480i@59.94		
1024	15.75	60.00	54.054	2880 × 480	Int	Ν	Ν	ANALOG	YPbPr	EIA2880 × 480i@60		
1025	15.73	59.94	54.000	2880 × 480	Int	Ν	Ν	ANALOG	YPbPr	EIA2880 × 480iW@59.94		
1026	15.75	60.00	54.054	2880 × 480	Int	Ν	Ν	ANALOG	YPbPr	EIA2880 × 480iW@60		
1027	15.73	60.05	54.000	2880 × 240	Prog	Ν	Ν	ANALOG	YPbPr	EIA2880 × 240p@59.94		
1028	15.75	60.11	54.054	2880 × 240	Prog	Ν	Ν	ANALOG	YPbPr	EIA2880 × 240p@60		
1029	15.73	59.83	54.000	2880 × 240	Prog	Ν	Ν	ANALOG	YPbPr	EIA2880 × 240p@59.94		
1030	15.75	59.89	54.054	2880 × 240	Prog	Ν	Ν	ANALOG	YPbPr	EIA2880 × 240p@59.94		
1031	15.73	60.05	54.000	2880 × 240	Prog	N	Ν	ANALOG	YPbPr	EIA2880 × 240pW@59.94	Gray scale, horizontal direction (4 steps)	Gray Scale H-4step
1032	15.75	60.11	54.054	2880 × 240	Prog	N	Ν	ANALOG	YPbPr	EIA2880 × 240pW@60	Gray scale, horizontal direction (8 steps)	Gray Scale H-8step
1033	15.73	59.83	54.000	2880 × 240	Prog	N	Ν	ANALOG	YPbPr	EIA2880 × 240pW@59.94	Gray scale, horizontal direction (16 steps)	Gray Scale H-16step
1034	15.75	59.89	54.054	2880 × 240	Prog	N	N	ANALOG	YPbPr	EIA2880 × 240pW@60	Gray scale, horizontal direction (32 steps)	Gray Scale H-32step
1035	31.47	59.94	54.000	1440 × 480	Prog	N	N	ANALOG	YPbPr	EIA1440 × 480p@59.94	Gray scale, horizontal direction (64 steps)	Gray Scale H-64step
1036	31.50	60.00	54.054	1440 × 480	Prog	N	N	ANALOG	YPbPr	EIA1440 × 480p@60	Gray scale, horizontal direction (128 steps)	Gray Scale H-128step
1037	31.47	59.94	54.000	1440 × 480	Prog	N	N	ANALOG	YPbPr	EIA1440 × 480pW@59.94	Gray scale, horizontal direction (256 steps)	Gray Scale H-256step
1038	31.50	60.00	54.054	1440 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA1440 × 480pW@60	Gray scale, vertical direction (4 steps)	Gray Scale V-4step
1039	67.43	59.94	148.352	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@59.94	Gray scale, vertical direction (8 steps)	Gray Scale V-8step
1040	67.50	60.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@60	Gray scale, vertical direction (16 steps)	Gray Scale V-16step

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog		nc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1041	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	EIA720 × 576p@50	Gray scale, vertical direction (32 steps)	Gray Scale V-32step
1042	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	EIA720 × 576pW@50	Gray scale, vertical direction (64 steps)	Gray Scale V-64step
1043	37.50	50.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA1280 × 720p@50	Gray scale, vertical direction (128 steps)	Gray Scale V-128step
1044	28.13	50.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080i@50	Gray scale, vertical direction (256 steps)	Gray Scale V-256step
1045	15.63	50.00	27.000	1440 × 576	Int	Ν	Ν	ANALOG	YPbPr	EIA1440 × 576i@50	Linear ramp, horizontal direction	Ramp Linear-H
1046	15.63	50.00	27.000	1440 × 576	Int	N	N	ANALOG	YPbPr	EIA1440 × 576iW@50	Linear ramp, vertical direction	Ramp Linear-V
1047	15.63	50.08	27.000	1440 × 288	Prog	N	N	ANALOG	YPbPr	EIA1440 × 288p@50	Linear ramp, horizontal and vertical directions	Ramp Linear-HV
1048	15.63	49.92	27.000	1440 × 288	Prog	N	N	ANALOG	YPbPr	EIA1440 × 288p@50		Ramp Linear-H RGBW-H
1049	15.63	49.76	27.000	1440 × 288	Prog	N	N	ANALOG	YPbPr	EIA1440 × 288p@50		Ramp Linear-V RGBW-V
1050	15.63	50.08	27.000	1440 × 288	Prog	N	N	ANALOG	YPbPr	EIA1440 × 288pW@50		Ramp Linear-H RGBW-V
1051	15.63	49.92	27.000	1440 × 288	Prog	N	Ν	ANALOG	YPbPr	EIA1440 × 288pW@50	Turn ramp	Ramp-H 1Level/dot
1052	15.63	49.76	27.000	1440 × 288	Prog	Ν	N	ANALOG	YPbPr	EIA1440 × 288pW@50		
1053	15.63	50.00	54.000	2880 × 576	Int	Ν	Ν	ANALOG	YPbPr	EIA2880 × 576i@50		Ramp Linear H:G V:R
1054	15.63	50.00	54.000	2880 × 576	Int	Ν	Ν	ANALOG	YPbPr	EIA2880 × 576iW@50		Ramp Linear H:B V:R
1055	15.63	50.08	54.000	2880 × 288	Prog	Ν	Ν	ANALOG	YPbPr	EIA2880 × 288p@50		Ramp Linear H:B V:G
1056	15.63	49.92	54.000	2880 × 288	Prog	Ν	Ν	ANALOG	YPbPr	EIA2880 × 288p@50		Ramp Linear H:R V:G
1057	15.63	49.76	54.000	2880 × 288	Prog	N	Ν	ANALOG	YPbPr	EIA2880 × 288p@50		Ramp Linear H:R V:B
1058	15.63	50.08	54.000	2880 × 288	Prog	N	N	ANALOG	YPbPr	EIA2880 × 288pW@50		Ramp Linear H:G V:B
1059	15.63	49.92	54.000	2880 × 288	Prog	N	N	ANALOG	YPbPr	EIA2880 × 288pW@50	128-step gray scale ramp (top: $R \rightarrow L$ , bottom: $R \leftarrow L$ )	Ramp 128 R->L L->R
1060	15.63	49.76	54.000	2880 × 288	Prog	N	N	ANALOG	YPbPr	EIA2880 × 288pW@50	256-step gray scale ramp (top: R $\rightarrow$ L, bottom: R $\leftarrow$ L)	Ramp 256 R->L L->R

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog		rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1061	31.25	50.00	54.000	1440 × 576	Prog	N	N	ANALOG	YPbPr	EIA1440 × 576p@50		
1062	31.25	50.00	54.000	1440 × 576	Prog	N	N	ANALOG	YPbPr	EIA1440 × 576pW@50		
1063	56.25	50.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@50		
1064	26.97	23.98	74.176	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@23.97		
1065	27.00	24.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@24		
1066	28.13	25.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@25		
1067	33.72	29.97	74.176	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@29.97		
1068	33.75	30.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080p@30		
1069	31.47	59.94	108.000	2880 × 480	Prog	Ν	N	ANALOG	YPbPr	EIA2880 × 480p@59.94		
1070	31.50	60.00	108.108	2880 × 480	Prog	Ν	N	ANALOG	YPbPr	EIA2880 × 480p@60		
1071	31.47	59.94	108.000	2880 × 480	Prog	N	N	ANALOG	YPbPr	EIA2880 × 480pW@59.94	Linear ramp, horizontal direction + scroll	Ramp Linear-H Scroll
1072	31.50	60.00	108.108	2880 × 480	Prog	N	N	ANALOG	YPbPr	EIA2880 × 480pW@60	Linear ramp, vertical direction + scroll	Ramp Linear-V Scroll
1073	31.25	50.00	108.000	2880 × 576	Prog	N	N	ANALOG	YPbPr	EIA2880 × 576p@50	Linear ramp, horizontal and vertical directions + scroll	Ramp Linear-HV Scroll
1074	31.25	50.00	108.000	2880 × 576	Prog	N	N	ANALOG	YPbPr	EIA2880 × 576pW@50		
1075	31.25	50.00	72.000	1920 × 1080	Int	Р	N	HDTV1250 (AUS)	YPbPr	EIA1920 × 1080i@50		
1076	56.25	100.00	148.500	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080i@100		
1077	75.00	100.00	148.500	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA1280 × 720p@100		
1078	62.50	100.00	54.000	720 × 576	Prog	Ν	N	ANALOG	YPbPr	EIA720 × 576p@100		
1079	62.50	100.00	54.000	720 × 576	Prog	N	N	ANALOG	YPbPr	EIA720 × 576pW@100		
1080	31.25	100.00	54.000	1440 × 576	Int	N	N	ANALOG	YPbPr	EIA1440 × 576i@100		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1081	31.25	100.00	54.000	1440 × 576	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 576iW@100		
1082	67.43	119.88	148.352	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080i@119.88		
1083	67.50	120.00	148.500	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1920 × 1080i@120		
1084	89.91	119.88	148.352	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA1280 × 720p@119.88		
1085	90.00	120.00	148.500	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA1280 × 720p@120		
1086	62.94	119.88	54.000	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480p@119.88		
1087	63.00	120.00	54.054	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480p@120		
1088	62.94	119.88	54.000	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480pW@119.88		
1089	63.00	120.00	54.054	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480pW@120		
1090	31.47	119.88	54.000	1440 × 480	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 480i@119.88		
1091	31.50	120.00	54.054	1440 × 480	Int	Ν	Ν	ANALOG	YPbPr	EIA1440 × 480i@120		
1092	31.47	119.88	54.000	1440 × 480	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 480iW@119.88		
1093	31.50	120.00	54.054	1440 × 480	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 480iW@120		
1094	125.00	200.00	108.000	720 × 576	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 576p@200		
1095	125.00	200.00	108.000	720 × 576	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 576pW@200		
1096	62.50	200.00	108.000	1440 × 576	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 576i@200		
1097	62.50	200.00	108.000	1440 × 576	Int	N	Ν	ANALOG	YPbPr	EIA1440 × 576iW@200		
1098	125.87	239.76	108.000	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480p@239.76		
1099	126.00	240.00	108.108	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480p@240		
1100	125.87	239.76	108.000	720 × 480	Prog	N	Ν	ANALOG	YPbPr	EIA720 × 480pW@239.76		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sync polarity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1101	126.00	240.00	108.108	720 × 480	Prog	N N	ANALOG	YPbPr	EIA720 × 480pW@240	Multi burst 100%	Multi Burst 100%
1102	62.94	239.76	108.000	1440 × 480	Int	N N	ANALOG	YPbPr	EIA1440 × 480i@239.76	Multi burst 50%	Multi Burst 50%
1103	63.00	240.00	108.108	1440 × 480	Int	N N	ANALOG	YPbPr	EIA1440 × 480i@240	Sweep pattern	Sweep
1104	62.94	239.76	108.000	1440 × 480	Int	N N	ANALOG	YPbPr	EIA1440 × 480iW@239.76	APDC pattern	APDC
1105	63.00	240.00	108.108	1440 × 480	Int	N N	ANALOG	YPbPr	EIA1440 × 480iW@240		
1106											
1107											
1108											
1109											
1110											
1111										OPT38 (SMPTE RP-133)	SMPTE RP-133
1112										OPT39 (SMPTE color version)	SMPTE RP-133+Color
1113										Monoscope	Monoscope
1114										Philips pattern	Philips
1115										Chinese monoscope	China Monoscope
1116										APDC1	APDC1
1117			_							APDC2	APDC2
1118										APDC3	APDC3
1119										APDC4	APDC4
1120											

<sup>\*</sup> Programs No. 1116 to 1119 require license registration. When the license is not input, a license error results. For information on purchasing a license, contact ASTRODESIGN sales.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sync polarity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1121										White solid	Raster White
1122										Red solid	Raster Red
1123										Green solid	Raster Green
1124										Blue solid	Raster Blue
1125										Black solid	Raster Black
1126										50% solid gray	Raster 50%Gray
1127										Magenta solid	Raster Magenta
1128										Cyan solid	Raster Cyan
1129										Yellow solid	Raster Yellow
1130											
1131											
1132											
1133											
1134											
1135											
1136											
1137											
1138											
1139											
1140											

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog		rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1141											Overscan pattern	Over Scan
1142											AFD pattern 4:3 Type 0	AFD 4:3 Type0
1143											AFD pattern 4:3 Type 1	AFD 4:3 Type1
1144											AFD pattern 4:3 Type 2	AFD 4:3 Type2
1145											AFD pattern 4:3 Type 3	AFD 4:3 Type3
1146											AFD pattern 4:3 Type 4	AFD 4:3 Type4
1147											AFD pattern 4:3 Type 5	AFD 4:3 Type5
1148											AFD pattern 4:3 Type 6	AFD 4:3 Type6
1149											AFD pattern 4:3 Type 7	AFD 4:3 Type7
1150											AFD pattern 4:3 Type 8	AFD 4:3 Type8
1151	31.47	59.94	27.000	720 × 480	Prog	N	N	ANALOG	YPbPr	EIA480p59-YCC-12	AFD pattern 4:3 Type 9	AFD 4:3 Type9
1152	33.72	59.94	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1080i59-YCC-12	AFD pattern 4:3 Type 10	AFD 4:3 Type10
1153	44.96	59.94	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA720p59-YCC-12	AFD pattern 4:3 Type 11	AFD 4:3 Type11
1154	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	EIA480p59-YCC-12	AFD pattern 4:3 Type 12	AFD 4:3 Type12
1155	67.43	59.94	148.352	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p59-YCC-12	AFD pattern 16:9 Type 0	AFD 16:9 Type0
1156	15.73	59.94	27.000	1440 × 480	Int	N	N	ANALOG	YPbPr	EIA480i59-YCC-12	AFD pattern 16:9 Type 1	AFD 16:9 Type1
1157	27.00	24.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p24-YCC-12	AFD pattern 16:9 Type 2	AFD 16:9 Type2
1158	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	EIA576p50-YCC-12	AFD pattern 16:9 Type 3	AFD 16:9 Type3
1159	28.13	50.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1080i50-YCC-12	AFD pattern 16:9 Type 4	AFD 16:9 Type4
1160	37.50	50.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA720p50-YCC-12	AFD pattern 16:9 Type 5	AFD 16:9 Type5

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1161	56.25	50.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p50-YCC-12	AFD pattern 16:9 Type 6	AFD 16:9 Type6
1162	15.63	50.00	27.000	1440 × 576	Int	N	N	ANALOG	YPbPr	EIA576i50-YCC-12	AFD pattern 16:9 Type 7	AFD 16:9 Type7
1163	28.13	25.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p25-YCC-12	AFD pattern 16:9 Type 8	AFD 16:9 Type8
1164											AFD pattern 16:9 Type 9	AFD 16:9 Type9
1165											AFD pattern 16:9 Type 10	AFD 16:9 Type10
1166											AFD pattern 16:9 Type 11	AFD 16:9 Type11
1167											AFD pattern 16:9 Type 12	AFD 16:9 Type12
1168												
1169												
1170												
1171	31.47	59.94	27.000	720 × 480	Prog	N	N	ANALOG	YPbPr	EIA480p59-RGB-12		
1172	33.72	59.94	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1080i59-RGB-12		
1173	44.96	59.94	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA720p59-RGB-12		
1174	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	EIA480p59-RGB-12		
1175	67.43	59.94	148.352	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p59-RGB-12		
1176	15.73	59.94	27.000	1440 × 480	Int	N	N	ANALOG	YPbPr	EIA480i59-RGB-12		
1177	27.00	24.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p24-RGB-12		
1178	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	EIA576p50-RGB-12		
1179	28.13	50.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1080i50-RGB-12		
1180	37.50	50.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA720p50-RGB-12		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Syl pola H	nc rity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1181	56.25	50.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p50-RGB-12		
1182	15.63	50.00	27.000	1440 × 576	Int	N	N	ANALOG	YPbPr	EIA576i50-RGB-12		
1183	28.13	25.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p25-RGB-12		
1184												
1185												
1186												
1187												
1188												
1189												
1190												
1191	33.72	59.94	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1080i59-YCC-12-xv		
1192	44.96	59.94	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA720p59-YCC-12-xv		
1193	67.43	59.94	148.352	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p59-YCC-12-xv		
1194	27.00	24.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p24-YCC-12-xv		
1195	28.13	50.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	EIA1080i50-YCC-12-xv		
1196	37.50	50.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	EIA720p50-YCC-12-xv		
1197	56.25	50.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p50-YCC-12-xv		
1198	28.13	25.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	EIA1080p25-YCC-12-xv		
1199												
1200												

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1201	134.87	59.94	296.704	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 FramePack	1-dot × 1-dot checker	Checker 1dot*1dot
1202	112.50	50.00	297.000	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 FramePack	2-dot × 1-dot checker	Checker 2dot*1dot
1203	67.43	29.97	148.352	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p30 FramePack	4-dot × 1-dot checker	Checker 4dot*1dot
1204	53.95	23.98	148.352	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 FramePack	4 × 4 checker	Checker 4*4
1205	67.43	29.97	148.352	1920x2228	Prog	Р	Р	ANALOG	YPbPr	3D 1080i60 FramePack	8 × 8 checker	Checker 8*8
1206	56.25	25.00	148.500	1920x2228	Prog	Р	Р	ANALOG	YPbPr	3D 1080i50 FramePack	Sub-pixel checker	SubPixel
1207	89.91	59.94	148.352	1280x1470	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 FramePack		
1208	75.00	50.00	148.500	1280x1470	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 FramePack		
1209	44.96	29.97	148.352	1280x1470	Prog	Р	Р	ANALOG	YPbPr	3D 720p30 FramePack		
1210	35.96	23.98	118.681	1280x1470	Prog	Р	Р	ANALOG	YPbPr	3D 720p24 FramePack		
1211	62.94	59.94	54.000	720x1005	Prog	N	N	ANALOG	YPbPr	3D 480p60 FramePack		
1212	31.47	29.97	54.000	1440x1028	Prog	N	N	ANALOG	YPbPr	3D 480i60 FramePack		
1213	62.50	50.00	54.000	720x1201	Prog	N	N	ANALOG	YPbPr	3D 576p50 FramePack		
1214	31.25	25.00	54.000	1440x1226	Prog	N	N	ANALOG	YPbPr	3D 576i50 FramePack		
1215	62.94	59.94	50.350	640x1005	Prog	N	N	ANALOG	RGB	3D VGAp60 FramePack		
1216									_			
1217												
1218												
1219												
1220												

<sup>\*</sup> For program no. 1201 to 1215, the requisite license must be registered for the 3D Vendor Specific InfoFrame output. When the license is not input, a license error results.

For information on purchasing a license, contact ASTRODESIGN sales.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	rnc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1221	67.43	59.94	148.352	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 Side_half	Character list 7 × 9	Character List 7*9
1222	56.25	50.00	148.500	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 Side_half	Character H ( 5 × 7 / 10 × 14 )	Character all H5*7
1223	33.72	29.97	74.176	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p30 Side_half	Character H ( 7 × 9 / 14 × 18 )	Character all H7*9
1224	26.97	23.98	74.176	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 Side_half	Character H ( 16 × 16 / 32 × 32 )	Character all H16*16
1225	33.72	59.94	74.176	1920x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i60 Side_half	Corner & center character H ( 5 × 7 / 10 × 14 )	Chara Cor&Cen H5*7
1226	28.13	50.00	74.250	1920x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i50 Side_half	Corner & center character H ( 7 × 9 / 14 × 18 )	Chara Cor&Cen H7*9
1227	44.96	59.94	74.176	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 Side_half	Corner & center character H ( 16 × 16 / 32 × 32 )	Chara Cor&Cen H16*16
1228	37.50	50.00	74.250	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 Side_half	Chinese character "BI" ( 7 × 9 / 64 × 64 )	Chara all Chinese
1229	22.48	29.97	74.176	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p30 Side_half	Character "me" 18 × 18	Chara all me
1230	17.98	23.98	59.341	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p24 Side_half	Character "me" 18 × 18 (VESA specifications)	Chara all me (VESA)
1231	31.47	59.94	27.000	720x480	Prog	N	N	ANALOG	YPbPr	3D 480p60 Side_half		
1232	15.73	59.94	27.000	1440x480	Int	N	N	ANALOG	YPbPr	3D 480i60 Side_half		
1233	31.25	50.00	27.000	720x576	Prog	N	Ν	ANALOG	YPbPr	3D 576p50 Side_half		
1234	15.63	50.00	27.000	1440x576	Int	N	N	ANALOG	YPbPr	3D 576i50 Side_half		
1235	31.47	59.94	25.175	640x480	Prog	N	N	ANALOG	RGB	3D VGAp60 Side_half		
1236												
1237												
1238												
1239												
1240												

<sup>\*</sup> For program no. 1221 to 1235, the requisite license must be registered for the 3D Vendor Specific InfoFrame output. When the license is not input, a license error results.

For information on purchasing a license, contact ASTRODESIGN sales.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1241	67.43	59.94	148.352	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 Top&Bot	Crosshatch pattern	Cross Hatch
1242	56.25	50.00	148.500	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 Top&Bot		
1243	33.72	29.97	74.176	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p30 Top&Bot		
1244	26.97	23.98	74.176	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 Top&Bot		
1245	33.72	59.94	74.176	1920x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i60 Top&Bot		
1246	28.13	50.00	74.250	1920x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i50 Top&Bot		
1247	44.96	59.94	74.176	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 Top&Bot		
1248	37.50	50.00	74.250	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 Top&Bot		
1249	22.48	29.97	74.176	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p30 Top&Bot		
1250	17.98	23.98	59.341	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p24 Top&Bot		
1251	31.47	59.94	27.000	720x480	Prog	N	Ν	ANALOG	YPbPr	3D 480p60 Top&Bot	H=20, V=20 dot pattern	Dot H=20,V=20
1252	15.73	59.94	27.000	1440x480	Int	N	Ν	ANALOG	YPbPr	3D 480i60 Top&Bot	H=60, V=60 dot pattern	Dot H=60,V=60
1253	31.25	50.00	27.000	720x576	Prog	Ν	Ν	ANALOG	YPbPr	3D 576p50 Top&Bot		
1254	15.63	50.00	27.000	1440x576	Int	Ν	Ν	ANALOG	YPbPr	3D 576i50 Top&Bot		
1255	31.47	59.94	25.175	640x480	Prog	Ν	Ν	ANALOG	RGB	3D VGAp60 Top&Bot		
1256												
1257												
1258												
1259												
1260												

<sup>\*</sup> For program no. 1241 to 1255, the requisite license must be registered for the 3D Vendor Specific InfoFrame output. When the license is not input, a license error results.

For information on purchasing a license, contact ASTRODESIGN sales.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1261	67.43	59.94	296.704	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 Side_full	Edge marker pattern	Edge Marker
1262	56.25	50.00	297.000	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 Side_full	Diagonal line pattern	Diagonal Line
1263	33.72	29.97	148.352	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p30 Side_full	Center marker pattern	Center Marker
1264	26.97	23.98	148.352	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 Side_full		
1265	33.72	59.94	148.352	3840x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i60 Side_full		
1266	28.13	50.00	148.500	3840x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i50 Side_full		
1267	44.96	59.94	148.352	2560x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 Side_full		
1268	37.50	50.00	148.500	2560x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 Side_full		
1269	22.48	29.97	148.352	2560x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p30 Side_full		
1270	17.98	23.98	118.681	2560x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p24 Side_full		
1271	31.47	59.94	54.000	1440x480	Prog	N	Ν	ANALOG	YPbPr	3D 480p60 Side_full	Circle (Format 0)	Circle Format0
1272	15.73	59.94	54.000	2880x480	Int	N	Ν	ANALOG	YPbPr	3D 480i60 Side_full	Circle (Format 1)	Circle Format1
1273	31.25	50.00	54.000	1440x576	Prog	N	Ν	ANALOG	YPbPr	3D 576p50 Side_full	Circle (Format 2)	Circle Format2
1274	15.63	50.00	54.000	2880x576	Int	N	N	ANALOG	YPbPr	3D 576i50 Side_full	Circle (Format 3)	Circle Format3
1275	31.47	59.94	50.350	1280x480	Prog	N	N	ANALOG	RGB	3D VGAp60 Side_full	Circle (Format 4)	Circle Format4
1276											Circle (Format 5)	Circle Format5
1277											Circle (Format 6)	Circle Format6
1278												
1279												
1280												

<sup>\*</sup> For program no. 1261 to 1275, the requisite license must be registered for the 3D Vendor Specific InfoFrame output. When the license is not input, a license error results.

For information on purchasing a license, contact ASTRODESIGN sales.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sync polarity	Color difference	Timing data name	Pattern data	Pattern data name
1281									Burst $L \rightarrow R$	Burst L->R
1282									Burst L ← R	Burst L<-R
1283									Burst L $\leftarrow$ C $\rightarrow$ R	Burst L<-C->R
1284									Burst $L \rightarrow C \leftarrow R$	Burst L->C<-R
1285									Burst T $\rightarrow$ B	Burst T->B
1286									Burst T ← B	Burst T<-B
1287									Burst $T \leftarrow C \rightarrow B$	Burst T<-C->B
1288									Burst $T \rightarrow C \leftarrow B$	Burst T->C<-B
1289										
1290										
1291										
1292										
1293										
1294										
1295										
1296										
1297										
1298										
1299										
1300										

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1301	15.73	59.94	27.000	1440x487	Int	Ν	N	ANALOG	YPbPr	SD-SDI 487i@59.94	1 window	1 Window
1302	15.63	50.00	27.000	1440x576	Int	Ν	N	ANALOG	YPbPr	SD-SDI 576i@50	4 windows	4 Window
1303											9 windows	9 Window
1304											16 windows	16 Window
1305											25 windows	25 Window
1306											64 windows	64 Window
1307											3 windows, vertical direction	3 Window in V Row
1308											3 windows, horizontal direction	3 Window in H Row
1309											Window user position/center	User pos-Center
1310											Window user position/corner	User pos-Corner
1311	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@60i	Window scroll: Left	Window Scroll:Left
1312	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@59.94i	Window scroll: Right	Window Scroll:Right
1313	28.13	50.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@50i	Window scroll: Up	Window Scroll:Up
1314	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@30sf	Window scroll: Down	Window Scroll:Down
1315	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@29.97sf	Window scroll: Top left	Window Scroll:L Up
1316	28.13	50.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@25sf	Window scroll: Bottom left	Window Scroll:L Down
1317	27.00	48.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@24sf	Window scroll: Top right	Window Scroll:R Up
1318	26.97	47.96	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	HD-SDI 1080@23.98sf	Window scroll: Bottom right	Window Scroll:R Down
1319	45.00	60.00	74.250	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@60p	Window scroll L ⇔ R	Window Scroll:L<->R
1320	44.96	59.94	74.176	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@59.94p	Window scroll: Up ⇔ down	Window Scroll:Up<->D

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1321	22.50	30.00	74.250	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@30p	Window scroll: Random	Window Scroll:Random
1322	22.48	29.97	74.176	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@29.97p	Window + monoscope	Window & Monoscope
1323	18.75	25.00	74.250	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@25p	Window: 2-3 pull-down	Window 2-3pull down
1324	18.00	24.00	74.250	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@24p	0% window	Window HV Size 0%
1325	17.98	23.98	74.176	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@23.98p	5% window	Window HV Size 5%
1326	37.50	50.00	74.250	1280x720	Prog	Р	Р	HDTV720	YPbPr	HD-SDI 720@50p	10% window	Window HV Size 10%
1327											20% window	Window HV Size 20%
1328											30% window	Window HV Size 30%
1329											40% window	Window HV Size 40%
1330											50% window	Window HV Size 50%
1331	67.50	60.00	148.500	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	3G-A 60p YC422 10b	60% window	Window HV Size 60%
1332	67.43	59.94	148.352	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	3G-A 59p YC422 10b	70% window	Window HV Size 70%
1333	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-A 60i RGB 12b	80% window	Window HV Size 80%
1334	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-A 59i RGB 12b	90% window	Window HV Size 90%
1335	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-A 60i YC444 12b	100% window	Window HV Size 100%
1336	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-A 59i YC444 12b	Window: Flicker 1 V	Window Flicker 1 V
1337	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-A 60i YC422 12b	Window: Flicker 2 V	Window Flicker 2 V
1338	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-A 59i YC422 12b	Window: Flicker 3 V	Window Flicker 3 V
1339	27.00	24.00	74.250	2048x1080	Prog	Р	Р	ANALOG	YPbPr	3G-A DCI RGB 12b	Window: Flicker 4 V	Window Flicker 4 V
1340											Window: Level Up	Window Auto Level

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	pola	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1341	67.50	60.00	148.500	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	3G-B 60p YC422 10b	Bar: L → R	Moving Bar
1342	67.43	59.94	148.352	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	3G-B 59p YC422 10b		
1343	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-B 60i RGB 12b		
1344	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-B 59i RGB 12b		
1345	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-B 60i YC444 12b		
1346	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-B 59i YC444 12b		
1347	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-B 60i YC422 12b		
1348	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	3G-B 59i YC422 12b		
1349	27.00	24.00	74.250	2048x1080	Prog	Р	Р	ANALOG	RGB	3G-B DCI RGB 12b		
1350												
1351	67.50	60.00	148.500	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	Dual 60p YC422 10b		
1352	67.43	59.94	148.352	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	Dual 59p YC422 10b		
1353	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	Dual 60i RGB 12b		
1354	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	Dual 59i RGB 12b		
1355	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	Dual 60i YC444 12b		
1356	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	Dual 59i YC444 12b		
1357	33.75	60.00	74.250	1920x1080	Int	Р	Р	HDTV1080	YPbPr	Dual 60i YC422 12b		
1358	33.72	59.94	74.176	1920x1080	Int	Р	Р	HDTV1080	YPbPr	Dual 59i YC422 12b		
1359	27.00	24.00	74.250	2048x1080	Prog	Р	Р	HDTV720	YPbPr	Dual DCI RGB 12b		
1360												

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Syno polari H	ty V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1361	56.25	50.00	148.500	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	3G-A 50p YC422 10b		
1362	56.25	50.00	148.500	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	3G-B 50p YC422 10b		
1363	56.25	50.00	148.500	1920x1080	Prog	Р	Р	HDTV1080	YPbPr	Dual 50p YC422 10b		
1364												
1365												
1366												
1367												
1368												
1369												
1370												
1371												
1372												
1373												
1374												
1375												
1376												
1377		-										
1378												
1379												
1380				-						_		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Sy pola H	rnc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1381	135.00	60.00	594.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p60 s0		
1382	135.00	60.00	594.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p60 s1		
1383	135.00	60.00	594.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p60 s2		
1384	135.00	60.00	594.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p60 s3		
1385	270.00	120.00	1188.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p120s0		
1386	270.00	120.00	1188.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p120s1		
1387	270.00	120.00	1188.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p120s2		
1388	270.00	120.00	1188.000	3840x2160	Prog	Р	Р	HDTV1080	RGB	4K2K 3840x2160p120s3		
1389	67.43	29.97	296.703	3840x2160	Prog	Р	Р	ANALOG	YPbPr	4K2K 3840x2160p30		
1390	56.25	25.00	297.000	3840x2160	Prog	Р	Р	ANALOG	YPbPr	4K2K 3840x2160p25		
1391	53.95	23.98	296.703	3840x2160	Prog	Р	Р	ANALOG	YPbPr	4K2K 3840x2160p24		
1392	53.95	23.98	296.703	4096x2160	Prog	Р	Р	ANALOG	YPbPr	4K2K 4096x2160p24		
1393												
1394												
1395												
1396												
1397												
1398									_			
1399												
1400												

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1401	31.47	59.94	27.000	720 × 483	Prog	N	N	ANALOG	YPbPr	NTSC PROG.	256-block color	256-Color Block
1402	31.47	59.94	27.000	720 × 483	Prog	N	N	ANALOG	YPbPr	NTSC PROG. W	64-gradation block gray (white → black)	64Gray Block White->
1403	31.47	59.94	27.000	720 × 483	Prog	N	N	ANALOG	YPbPr	NTSC PROG. LB	64-gradation block gray (black → white)	64Gray Block Black->
1404	33.72	59.94	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@59.94i	8 color bars & 16 gray scale	8-Color & 16-Gray
1405	33.75	60.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@60i	Gray scale & crosshatch	Gray & Cross Hatch
1406	67.43	59.94	148.352	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@59.94p	Color bar & crosshatch	Color & Cross Hatch
1407	67.50	60.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@60p	Color temperature	Color Temperature
1408	44.96	59.94	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@59.94p	Pairing	Pairing
1409	45.00	60.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@60p	Crosshatch & circle & gray	Cross&Circle&Gray
1410	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	NTSC-J 4:3	Crosshatch & circle & color bar & character	Cross&Circle&Color&H
1411											Circle & line	Circle & Line
1412											Character edge (H)	H-Character Line
1413											Character edge (O)	O-Character Line
1414											Crosstalk (width 90%)	Cross Talk W=90%
1415											Sine wave scroll	Sign Wave Scroll
1416	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	PAL PROG.	10 steps & 1/10 MHz	1/10 MHz × 10step
1417	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	PAL PROG. W	Gamma correction ramp wγ = 2.5	Gamma Ramp wr=2.5
1418	31.25	50.00	27.000	720 × 576	Prog	N	N	ANALOG	YPbPr	PAL PROG. LB	Gamma correction ramp γ = 2.0	Gamma Ramp r=2.0
1419	28.13	50.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@50i	Gamma correction ramp γ = 0.5	Gamma Ramp r=0.5
1420	56.25	50.00	148.500	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@50p	SMPTE RP-27.1	SMPTE RP-27.1

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1421	37.50	50.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@50p	ITC pattern 9 windows	ITC 9-Window
1422	15.63	50.00	13.500	702 × 574	Int	N	Ν	PAL	YPbPr	PAL 4:3	ITC pattern crosshatch & marker	ITC Cross & Marker
1423											ITC pattern H character	ITC H-Character
1424											64 gray + RGBW color bars superimposed	64-Gray & RGBW-Color
1425											Gray scale + circle	Gray & Circle
1426	33.72	29.97	74.176	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@29.97p	Corner & center point marker	Corner&Center Marker
1427	33.75	30.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@30p	Crosstalk (width 60%)	Cross Talk W=60%
1428	26.97	23.98	74.176	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@23.98p	Song of Youth	SpeakerCheck / Youth
1429	27.00	24.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@24p	Crosshatch & marker	Cross & Marker 1
1430	28.13	25.00	74.250	1920 × 1080	Prog	Р	Р	HDTV1080	YPbPr	1920 × 1080@25p	256-color block color "Color" letters	256-Color <color></color>
1431	33.72	59.94	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@29.97sf	Random 256-color color bar	256-Color Random
1432	33.75	60.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@30sf	256-step gray scale & 7 color bars	256-Gray & 7-Color
1433	26.97	47.96	74.176	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@23.98sf	Center, corner window & edge marker	Corner&Center Window
1434	27.00	48.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@24sf	3-step gray scale window	3gray-Window
1435	28.13	50.00	74.250	1920 × 1080	Int	Р	Р	HDTV1080	YPbPr	1920 × 1080@25sf	19 × 15 crosshatch & marker	Cross & Marker 2
1436	22.48	29.97	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@29.97p	Crosshatch & circle	Circle & Cross Hatch
1437	22.50	30.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@30p	Checkerboard & window	1dotChecker & Window
1438	17.98	23.98	74.176	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@23.98p	ANSI pattern (Setup)	ANSI Setup
1439	18.00	24.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@24p	ANSI pattern (Contrast)	ANSI Contrast
1440	18.75	25.00	74.250	1280 × 720	Prog	Р	Р	HDTV720	YPbPr	1280 × 720@25p	ANSI pattern (9Point)	ANSI 9-Point

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1441											ANSI pattern (Hor Reso)	ANSI H-Resolution
1442											ANSI pattern (Ver Reso)	ANSI V-Resolution
1443											Gamma correction ramp γ = 2.2	Gamma Ramp r=2.2
1444											Gamma correction ramp γ = 0.45	Gamma Ramp r=0.45
1445											Limited ramp in horizontal direction	
1446											Limited ramp in vertical direction	
1447												
1448												
1449												
1450												
1451	33.72	59.94	74.176	1920 × 1035	Int	Р	Р	HDTV1080	YPbPr	1920 × 1035@59.94i		
1452	33.75	60.00	74.250	1920 × 1035	Int	Р	Р	HDTV1080	YPbPr	1920 × 1035@60i		
1453	31.25	50.00	74.250	1920 × 1080	Int	Ν	Ζ	HDTV1250	YPbPr	SMPTE295Mi		
1454	62.50	50.00	148.500	1920 × 1080	Prog	Ν	Ν	HDTV1250	YPbPr	SMPTE295Mp		
1455	31.25	50.00	48.000	1280 × 1152	Int	Р	Р	HDTV1152 (AUS)	YPbPr	AUS 1152i		
1456	31.25	50.00	72.000	1920 × 1080	Int	Р	Ν	HDTV1250 (AUS)	YPbPr	AUS 1080i		
1457												
1458												
1459												
1460												

<sup>\*</sup> Program numbers 1461 to 1480 are not registered.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Sync polarity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1481										Motion blur 1	Motion Blur1
1482										Motion blur 2	Motion Blur2
1483										Motion blur 3	Motion Blur3
1484										Motion blur 4	Motion Blur4
1485										Motion blur 5	Motion Blur5
1486										Motion blur 6	Motion Blur6
1487										Motion blur 7	Motion Blur7
1488										Motion blur 8	Motion Blur8
1489										Motion blur 9	Motion Blur9
1490										Motion blur 10	Motion Blur10
1491										Motion blur 11	Motion Blur11
1492										Motion blur 12	Motion Blur12
1493											
1494											
1495											
1496											
1497											
1498			_								
1499									_		
1500		·	·								

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	S) pol	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1501	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	NTSC-J 4:3	Timing data	Timing Data
1502	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	NTSC-J 16:9		
1503	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	NTSC-J LB		
1504	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	PAL 4:3		
1505	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	PAL 16:9		
1506	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	PAL LB		
1507	15.63	50.00	13.500	702 × 574	Int	N	N	SECAM	YPbPr	SECAM 4:3		
1508	15.63	50.00	13.500	702 × 574	Int	N	N	SECAM	YPbPr	SECAM 16:9		
1509	15.63	50.00	13.500	702 × 574	Int	N	N	SECAM	YPbPr	SECAM LB		
1510	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC-M	YPbPr	NTSC-M		
1511	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC-443	YPbPr	NTSC-443		HDCP On Screen
1512	15.73	59.94	13.500	712 × 484	Int	N	N	PAL-M	YPbPr	PAL-M		
1513	15.73	59.94	13.500	712 × 484	Int	Ν	N	PAL-60	YPbPr	PAL-60		
1514	15.63	50.00	13.500	718 × 572	Int	N	N	PAL-N	YPbPr	PAL-N		
1515	15.63	50.00	13.500	702 × 574	Int	N	N	PAL-Nc	YPbPr	PAL-Nc		
1516												
1517												
1518												
1519												
1520												

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pol	ync arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1521	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	Closed Caption CC1		HDMI Packet Data
1522	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	Closed Caption CC2		
1523	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	Closed Caption Text1		
1524	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	Closed Caption Text2		
1525	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC-M	YPbPr	V Chip MPAA G		
1526	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	V Chip MPAA X		
1527	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	V Chip US TV-Y		
1528	15.73	59.94	13.500	712 × 484	Int	Ν	N	NTSC-M	YPbPr	V Chip US TV-MA-VSL		
1529												
1530												
1531	15.63	50.00	13.500	702 × 574	Int	Ν	N	PAL	YPbPr	PAL TELETEXT	EDID pattern DVI-1	EDID DVI1
1532											EDID pattern DVI-1 (HEX)	EDID DVI1 (HEX)
1533											EDID pattern DVI-2	EDID DVI2
1534											EDID pattern DVI-2 (HEX)	EDID DVI2 (HEX)
1535											EDID pattern HDMI1	EDID HDMI1
1536								-		·	EDID pattern HDMI1 (HEX)	EDID HDMI1 (HEX)
1537											EDID pattern HDMI2	EDID HDMI2
1538											EDID pattern HDMI2 (HEX)	EDID HDMI2 (HEX)
1539	-	-									EDID pattern PC-DVI	EDID PC-DVI
1540											EDID pattern PC-DVI (HEX)	EDID PC-DVI (HEX)

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pol	nc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1541	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	Mac NTSC-J DVD Type1	EDID pattern PC-VGA	EDID PC-VGA
1542	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	Mac NTSC-J DVD Type2	EDID pattern PC-VGA (HEX)	EDID PC-VGA (HEX)
1543	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	Mac NTSC-J DVD Type3	EDID pattern TV-VGA	EDID TV-VGA
1544	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	Mac PAL DVD	EDID pattern TV-VGA (HEX)	EDID TV-VGA (HEX)
1545												
1546												
1547												
1548												
1549												
1550												
1551	15.63	50.00	13.500	702 × 574	Int	N	Ν	PAL	YPbPr	SCART PAL VBS 4:3		
1552	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	SCART PAL Y/C 4:3		
1553	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	SCART PAL RGB 4:3		
1554	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	SCART PAL VBS 16:9		
1555	15.63	50.00	13.500	702 × 574	Int	N	N	PAL	YPbPr	SCART PAL TELETEXT		
1556												
1557												
1558												
1559												
1560												

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sync polarity H V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1561										DDC/Clpattern DVI-1 L-0	DDC/CI DVI1 L-0
1562										DDC/CI pattern DVI-1 L-100	DDC/CI DVI1 L-100
1563										DDC/CI pattern DVI-1 L-200	DDC/CI DVI1 L-200
1564										DDC/CI pattern DVI-2 L-0	DDC/CI DVI2 L-0
1565										DDC/CI pattern DVI-2 L-100	DDC/CI DVI2 L-100
1566										DDC/CI pattern DVI-2 L-200	DDC/CI DVI2 L-200
1567										DDC/CI pattern HDMI1 L-0	DDC/CI HDMI1 L-0
1568										DDC/CI pattern HDMI1 L-100	DDC/CI HDMI1 L-100
1569										DDC/CI pattern HDMI1 L-200	DDC/CI HDMI1 L-200
1570										DDC/CI pattern HDMI2 L-0	DDC/CI HDMI2 L-0
1571										DDC/CI pattern HDMI2 L-100	DDC/CI HDMI2 L-100
1572										DDC/CI pattern HDMI2 L-200	DDC/CI HDMI2 L-200
1573										DDC/CI pattern PC-DVI L-0	DDC/CI pcDVI L-0
1574										DDC/CI pattern PC-DVI L-100	DDC/CI pcDVI L-100
1575										DDC/CI pattern PC-DVI L-200	DDC/CI pcDVI L-200
1576										DDC/CI pattern PC-VGA L-0	DDC/CI pcVGA L-0
1577										DDC/CI pattern PC-VGA L-100	DDC/CI pcVGA L-100
1578	_		_					_		DDC/CI pattern PC-VGA L-200	DDC/CI pcVGA L-200
1579			_							DDC/CI pattern TV-VGA L-0	DDC/CI tvVGA L-0
1580										DDC/CI pattern TV-VGA L-100	DDC/CI tvVGA L-100
1581			·							DDC/CI pattern TV-VGA L-200	DDC/CI tvVGA L-200

<sup>\*</sup> Program numbers 1582 to 1600 are not registered.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	nc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1601	37.86	85.08	31.500	640 × 350	Prog	Р	Ν	ANALOG	RGB	VESA640 × 350@85		
1602	37.86	85.08	31.500	640 × 400	Prog	N	Р	ANALOG	RGB	VESA640 × 400@85		
1603	37.93	85.04	35.500	720 × 400	Prog	N	Р	ANALOG	RGB	VESA720 × 400@85		
1604	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VESA640 × 480@60		
1605	37.86	72.81	31.500	640 × 480	Prog	Ν	Ν	ANALOG	RGB	VESA640 × 480@72		
1606	37.50	75.00	31.500	640 × 480	Prog	N	Ν	ANALOG	RGB	VESA640 × 480@75		
1607	43.27	85.01	36.000	640 × 480	Prog	N	N	ANALOG	RGB	VESA640 × 480@85		
1608	35.16	56.25	36.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA800 × 600@56		
1609	37.88	60.32	40.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA800 × 600@60		
1610	48.08	72.19	50.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA800 × 600@72		
1611	46.88	75.00	49.500	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA800 × 600@75		
1612	53.67	85.06	56.250	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA800 × 600@85		
1613	76.30	119.97	73.250	800 × 600	Prog	Р	Ν	ANALOG	RGB	VESA800 × 600@120CVT		
1614	31.02	60.00	33.750	848 × 480	Prog	Р	Р	ANALOG	RGB	VESA848 × 480@60		
1615	35.52	86.96	44.900	1024 × 768	Int	Р	Р	ANALOG	RGB	VESA1024 × 768@43		
1616	48.36	60.00	65.000	1024 × 768	Prog	N	Ν	ANALOG	RGB	VESA1024 × 768@60		
1617	56.48	70.07	75.000	1024 × 768	Prog	N	N	ANALOG	RGB	VESA1024 × 768@70	·	
1618	60.02	75.03	78.750	1024 × 768	Prog	Р	Р	ANALOG	RGB	VESA1024 × 768@75	<u> </u>	
1619	68.68	85.00	94.500	1024 × 768	Prog	Р	Р	ANALOG	RGB	VESA1024 × 768@85		
1620	97.55	119.99	115.500	1024 × 768	Prog	Р	Ν	ANALOG	RGB	VESA1024 × 768@120CVT		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1621	67.50	75.00	108.000	1152 × 864	Prog	Р	Р	ANALOG	RGB	VESA1152 × 864@75	CEC pattern HDMI1 Standby	CEC HDMI1 Standby
1622	47.40	59.99	68.250	1280 × 768	Prog	Р	Ν	ANALOG	RGB	VESA1280 × 768@60	CEC pattern HDMI1 Im View On	CEC HDMI1 Im View On
1623	47.78	59.87	79.500	1280 × 768	Prog	N	Р	ANALOG	RGB	VESA1280 × 768@60	CEC pattern HDMI1 Set OSD Nm	CEC HDMI1 Set OSD Nm
1624	60.29	74.89	102.250	1280 × 768	Prog	Ν	Р	ANALOG	RGB	VESA1280 × 768@75	CEC pattern HDMI2 Standby	CEC HDMI2 Standby
1625	68.63	84.84	117.500	1280 × 768	Prog	Ν	Р	ANALOG	RGB	VESA1280 × 768@85	CEC pattern HDMI2 Im View On	CEC HDMI2 Im View On
1626	97.40	119.80	140.250	1280 × 768	Prog	Р	Ν	ANALOG	RGB	VESA1280 × 768@120CVT	CEC pattern HDMI2 Set OSD Nm	CEC HDMI2 Set OSD Nm
1627	49.31	59.91	71.000	1280 × 800	Prog	Р	N	ANALOG	RGB	VESA1280 × 800@60CVT		
1628	49.70	59.81	83.500	1280 × 800	Prog	Ν	Р	ANALOG	RGB	VESA1280 × 800@60		
1629	62.79	74.93	106.500	1280 × 800	Prog	Ν	Р	ANALOG	RGB	VESA1280 × 800@75		
1630	71.55	84.88	122.500	1280 × 800	Prog	Ν	Р	ANALOG	RGB	VESA1280 × 800@85		
1631	101.56	119.91	146.250	1280 × 800	Prog	Р	Ν	ANALOG	RGB	VESA1280 × 800@120CVT		
1632	60.00	60.00	108.000	1280 × 960	Prog	Р	Р	ANALOG	RGB	VESA1280 × 960@60		
1633	85.94	85.00	148.500	1280 × 960	Prog	Р	Р	ANALOG	RGB	VESA1280 × 960@85		
1634	121.88	119.84	175.500	1280 × 960	Prog	Р	N	ANALOG	RGB	VESA1280 × 960@120CVT		
1635	63.98	60.02	108.000	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1280 × 1024@60		
1636	79.98	75.02	135.000	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1280 × 1024@75		
1637	91.15	85.02	157.500	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1280 × 1024@85		
1638	130.03	119.96	187.250	1280 × 1024	Prog	Р	N	ANALOG	RGB	VESA1280 × 1024@120CVT		
1639	47.71	60.02	85.500	1360 × 768	Prog	Р	Р	ANALOG	RGB	VESA1360 × 768@60		
1640	97.53	119.97	148.250	1360 × 768	Prog	Р	N	ANALOG	RGB	VESA1360 × 768@120CVT		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H		SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1641	64.74	59.95	101.000	1400 × 1050	Prog	Р	Ν	ANALOG	RGB	VESA1400 × 1050@60	3D window 4% L:W/R:W	3D Wsize 4% L:W R:W
1642	65.32	59.98	121.750	1400 × 1050	Prog	N	Р	ANALOG	RGB	VESA1400 × 1050@60	3D window 4% L:W/R:B	3D Wsize 4% L:W R:B
1643	82.28	74.87	156.000	1400 × 1050	Prog	N	Р	ANALOG	RGB	VESA1400 × 1050@75	3D window 4% L:B/R:B	3D Wsize 4% L:B R:B
1644	93.88	84.96	179.500	1400 × 1050	Prog	N	Р	ANALOG	RGB	VESA1400 × 1050@85	3D window 4% L:B/R:W	3D Wsize 4% L:B R:W
1645	133.33	119.90	208.000	1400 × 1050	Prog	Р	N	ANALOG	RGB	VESA1400 × 1050@120CVT	3D window 100% L:W/R:W	3D Wsize100% L:W R:W
1646	55.47	59.90	88.750	1440 × 900	Prog	Р	N	ANALOG	RGB	VESA1440 × 900@60CVT	3D window 100% L:W/R:B	3D Wsize100% L:W R:B
1647	55.93	59.89	106.500	1440 × 900	Prog	N	Р	ANALOG	RGB	VESA1440 × 900@60	3D window 100% L:B/R:B	3D Wsize100% L:B R:B
1648	70.64	74.98	136.750	1440 × 900	Prog	N	Р	ANALOG	RGB	VESA1440 × 900@75	3D window 100% L:B/R:W	3D Wsize100% L:B R:W
1649	80.43	84.84	157.000	1440 × 900	Prog	N	Р	ANALOG	RGB	VESA1440 × 900@85	3D raster L100%/R100%	3D Level L100% R100%
1650	114.22	119.85	182.750	1440 × 900	Prog	Р	N	ANALOG	RGB	VESA1440 × 900@120CVT	3D raster L100%/R 75%	3D Level L100% R 75%
1651	75.00	60.00	162.000	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1600 × 1200@60	3D raster L100%/R 50%	3D Level L100% R 50%
1652	81.25	65.00	175.500	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1600 × 1200@65	3D raster L100%/R 25%	3D Level L100% R 25%
1653	87.50	70.00	189.000	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1600 × 1200@70	3D raster L100%/R 0%	3D Level L100% R 0%
1654	93.75	75.00	202.500	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1600 × 1200@75	3D raster L 0%/R100%	3D Level L 0% R100%
1655	106.25	85.00	229.500	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1600 × 1200@85	3D raster L 0%/R 75%	3D Level L 0% R 75%
1656	152.41	119.92	268.250	1600 × 1200	Prog	Р	N	ANALOG	RGB	VESA1600 × 1200@120CVT	3D raster L 0%/R 50%	3D Level L 0% R 50%
1657	64.67	59.88	119.000	1680 × 1050	Prog	Р	N	ANALOG	RGB	VESA1680 × 1050@60CVT	3D raster L 0%/R 25%	3D Level L 0% R 25%
1658	65.29	59.95	146.250	1680 × 1050	Prog	N	Р	ANALOG	RGB	VESA1680 × 1050@60	3D raster L 0%/R 0%	3D Level L 0% R 0%
1659	82.31	74.89	187.000	1680 × 1050	Prog	N	Р	ANALOG	RGB	VESA1680 × 1050@75	3D pattern cross hatch	3D Cross Hatch
1660	93.86	84.94	214.750	1680 × 1050	Prog	N	Р	ANALOG	RGB	VESA1680 × 1050@85	3D pattern marker	3D Markers

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1661	133.42	119.99	245.500	1680 × 1050	Prog	Р	Ν	ANALOG	RGB	VESA1680 × 1050@120CVT	3D pattern 9 circle	3D 9 Circles
1662	83.64	60.00	204.750	1792 × 1344	Prog	N	Р	ANALOG	RGB	VESA1792 × 1344@60	3D pattern color bar V-1	3D Color Bar V-1
1663	106.27	75.00	261.000	1792 × 1344	Prog	N	Р	ANALOG	RGB	VESA1792 × 1344@75	3D pattern color bar V-2	3D Color Bar V-2
1664	170.72	119.97	333.250	1792 × 1344	Prog	Р	N	ANALOG	RGB	VESA1792 × 1344@120CVT	3D pattern vertical	3D Vertical Bar
1665	86.33	60.00	218.250	1856 × 1392	Prog	N	Р	ANALOG	RGB	VESA1856 × 1392@60	3D pattern checker dot	3D Checker Dot
1666	112.50	75.00	288.000	1856 × 1392	Prog	N	Р	ANALOG	RGB	VESA1856 × 1392@75	3D pattern checker block	3D Checker Block
1667											3D Slant Color Bar	3D Slant Color Bar
1668	74.04	59.95	154.000	1920 × 1200	Prog	Р	N	ANALOG	RGB	VESA1920 × 1200@60	3D Slant Gray Scale	3D Slant Gray Scale
1669	74.56	59.88	193.250	1920 × 1200	Prog	N	Р	ANALOG	RGB	VESA1920 × 1200@60	3D Monoscope	3D Monoscope
1670	94.04	74.93	245.250	1920 × 1200	Prog	N	Р	ANALOG	RGB	VESA1920 × 1200@75	3D Monoscope Offset	3D Monoscope Offset
1671	107.18	84.93	281.250	1920 × 1200	Prog	N	Р	ANALOG	RGB	VESA1920 × 1200@85		
1672	152.40	119.91	317.000	1920 × 1200	Prog	Р	Ν	ANALOG	RGB	VESA1920 × 1200@120CVT		
1673	90.00	60.00	234.000	1920 × 1440	Prog	N	Р	ANALOG	RGB	VESA1920 × 1440@60		
1674	112.50	75.00	297.000	1920 × 1440	Prog	N	Р	ANALOG	RGB	VESA1920 × 1440@75		
1675												
1676	98.71	59.97	268.500	2560 × 1600	Prog	Р	Ν	ANALOG	RGB	VESA2560 × 1600@60CVT		
1677	47.71	59.79	85.500	1366 × 768	Prog	Р	Р	ANALOG	RGB	VESA1366 × 768@60		
1678	45.00	60.00	74.250	1280x720	Prog	Р	Р	ANALOG	RGB	VESA1280x720@60		
1679	48.00	60.00	72.000	1366x768	Prog	Р	Р	ANALOG	RGB	VESA1366x768@60		
1680	60.00	60.00	108.000	1600x900	Prog	Р	Р	ANALOG	RGB	VESA1600x900@60		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Sync polarity H V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1681	67.50	60.00	148.50	1920x1080	Prog	P P	ANALOG	RGB	VESA1920x1080@60		
1682	72.00	60.00	162.00	2048x1152	Prog	P P	ANALOG	RGB	VESA2048x1152@60		
1683											
1684											
1685											
1686											
1687											
1688											
1689											
1690											
1691											
1692											
1693											
1694											
1695											
1696											
1697											
1698	·										
1699											
1700											

<sup>\*</sup> Program numbers 1701 to 1800 are not registered.

\* The 3D timing data has been moved or added to program no. 1201 to 1275 when HDMI Specifications Ver.1.4a came to be supported. As a result, the data in program no. 1801 to 1848 was all deleted. The data stored in program no. 1801 to 1848 can be read and set using the SP-8870 software program provided as a standard accessory.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Syr polai		SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1801	134.87	59.94	296.704	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 FramePack		
1802	112.50	50.00	297.000	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 FramePack		
1803	53.95	23.98	148.352	1920x2205	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 FramePack		
1804	67.43	29.97	148.352	1920x2228	Prog	Р	Р	ANALOG	YPbPr	3D 1080i60 FramePack		
1805	56.25	25.00	148.500	1920x2228	Prog	Р	Р	ANALOG	YPbPr	3D 1080i50 FramePack		
1806	89.91	59.94	148.352	1280x1470	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 FramePack		
1807	75.00	50.00	148.500	1280x1470	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 FramePack		
1808	62.94	59.94	54.000	720x1005	Prog	Ν	Ν	ANALOG	YPbPr	3D 480p60 FramePack		
1809	62.50	50.00	54.000	720x1201	Prog	Ν	Ν	ANALOG	YPbPr	3D 576p50 FramePack		
1810	31.47	29.97	54.000	1440x1028	Prog	Ν	Ν	ANALOG	YPbPr	3D 480i60 FramePack		
1811	31.25	25.00	54.000	1440x1226	Prog	Ν	Ν	ANALOG	YPbPr	3D 576i50 FramePack		
1812	62.94	59.94	50.350	640x1005	Prog	Ν	Ν	ANALOG	RGB	3D VGAp60 FramePack		
1813	67.43	59.94	148.352	1920x2206	Int	Р	Р	ANALOG	YPbPr	3D 1080i60 FieldAlte		
1814	56.25	50.00	148.500	1920x2206	Int	Р	Р	ANALOG	YPbPr	3D 1080i50 FieldAlte		
1815	31.47	59.94	54.000	1440x1006	Int	Ν	Ν	ANALOG	YPbPr	3D 480i60 FieldAlte		
1816	31.25	50.00	54.000	1440x1202	Int	Ν	Ν	ANALOG	YPbPr	3D 576i50 FieldAlte		
1817	134.87	59.94	296.704	1920x2160	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 LineAlter		
1818	112.50	50.00	297.000	1920x2160	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 LineAlter		
1819	53.95	23.98	148.352	1920x2160	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 LineAlter		
1820	89.91	59.94	148.352	1280x1440	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 LineAlter		

\* The 3D timing data has been moved or added to program no. 1201 to 1275 when HDMI Specifications Ver.1.4a came to be supported. As a result, the data in program no. 1801 to 1848 was all deleted. The data stored in program no. 1801 to 1848 can be read and set using the SP-8870 software program provided as a standard accessory.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Syn polar	rity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1821	75.00	50.00	148.500	1280x1440	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 LineAlter	CEC pattern HDMI1 Standby	CEC HDMI1 Standby
1822	62.94	59.94	54.000	720x960	Prog	Ν	Ν	ANALOG	YPbPr	3D 480p60 LineAlter	CEC pattern HDMI1 Im View On	CEC HDMI1 Im View On
1823	62.50	50.00	54.000	720x1152	Prog	Ν	Ν	ANALOG	YPbPr	3D 576p50 LineAlter	CEC pattern HDMI1 Set OSD Nm	CEC HDMI1 Set OSD Nm
1824	62.94	59.94	50.350	640x960	Prog	Ν	Ν	ANALOG	RGB	3D VGAp60 LineAlter	CEC pattern HDMI2 Standby	CEC HDMI2 Standby
1825	67.43	59.94	296.704	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 Side_full	CEC pattern HDMI2 Im View On	CEC HDMI2 Im View On
1826	56.25	50.00	297.000	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 Side_full	CEC pattern HDMI2 Set OSD Nm	CEC HDMI2 Set OSD Nm
1827	26.97	23.98	148.352	3840x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 Side_full		
1828	33.72	59.94	148.352	3840x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i60 Side_full		
1829	28.13	50.00	148.500	3840x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i50 Side_full		
1830	44.96	59.94	148.352	2560x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 Side_full		
1831	37.50	50.00	148.500	2560x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 Side_full		
1832	31.47	59.94	54.000	1440x480	Prog	Ν	Ν	ANALOG	YPbPr	3D 480p60 Side_full		
1833	31.25	50.00	54.000	1440x576	Prog	Ν	Ν	ANALOG	YPbPr	3D 576p50 Side_full		
1834	15.73	59.94	54.000	2880x480	Int	Ν	Ν	ANALOG	YPbPr	3D 480i60 Side_full		
1835	15.63	50.00	54.000	2880x576	Int	Ν	Ν	ANALOG	YPbPr	3D 576i50 Side_full		
1836	31.47	59.94	50.350	1280x480	Prog	Ν	Ν	ANALOG	RGB	3D VGAp60 Side_full		
1837	67.43	59.94	148.352	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p60 Side_half		
1838	56.25	50.00	148.500	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p50 Side_half		
1839	26.97	23.98	74.176	1920x1080	Prog	Р	Р	ANALOG	YPbPr	3D 1080p24 Side_half		
1840	33.72	59.94	74.176	1920x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i60 Side_half		

\* The 3D timing data has been moved or added to program no. 1201 to 1275 when HDMI Specifications Ver.1.4a came to be supported. As a result, the data in program no. 1801 to 1848 was all deleted. The data stored in program no. 1801 to 1848 can be read and set using the SP-8870 software program provided as a standard accessory.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (HxV)	Int / Prog	Sy pol	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1841	28.13	50.00	74.250	1920x1080	Int	Р	Р	ANALOG	YPbPr	3D 1080i50 Side_half		
1842	44.96	59.94	74.176	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p60 Side_half		
1843	37.50	50.00	74.250	1280x720	Prog	Р	Р	ANALOG	YPbPr	3D 720p50 Side_half		
1844	31.47	59.94	27.000	720x480	Prog	Ν	Ν	ANALOG	YPbPr	3D 480p60 Side_half		
1845	31.25	50.00	27.000	720x576	Prog	Ν	Ν	ANALOG	YPbPr	3D 576p50 Side_half		
1846	15.73	59.94	27.000	1440x480	Int	Ν	Ν	ANALOG	YPbPr	3D 480i60 Side_half		
1847	15.63	50.00	27.000	1440x576	Int	Ν	Ν	ANALOG	YPbPr	3D 576i50 Side_half		
1848	31.47	59.94	25.175	640x480	Prog	Ν	Ν	ANALOG	RGB	3D VGAp60 Side_half		
1849												
		-										

Note: The 3D timing for sampling # 1801 to 1848 contain programs which are not written in HDMI Specification Ver.1.4. Sample timing may be changed in future by updates of the specification or standards.

In the current Vblank3 period for field alternative, DE is HIGH. This spec is subject to change in future.

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1850	37.86	85.08	31.500	640 × 400	Prog	N	Р	ANALOG	RGB	VESA400-85		
1851	37.86	72.81	31.500	640 × 480	Prog	Ν	Ζ	ANALOG	RGB	VESA480-72		
1852	37.50	75.00	31.500	640 × 480	Prog	Ν	Ν	ANALOG	RGB	VESA480-75		
1853	35.16	56.25	36.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA600-56		
1854	37.88	60.32	40.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA600-60		
1855	48.08	72.19	50.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA600-72		
1856	48.36	60.00	65.000	1024 × 768	Prog	Ν	Ζ	ANALOG	RGB	VESA768-60		
1857	56.48	70.07	75.000	1024 × 768	Prog	N	N	ANALOG	RGB	VESA768-70		
1858	60.02	75.03	78.750	1024 × 768	Prog	Р	Р	ANALOG	RGB	VESA768-75		
1859	79.98	75.02	135.000	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1024-75		
1860	91.15	85.02	157.500	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1024-85		
1861	75.00	60.00	162.000	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1200-60		
1862	81.25	65.00	175.500	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1200-65		
1863	87.50	70.00	189.000	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1200-70		
1864	93.75	75.00	202.500	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1200-75		
1865	100.00	80.00	216.000	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1200-80		
1866	106.25	85.00	229.500	1600 × 1200	Prog	Р	Р	ANALOG	RGB	VESA1200-85		
1867	98.21	70.05	236.500	1800 × 1350	Prog	Ν	Р	ANALOG	RGB	VESA1350-70		
1868	18.44	49.83	16.260	720 × 350	Prog	N	Ν	ANALOG	RGB	MDA		
1869	15.75	60.10	14.360	640 × 200	Prog	N	Ν	ANALOG	RGB	CGA		
1870	21.85	59.71	16.260	640 × 350	Prog	N	Ν	ANALOG	RGB	EGA		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1871	30.48	60.00	24.870	640 × 400	Prog	N	Ν	ANALOG	RGB	PGA		
1872	31.47	50.03	28.320	720 × 350	Prog	N	N	ANALOG	RGB	VGA-TEXT350-50		
1873	31.47	59.94	28.320	720 × 350	Prog	N	N	ANALOG	RGB	VGA-TEXT350-60		
1874	31.47	70.08	28.320	720 × 350	Prog	N	N	ANALOG	RGB	VGA-TEXT350-70		
1875	31.47	50.03	28.320	720 × 400	Prog	N	Ν	ANALOG	RGB	VGA-TEXT400-50		
1876	31.47	59.94	28.320	720 × 400	Prog	N	N	ANALOG	RGB	VGA-TEXT400-60		
1877	31.47	70.08	28.320	720 × 400	Prog	N	N	ANALOG	RGB	VGA-TEXT400-70		
1878	31.47	50.03	25.175	640 × 350	Prog	Ν	N	ANALOG	RGB	VGA350-50		
1879	31.47	59.94	25.175	640 × 350	Prog	N	N	ANALOG	RGB	VGA350-60		
1880	31.47	70.09	25.175	640 × 350	Prog	N	N	ANALOG	RGB	VGA350-70		
1881	31.47	50.03	25.175	640 × 400	Prog	Ν	Ν	ANALOG	RGB	VGA400-50		
1882	31.47	59.94	25.175	640 × 400	Prog	N	N	ANALOG	RGB	VGA400-60		
1883	31.47	70.09	25.175	640 × 400	Prog	N	Ν	ANALOG	RGB	VGA400-70		
1884	31.47	50.03	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-50		
1885	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1886	35.16	56.16	36.000	800 × 600	Prog	N	Ν	ANALOG	RGB	S-VGA-56		
1887	48.08	72.19	50.000	800 × 600	Prog	N	N	ANALOG	RGB	S-VGA-72		
1888	46.88	75.00	49.500	800 × 600	Prog	N	N	ANALOG	RGB	S-VGA-75		
1889	48.08	59.80	65.000	1024 × 768	Prog	N	Ν	ANALOG	RGB	XGA-60		
1890	53.95	66.11	71.640	1024 × 768	Prog	N	N	ANALOG	RGB	XGA-66		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	nc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1891	56.48	70.07	75.000	1024 × 768	Prog	N	Ν	ANALOG	RGB	XGA-70		
1892	60.68	57.03	100.000	1280 × 1024	Prog	N	Ν	ANALOG	RGB	SXGA-57		
1893	63.5	59.68	106.930	1280 × 1024	Prog	N	Ν	ANALOG	RGB	SXGA-60A		
1894	63.75	59.75	110.160	1280 × 1024	Prog	N	Ν	ANALOG	RGB	SXGA-60B		
1895	63.72	60.00	109.470	1280 × 1024	Prog	N	Ν	ANALOG	RGB	SXGA-60C		
1896	78.91	74.16	132.880	1280 × 1024	Prog	N	Ν	ANALOG	RGB	SXGA-70		
1897	74.63	59.94	160.000	1600 × 1200	Prog	N	Ν	ANALOG	RGB	UXGA1200-60		
1898	107.42	85.05	220.000	1600 × 1200	Prog	N	Ν	ANALOG	RGB	UXGA1200-85A		
1899	106.48	85.05	230.000	1600 × 1200	Prog	N	Ν	ANALOG	RGB	UXGA1200-85B		
1900	107.42	80.05	220.000	1600 × 1280	Prog	N	Ν	ANALOG	RGB	UXGA1280-80A		
1901	106.48	80.06	230.000	1600 × 1280	Prog	Ν	Ν	ANALOG	RGB	UXGA1280-80B		
1902	106.4	80.00	238.340	1600 × 1280	Prog	N	Ν	ANALOG	RGB	UXGA1280-80C		
1903	109.82	80.40	246.000	1600 × 1280	Prog	Ν	Ν	ANALOG	RGB	UXGA1280-82		
1904	35.52	86.96	44.900	1024 × 768	Int	N	Ν	ANALOG	RGB	IBM 8514A		
1905	63.36	60.00	89.210	1024 × 1024	Prog	N	Ν	ANALOG	RGB	IBM 5080		
1906	29.58	73.14	24.020	640 × 754	Int	N	Ν	ANALOG	RGB	IBM 5550		
1907	63.36	60.00	111.520	1280 × 1024	Prog	N	Ν	ANALOG	RGB	IBM 6000		
1908	15.71	59.98	6.380	323 × 246	Prog	N	Ν	ANALOG	RGB	NAVIGATION		
1909	35	66.67	30.240	640 × 480	Prog	N	Ν	ANALOG	RGB	Mac 480-66A		
1910	34.97	66.60	31.330	640 × 480	Prog	N	Ν	ANALOG	RGB	Mac 480-66B		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1911	48.83	66.89	50.000	800 × 600	Prog	N	N	ANALOG	RGB	Mac 600-66		
1912	49.72	74.55	57.280	832 × 624	Prog	N	N	ANALOG	RGB	Mac 624-57		
1913	48.78	59.56	64.000	1024 × 768	Prog	N	N	ANALOG	RGB	Mac 768-60		
1914	60.24	74.93	80.000	1024 × 768	Prog	N	N	ANALOG	RGB	Mac 768-75		
1915	68.68	75.06	100.000	1152 × 870	Prog	N	N	ANALOG	RGB	Mac 870-75		
1916	24.82	56.42	21.050	640 × 400	Prog	N	N	ANALOG	RGB	NEC PC9801		
1917	32.86	79.84	47.840	1120 × 750	Int	N	N	ANALOG	RGB	NEC PC9801XL		
1918	50.02	60.05	78.430	1120 × 750	Prog	Ν	N	ANALOG	RGB	NEC 768-60A		
1919	56.48	70.07	75.000	1024 × 768	Prog	N	N	ANALOG	RGB	NEC 768-70		
1920	64.6	59.93	107.500	1280 × 1024	Prog	N	Ν	ANALOG	RGB	NEC 1024-60		
1921	74.88	69.85	127.000	1280 × 1024	Prog	N	N	ANALOG	RGB	NEC 1024-70		
1922	78.86	74.11	135.000	1280 × 1024	Prog	N	N	ANALOG	RGB	NEC 1024-75		
1923	48.36	60.08	65.000	1024 × 768	Prog	N	Ν	ANALOG	RGB	NEC 768-60B		
1924	61.8	65.95	92.940	1152 × 900	Prog	N	N	ANALOG	RGB	SUN 900-66		
1925	71.73	76.07	105.590	1152 × 900	Prog	N	N	ANALOG	RGB	SUN 900-76		
1926	70.84	84.03	92.940	1024 × 800	Prog	N	N	ANALOG	RGB	SUN 800-84		
1927	81.13	76.11	135.000	1280 × 1024	Prog	N	N	ANALOG	RGB	SUN 1024-76		
1928	63.38	60.02	107.500	1280 × 1024	Prog	N	N	ANALOG	RGB	SONY NEWS		
1929	78.86	74.11	135.000	1280 × 1024	Prog	N	N	ANALOG	RGB	SONY 1024-74		
1930	78.86	74.11	135.000	1280 × 1024	Prog	N	N	ANALOG	RGB	SONY 1024-74		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog		rnc arity	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1931	48.48	59.64	64.000	1024 × 768	Prog	N	N	ANALOG	RGB	SGI Indigo768-60		
1932	77.01	72.38	130.000	1280 × 1024	Prog	N	N	ANALOG	RGB	SGI Indigo1024-72		
1933	63.9	60.00	107.350	1280 × 1024	Prog	N	N	ANALOG	RGB	SGI IRIS4D		
1934	63.33	59.97	108.170	1280 × 1024	Prog	N	N	ANALOG	RGB	HP 9000t1		
1935	78.13	72.00	135.000	1280 × 1024	Prog	N	N	ANALOG	RGB	HP 9000t2		
1936	54	60.00	69.120	1024 × 864	Prog	N	N	ANALOG	RGB	VAX 768-60		
1937	70.66	66.47	119.840	1280 × 1024	Prog	N	N	ANALOG	RGB	VAX 1024-66		
1938	60.05	75.06	78.780	1024 × 768	Prog	N	N	ANALOG	RGB	Fujitsu FMV 1024-75		
1939	80.66	100.83	108.410	1024 × 768	Prog	N	N	ANALOG	RGB	Fujitsu FMV 1024-100		
1940	79.7	74.83	134.370	1280 × 1024	Prog	N	N	ANALOG	RGB	Fujitsu FMV5166		
1941	80.38	75.12	135.040	1280 × 1024	Prog	N	N	ANALOG	RGB	Fujitsu FMV5133		
1942	63.74	60.02	108.100	1280 × 1024	Prog	N	N	ANALOG	RGB	Fujitsu SIGMA		
1943	78.16	71.64	135.060	1280 × 1024	Prog	N	N	ANALOG	RGB	HITACHI SXGA		
1944	26.35	59.90	22.770	640 × 400	Prog	N	N	ANALOG	RGB	Panasonic M550		
1945	46.88	75.00	49.500	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA600-75		
1946	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1947	31.47	59.95	28.640	746 × 471	Prog	N	N	ANALOG	RGB	ASTRO SC-2025		
1948	64	59.98	115.200	1400 × 1050	Prog	N	N	ANALOG	RGB	SXGA+		
1949	94.64	59.60	265.000	2048 × 1536	Prog	N	N	ANALOG	RGB	QXGA		
1950	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC	YPbPr	NTSC		

Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog		nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1951	33.75	60.00	74.250	1920 × 1080	Int	N	Ν	HDTV1080	YPbPr	1080i		
1952	31.47	59.94	25.175	640 × 480	Prog	Ν	Ν	ANALOG	RGB	VGA480-60		
1953	31.47	59.94	25.175	640 × 480	Prog	Ν	Ν	ANALOG	RGB	VGA480-60		
1954	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VGA480-60		
1955	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VGA480-60		
1956	31.22	49.98	46.200	1170 × 1168	Int	N	Ν	ANALOG	RGB	MEDICAL-1I		
1957	31.22	50.03	46.200	1170 × 584	Prog	N	Ν	ANALOG	RGB	MEDICAL-1N		
1958	30.69	60.00	36.830	947 × 946	Int	Ζ	Ν	ANALOG	RGB	MEDICAL-2I		
1959	30.69	60.06	36.830	947 × 473	Prog	N	Ν	ANALOG	RGB	MEDICAL-2N		
1960	37.93	85.04	35.500	720 × 400	Prog	N	Р	ANALOG	RGB	VESA400-88		
1961	112.5	90.00	243.000	1600 × 1200	Prog	N	Ν	ANALOG	RGB	1200-90		
1962	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VGA480-60		
1963	63.98	60.02	108.000	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1024-60		
1964	15.63	50.00	13.500	702 × 574	Int	Ν	Ν	SECAM	YPbPr	SECAM		
1965	31.47	59.94	34.240	864 × 480	Prog	N	Ν	ANALOG	RGB	W-VGA		
1966	37.88	60.32	53.940	1072 × 600	Prog	N	Ν	ANALOG	RGB	W-SVGA		
1967	48.36	60.00	87.440	1376 × 768	Prog	N	Ν	ANALOG	RGB	W-XGA		
1968	15.73	59.94	13.500	712 × 484	Int	Ν	Ν	NTSC	YPbPr	NTSC		
1969	15.63	50.00	13.500	702 × 574	Int	Ν	Ν	PAL	YPbPr	PAL		
1970	67.5	60.00	148.500	1920 × 1080	Prog	N	N	HDTV1080	YPbPr	1080P		

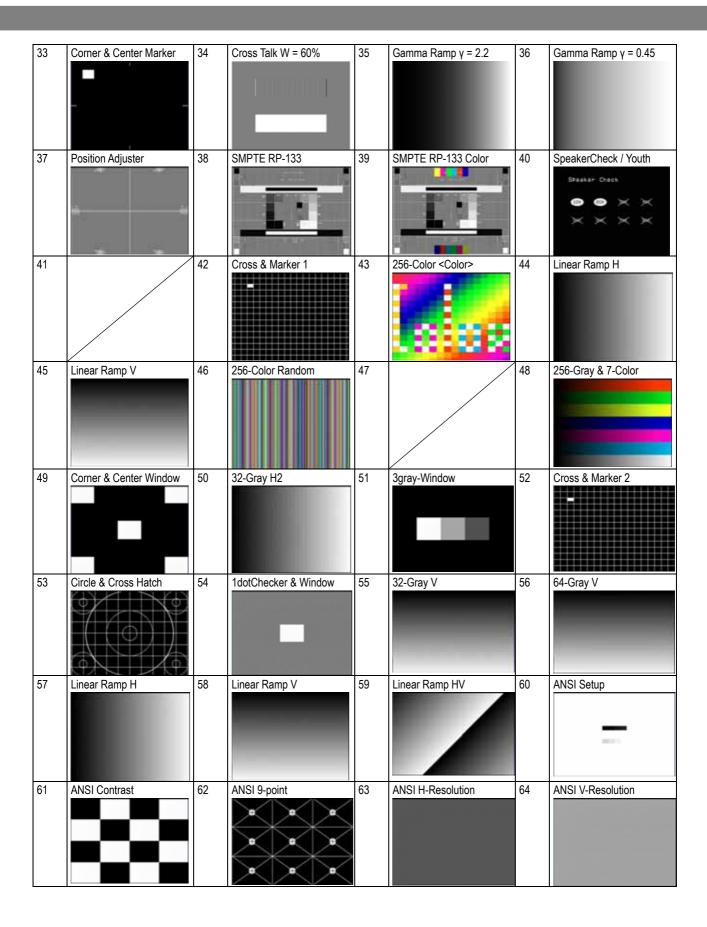
Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	Sy pola H	nc arity V	SyncType	Color difference	Timing data name	Pattern data	Pattern data name
1971	67.43	59.94	148.352	1920 × 1080	Prog	Ν	Ν	HDTV1080	YPbPr	1080P		
1972	33.75	60.00	74.250	1920 × 1080	Int	N	Ν	HDTV1080	YPbPr	1080i		
1973	33.72	59.94	74.176	1920 × 1080	Int	N	Ν	HDTV1080	YPbPr	1080i		
1974	33.75	60.00	74.250	1920 × 1035	Int	Ν	Ν	HDTV1080	YPbPr	1035i		
1975	33.72	59.94	74.176	1920 × 1035	Int	Ν	Ν	HDTV1080	YPbPr	1035i		
1976	45	60.00	74.250	1280 × 720	Prog	Ν	Ν	HDTV720	YPbPr	720P		
1977	44.96	59.94	74.176	1280 × 720	Prog	N	Ν	HDTV720	YPbPr	720P		
1978	31.47	59.94	27.000	720 × 483	Prog	N	Ν	ANALOG	YPbPr	483P		
1979	31.25	50.00	27.000	720 × 576	Prog	Ν	Ν	ANALOG	YPbPr	PAL*2		
1980	83.64	60.00	204.750	1792 × 1344	Prog	Ν	Р	ANALOG	RGB	VESA1344-60		
1981	83.64	60.00	204.750	1792 × 1344	Prog	Ν	Р	ANALOG	RGB	VESA1344-60		
1982	86.33	60.00	218.250	1856 × 1392	Prog	Ν	Р	ANALOG	RGB	VESA1392-60		
1983	86.33	60.00	218.250	1856 × 1392	Prog	Ν	Р	ANALOG	RGB	VESA1392-60		
1984	90	60.00	234.000	1920 × 1440	Prog	Ν	Р	ANALOG	RGB	VESA1440-60		
1985	90	60.00	234.000	1920 × 1440	Prog	Ν	Р	ANALOG	RGB	VESA1440-60		
1986	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1987	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VGA480-60		
1988	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VGA480-60		
1989	31.47	59.94	25.175	640 × 480	Prog	N	Ν	ANALOG	RGB	VGA480-60		
1990	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		

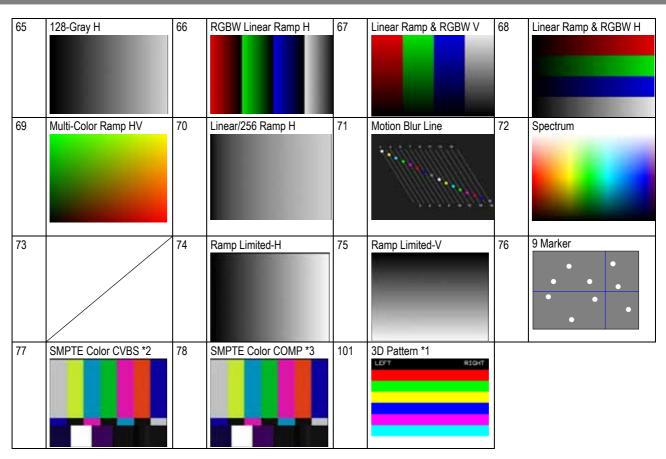
Program No.	Horizontal frequency [KHz]	Vertical frequency [Hz]	Dot clock frequency [MHz]	No. of display dots (H × V)	Int / Prog	pola	ync larity V	-7 - 71 -	Color difference	Timing data name	Pattern data	Pattern data name
1991	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1992	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1993	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1994	15.73	59.94	13.500	712 × 484	Int	N	N	NTSC-M	YPbPr	NTSC-M		
1995	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1996	31.47	59.94	25.175	640 × 480	Prog	N	N	ANALOG	RGB	VGA480-60		
1997	48.08	72.19	50.000	800 × 600	Prog	Р	Р	ANALOG	RGB	VESA600-72		
1998	56.48	70.07	75.000	1024 × 768	Prog	N	N	ANALOG	RGB	VESA768-70		
1999	79.98	75.02	135.000	1280 × 1024	Prog	Р	Р	ANALOG	RGB	VESA1024-75		

# 11.3.2 Optional pattern data

The internal optional pattern data (No.1 to No.70) of the VG-870B/871B is as shown below.

No.	Pattern Name	No.	Pattern Name	No.	Pattern Name	No.	Pattern Name
1	256-Color Block	2	64Gray Block White->	3	64Gray Block Black->	4	8-Color & 16-Gray
			-100				
5	Gray & Cross Hatch	6	Color & Cross Hatch	7	Color Temperature	8	Pairing
							$\begin{array}{c} & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ \end{array}$
9	Cross & Circle & Gray	10	Cross & Circle & Color & H	11	Circle & Line	12	H-Character Line
13	O-Character Line	14	Cross Talk W = 90%	15		16	NTSC Color
17	Sign Wave Scroll	18 I	Multi Burst 100%	19	1/10 MHz × 10step	20	Gamma Ramp wγ=2.5
21	Gamma Ramp γ=2.0	22	Gamma Ramp γ=0.5	23	SMPTE Color	24	SMPTE RP-27.1
25	ITC 9-Window	26	ITC Cross & Marker	27	ITC H-Character	28	32-Gray H
29	64-Gray H	30	64-Gray H & RGBW-Color	31	Gray & Circle	32	AFD
					<b>©</b>		





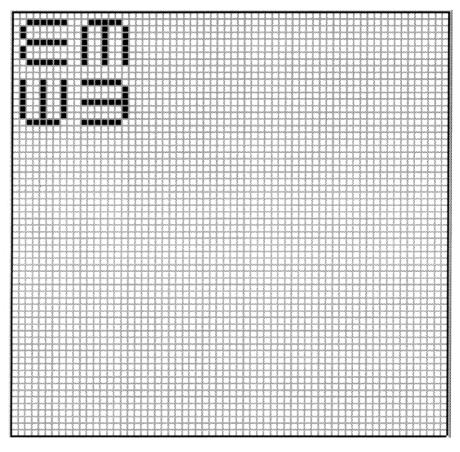
- \*1: This pattern requires the license. To purchase it, contact ASTRODESIGN sales.
- \*2: This is a minus level-compatible pattern used exclusively for <u>analog CVBS outputs</u>. It cannot be displayed at the correct level from other outputs.
- \*3: This is a minus level-compatible pattern used exclusively for <u>analog component outputs</u>. It cannot be displayed at the correct level from other outputs.

# 11.3.3 User character pattern data

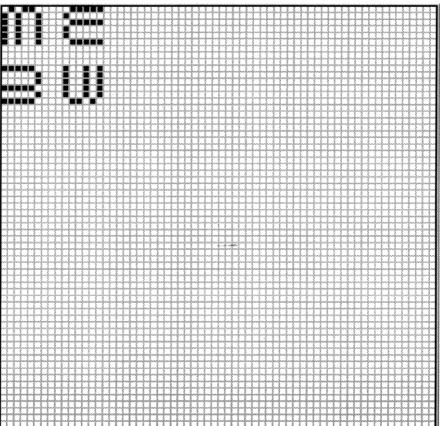
Code (H)	Description	Cell size	Reference page
F0	Letters "me" #1	18 × 18	p.473
F1	Letters "me" #2 (VESA specifications)	18 × 18	p.473
F2	Chinese character "AI"	64 × 64	p.474
F3	Chinese character "BI"	64 × 64	p.474
F4	Chinese character "TAKA"	32 × 32	p.475
F5	Chinese character "KIRI"	32 × 32	p.475
F6	Chinese character "KEN"	32 × 32	p.476
F7	Burst	64 × 64	p.476
F8			
F9			
FA			
FB			
FC			
FD			
FE			
FF			

### ■ F0H [letters "me" #1]/F1H [letters "me" #2 (VESA specifications)]

F0H

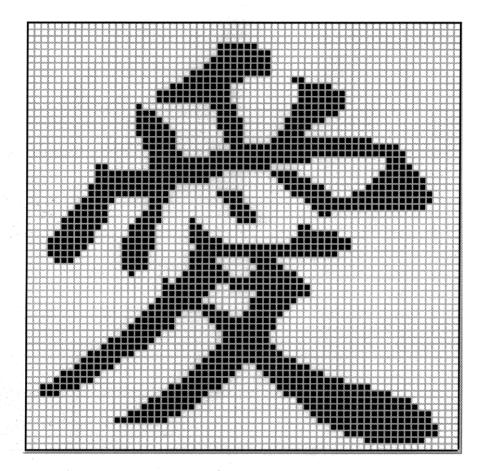


F1H

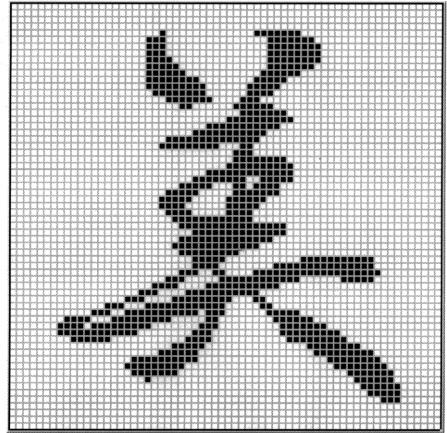


# ■ F2H [Chinese character "Al"]/F3H [Chinese character "Bl"]

F2H

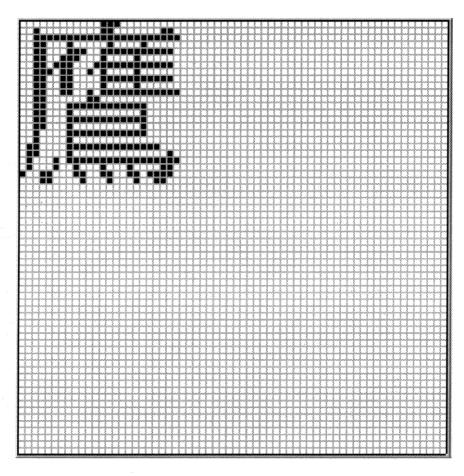


F3H

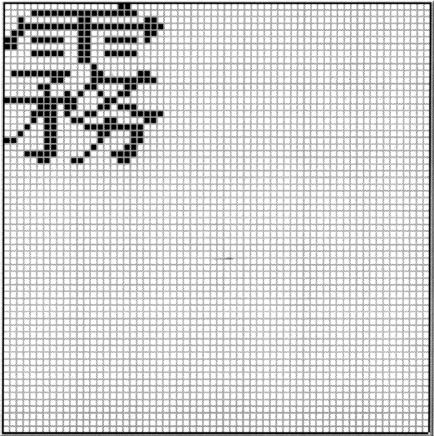


### ■ F4H [Chinese character "TAKA"]/F5H [Chinese character "KIRI"]

F4H

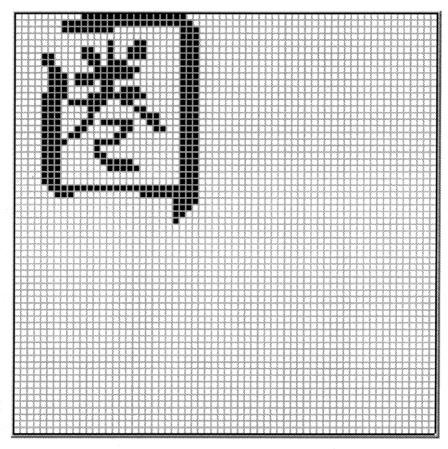


F5H

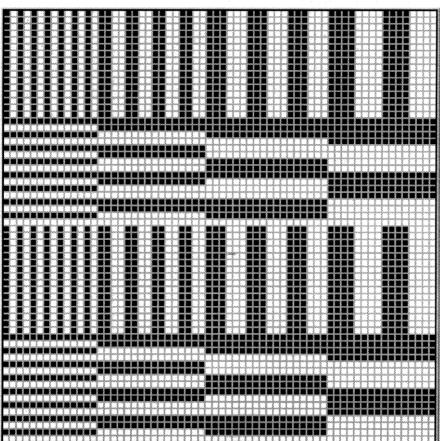


### ■ F6H [Chinese character "KEN"]/F7H [Burst]

F<sub>6</sub>H

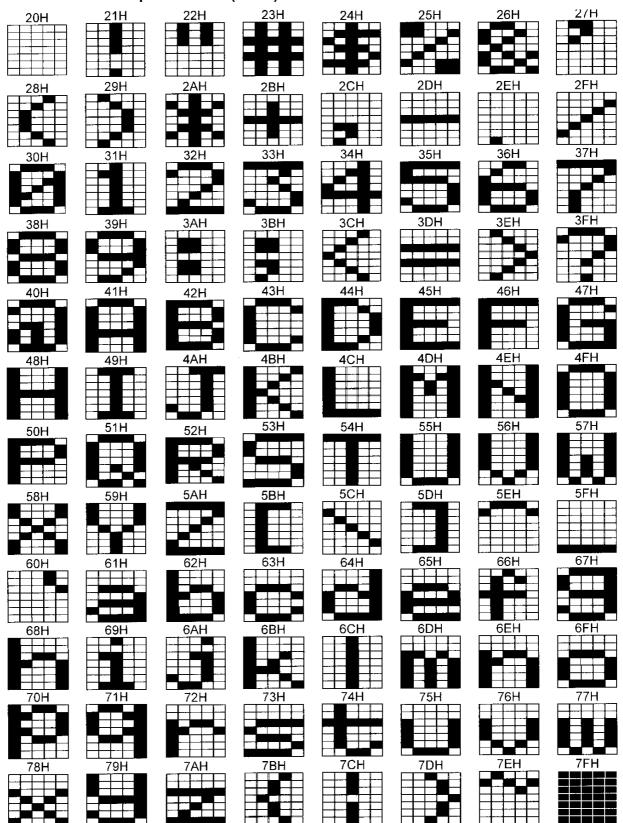


F7H

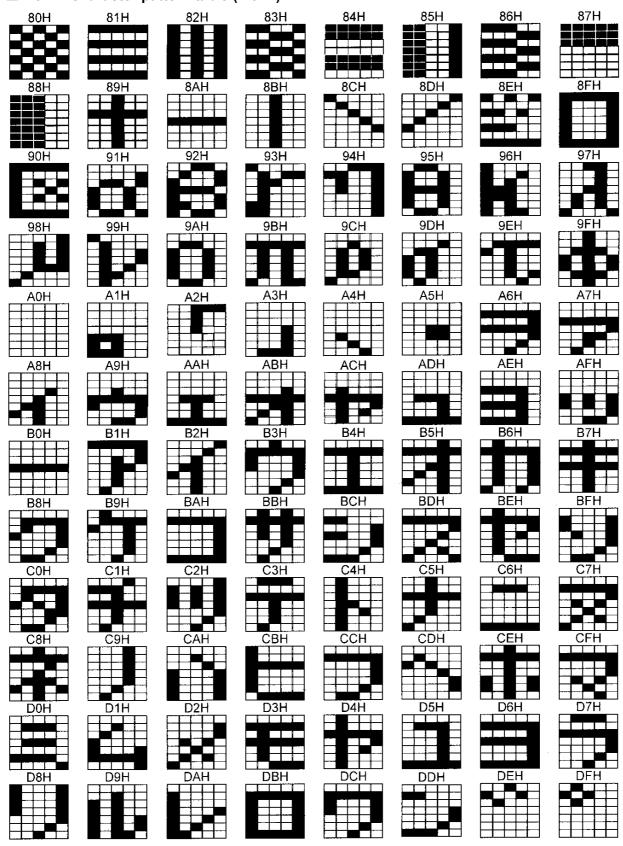


### 11.3.4 Character pattern data

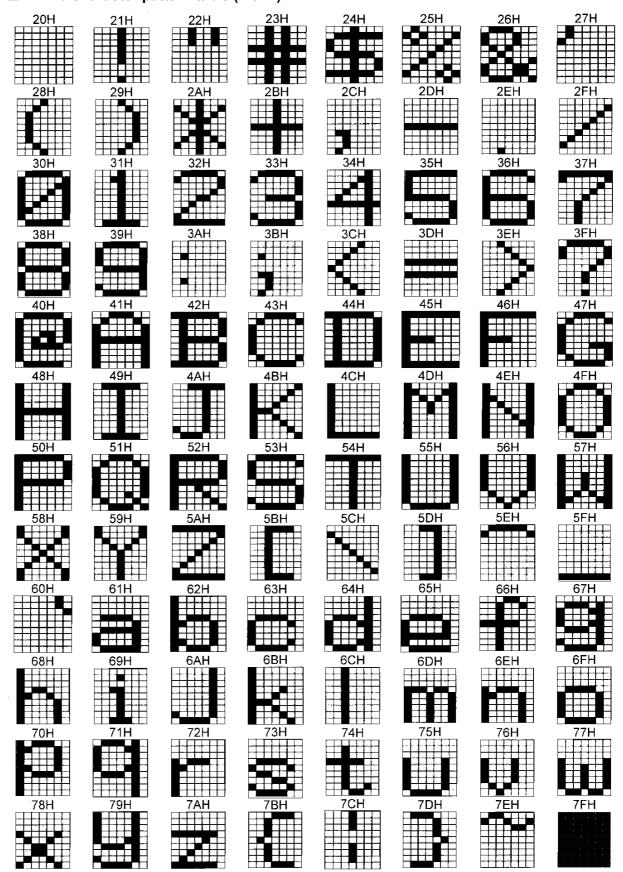
#### ■ 5 × 7 character pattern table (1 of 2)



#### ■ 5 × 7 character pattern table (2 of 2)



#### 7 × 9 character pattern table (1 of 2)

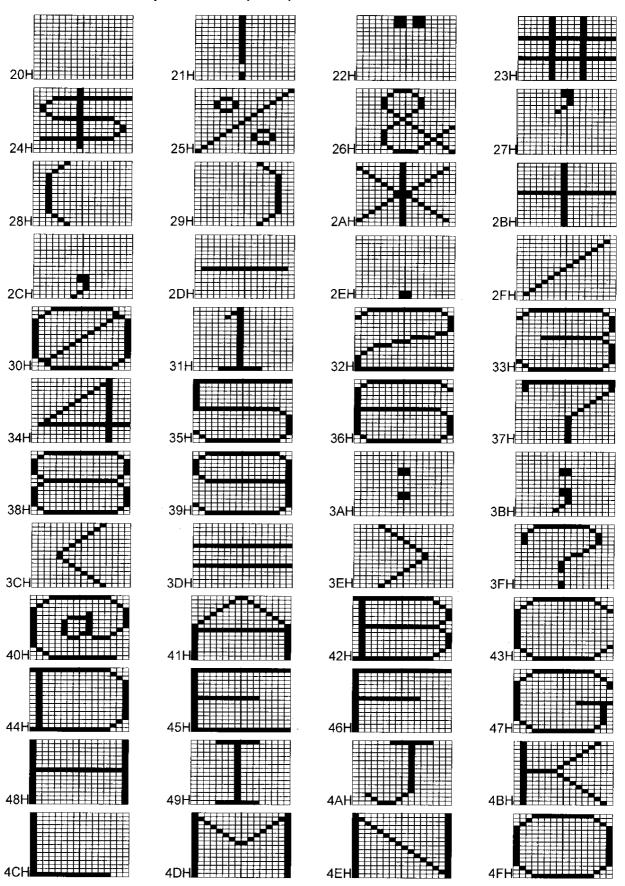


# ■ 7 × 9 character pattern table (2 of 2)

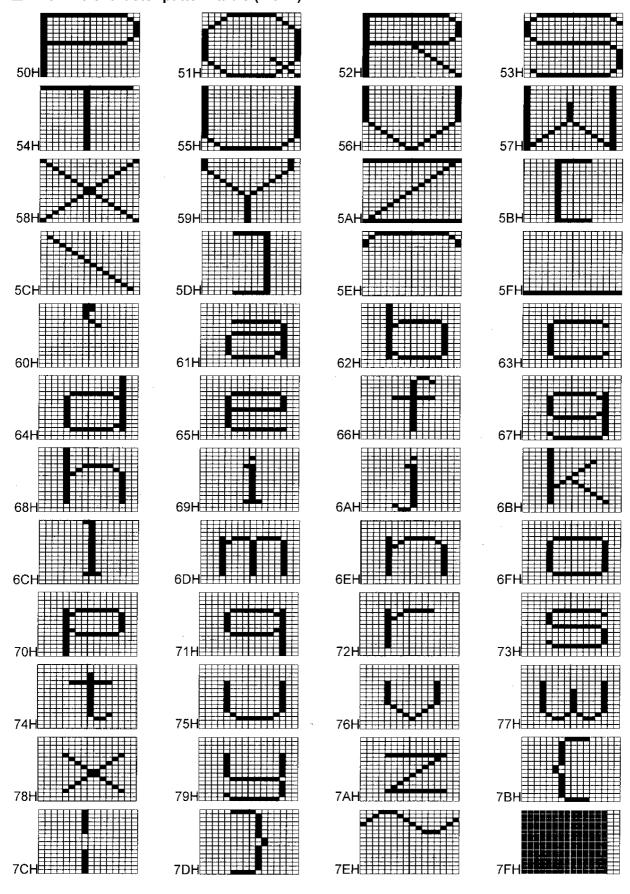
\*  $8 \times 9$  dots are used for 80H to 8FH.

80H	81H	82H	83H	84H	85H	86H	87H
<b>***</b>							
88H	89H	8AH	8BH	8CH	8DH	8EH	8FH
90H	91H	92H	93H	94H	95H	96H	97H
98H	99H	9AH	9BH	9CH	9DH	9EH	9FH
A0H	A1H	A2H	A3H	A4H	A5H	A6H	A7H
A8H	A9H	AAH	ABH	ACH	ADH	AEH	AFH
BOH	B1H	B2H	B3H	B4H	B5H	B6H	B7H
B8H	ВЭН	BAH	BBH	BCH	BDH	BEH	BFH
C0H	C1H	C2H	СЗН	C4H	C5H	C6H	C7H
C8H	C9H	CAH	СВН	CCH	CDH	CEH	CFH
		CAH			CDH		
D0H	D1H	D2H	D2H	D4H	D5H	D6H	D7H
DOH		DZH	D3H		Don	DON	
D8H	D9H	DAH	DBH	DCH	DDH	DEH	DFH

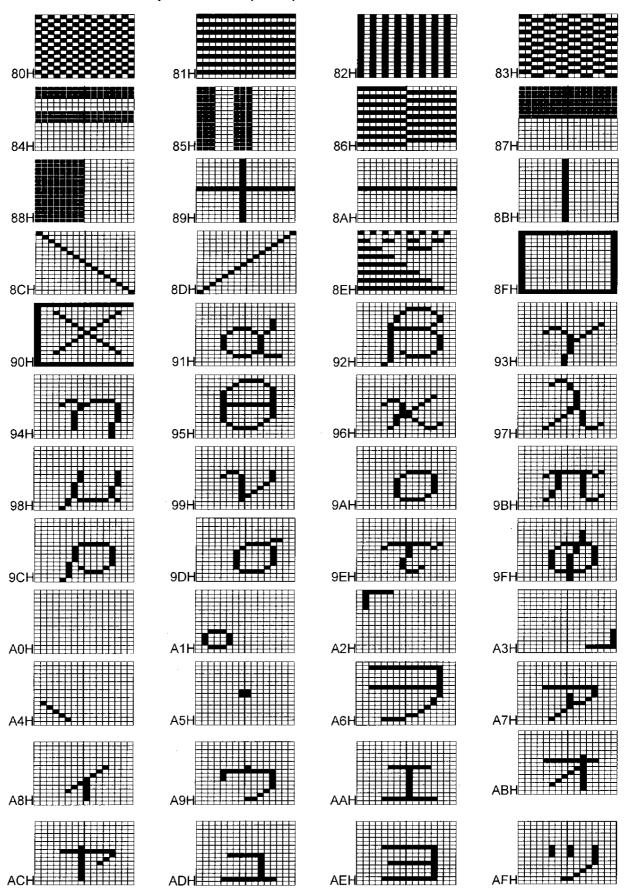
# ■ 16 × 16 character pattern table (1 of 4)



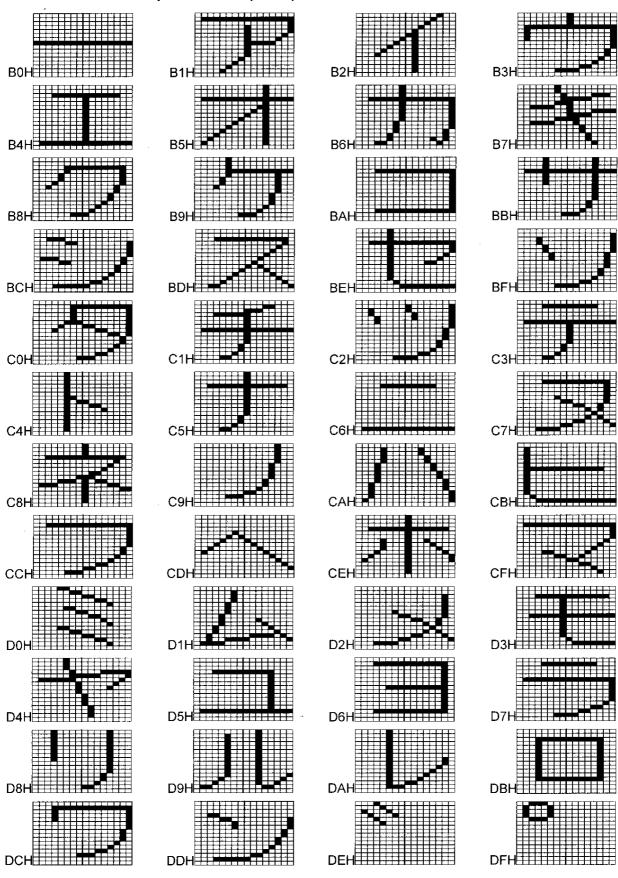
# ■ 16 × 16 character pattern table (2 of 4)



### ■ 16 × 16 character pattern table (3 of 4)



### ■ 16 × 16 character pattern table (4 of 4)



# 11.3.5 Tables of standard signals

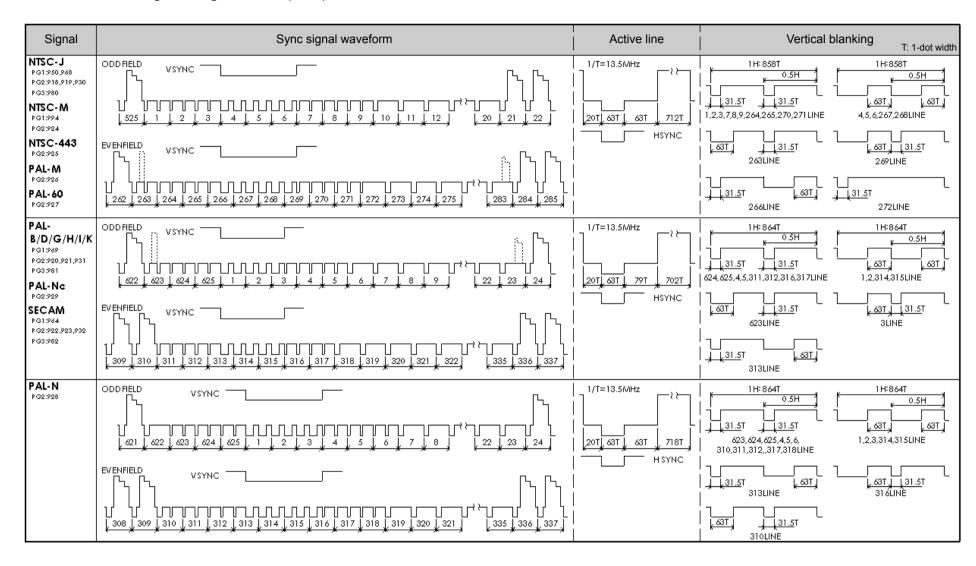
#### ■ Table of TV standard signals (1 of 2)

Signal format	Total no. of samples	Total no. of samples	Total no. of samples	Frame rate [Hz]	Scanning system	Subcarrier frequency [MHz]	Aspect ratio	Video level [mV]	Sync level [mV]	SETUP	Main countries where used
NTSC-J (Japan)	NTSC (RS-170A)	712 × 484	858 × 525	60/1.001	Interlaced	3.579545	4:3	714	286	No	Japan
NTSC-M	NTSC	712 × 484	858 × 525	60/1.001	Interlaced	3.579545	4:3	714	286	Yes	USA
NTSC-443	NTSC	712 × 484	858 × 525	60/1.001	Interlaced	4.43361875	4:3	714	286	Yes	
PAL-60	PAL	712 × 484	858 × 525	60/1.001	Interlaced	4.43361875	4:3	700	300	No	
PAL-M	PAL	712 × 484	858 × 525	60/1.001	Interlaced	3.57561189	4:3	714	286	Yes	Brazil
PAL (B/D/G/H/I/K)	PAL (BT.470-6)	702 × 574	864 × 625	50	Interlaced	4.43361875	4:3	700	300	No	U.K, Germany
PAL-N	PAL	718 × 574	864 × 625	50	Interlaced	4.43361875	4:3	714	286	Yes	Uruguay
PAL-Nc	PAL	702 × 574	864 × 625	50	Interlaced	3.58205625	4:3	700	300	No	Argentina
SECAM	SECAM	702 × 574	864 × 625	50	Interlaced	for = 4.406250 fob = 4.250000	4:3	700	300	No	France, Russia
483p (NTSC-PROG)	SMPTE293M	720 × 483	848 × 525	60/1.001	Progressive	-	4:3	700	300	-	-
576p (PAL-PROG)	BT.1358	720 × 574	864 × 625	50	Progressive	-	4:3	700	300	-	-

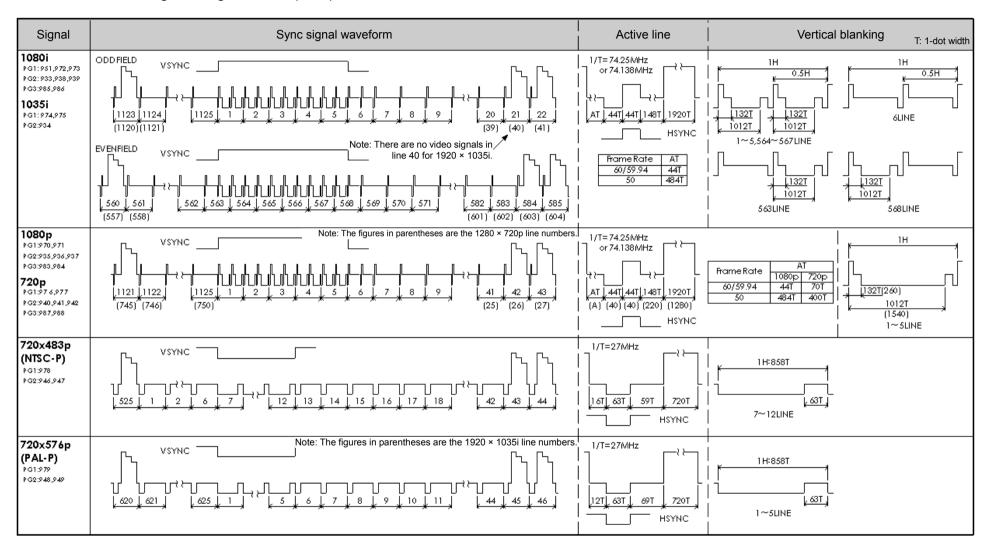
### ■ Table of TV standard signals (2 of 2)

Signal format	Total no. of samples	Total no. of samples	Total no. of samples	Frame rate [Hz]	Scanning system	Subcarrier frequency [MHz]	Aspect ratio	Video level [mV]	Sync level [mV]	SETUP	Main countries where used
720p	SMPTE296M	1280 × 720	1650 × 750	60	Progressive	-	16:9	700	300	-	-
			1650 × 750	60/1.001							
			1980 × 750	50							
			3300 × 750	30							
			3300 × 750	30/1.001							
			3960 × 750	25							
			4125 × 750	24							
			4125 × 750	24/1.001							
1035i	BTA S-001A	1920 × 1035	2200 × 1125	60	Interlaced	-	16:9	700	300	-	-
				60/1.001							
1080i	SMPTE274M	1920 × 1080	2200 × 1125	60	Interlaced	-	16:9	700	300		
			2200 × 1125	60/1.001							
			2640 × 1125	50							
1080p	SMPTE274M	1920 × 1080	2200 × 1125	60	Progressive	-	16:9	700	300		
			2200 × 1125	60/1.001							
			2640 × 1125	50							
			2200 × 1125	30							
			2200 × 1125	30/1.001							
			2640 × 1125	25							
			2750 × 1125	24							
			2750 × 1125	24/1.001							

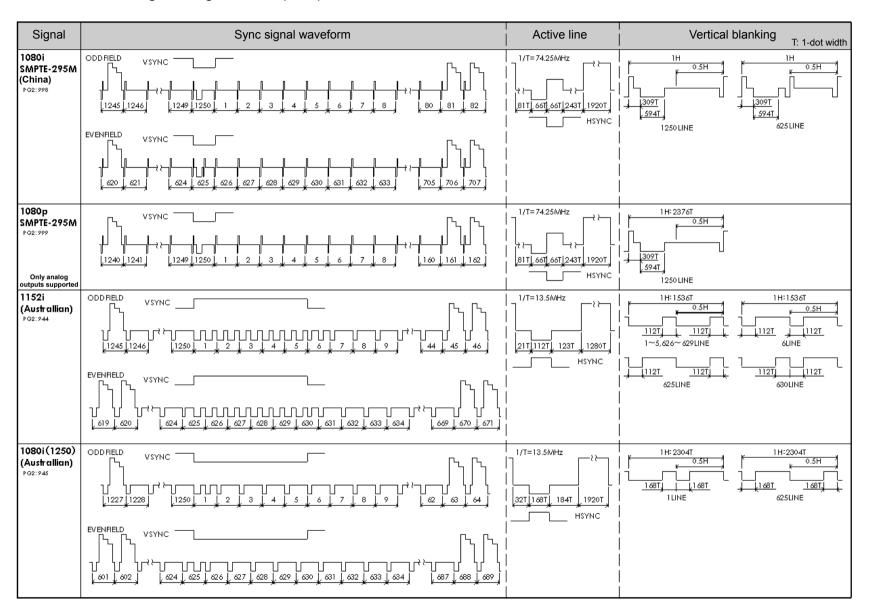
#### ■ Table of TV standard signal timing waveforms (1 of 3)



#### ■ Table of TV standard signal timing waveforms (2 of 3)



#### ■ Table of TV standard signal timing waveforms (3 of 3)



# **PRECAUTIONARY ITEMS**

## 12.1 Differences between the generator models

This instructions manual has been designed for the VG-870B/871B and, as such, some functions are not supported by the VG-870/871/870A/871A. The table below lists the main differences between the models.

#### Main differences between the generator models

Function	VG-870/871	VG-870A/871A	VG-870B/871B	VG-873/874
Moving images	Not supported	Supported	Supported	Supported
USB	Not supported	Supported	Supported	Supported
iTMDS	Not supported	Supported	Supported	Supported
V-by-One HS (VM-1825)	Not supported	Not supported	Supported	Supported
4K2K(iTMDS)(VM-1824)	Not supported	Not supported	Supported	Supported
4K2K (iTMDS QUAD) (VM-1824-A)	Not supported	Not supported	Supported	Supported
HDMI300MHz	Not supported	Not supported	Not supported	Supported

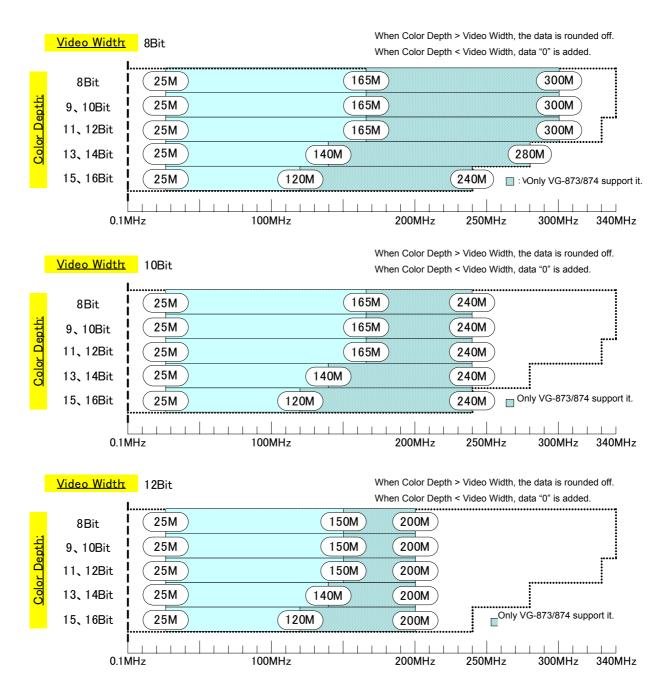
<sup>\*</sup> If you want to upgrade VG-870/871/870A/871A to VG-870B/871B, contact ASTRODESIGN sales representative.

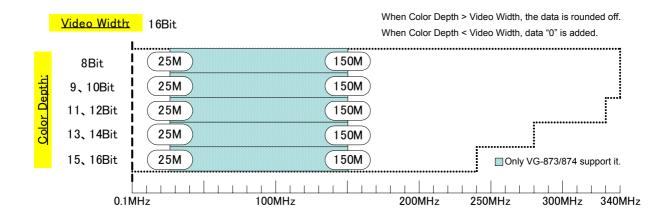
# 12.2 Relationships between pattern drawing bit length and dot clock frequency

The pattern drawing bit length stands in relationships of dependency on the dot clock frequency. Pattern drawing bit lengths and dot clock frequencies outside the bounds of these relationships cannot be set. These relationships also differ depending on the output video bit length of each unit. They are shown in the following figures.

#### 12.2.1 HDMI Unit

The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figure below. Data skipping occurs when the output video bit length (Video Width) at this time is less than the pattern drawing bit length (Color Depth).





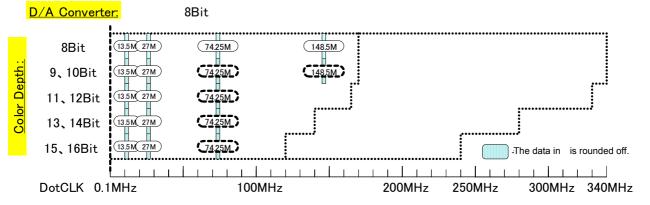
#### Dot clock limi of HDMI1 and HDMI2

For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

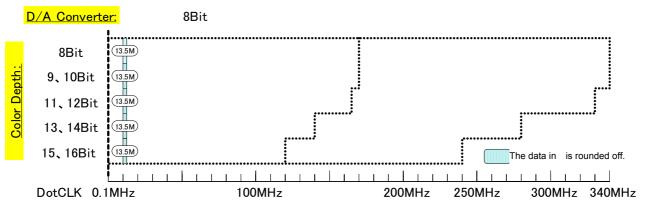
For details on the Output video bit length (Video Width), refer to "4.2.2 HDMI setting procedure."

#### 12.2.2 TV encoder unit

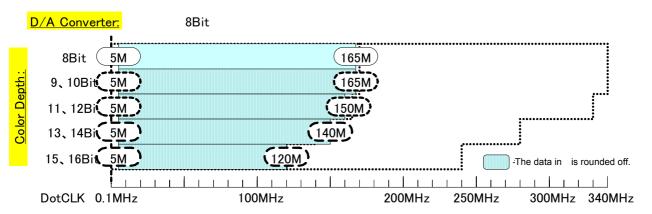
The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figures below. An 8-bit D/A converter is installed in the TV encoder unit, and data skipping occurs when the pattern drawing bit length (Color Depth) is more than 8 bits.



Restrictions on dot clock frequency for D5, YPbPr



Restrictions on dot clock frequency for COMPOSITE, Y/C (S connector), SCART

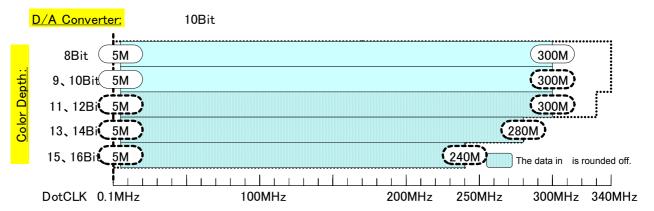


#### Restrictions on dot clock frequency for VGA

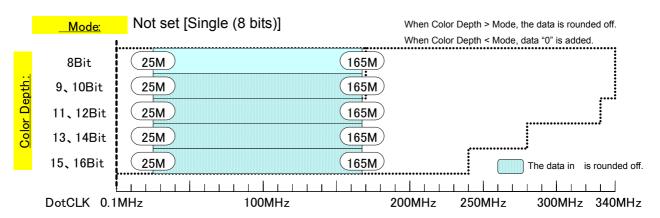
For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

#### 12.2.3 PC analog unit

The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figures below. 10-bit D/A converter is installed in the PC analog unit, and data skipping occurs when the pattern drawing bit length (Color Depth) is more than 10 bits. A DVI-I (Single Link) unit is also installed, and the data skipping occurs when the pattern drawing bit length (Color Depth) is more than 8 bits.



Restrictions on dot clock frequency for RGB, Dsub15, DVI (analog)

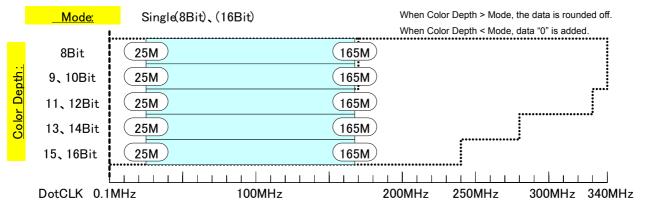


#### Restrictions on dot clock frequency for DVI (digital)

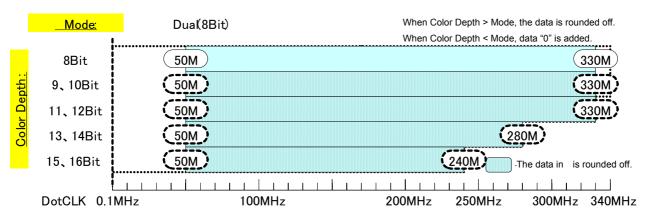
For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

#### 12.2.4 **DVI unit**

The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figures below. Data skipping occurs when the output video bit length (Video Width) at this time is less than the pattern drawing bit length (Color Depth).



#### Restrictions on dot clock frequency for DVI (Single Link)



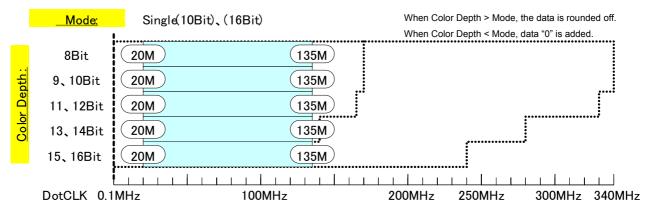
#### Restrictions on dot clock frequency for DVI (Dual Link)

For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

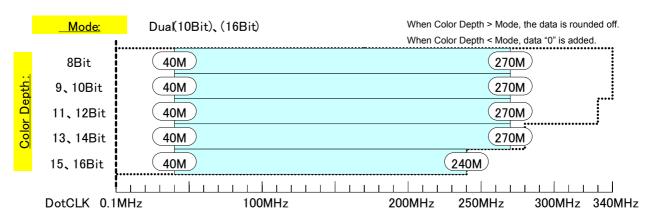
For details on the output video bit length (Mode), refer to "4.3.2 DVI unit setting procedure."

#### 12.2.5 LVDS unit

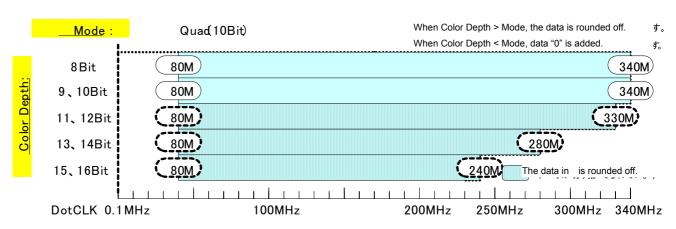
The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figures below. Data skipping occurs when the output video bit length (Video Width) at this time is less than the pattern drawing bit length (Color Depth).



Restrictions on dot clock frequency for LVDS (Single Link)



#### Restrictions on dot clock frequency for LVDS (Dual Link)



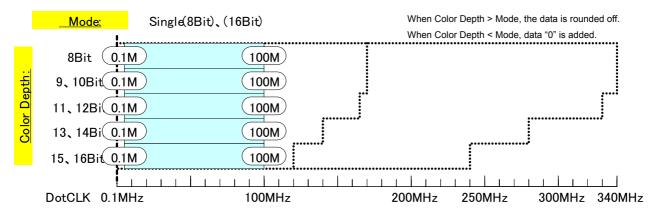
#### Restrictions on dot clock frequency for LVDS (Quad Link)

For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

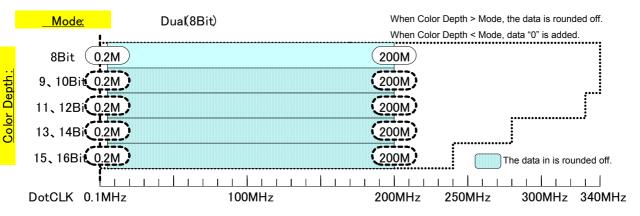
For details on the output video bit length (Mode), refer to "4.5.2 LVDS setting procedure."

#### 12.2.6 Parallel unit

The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figures below. Data skipping occurs when the output video bit length (Video Width) at this time is less than the pattern drawing bit length (Color Depth).



#### Restriction on parallel (Single Link) dot clock frequency



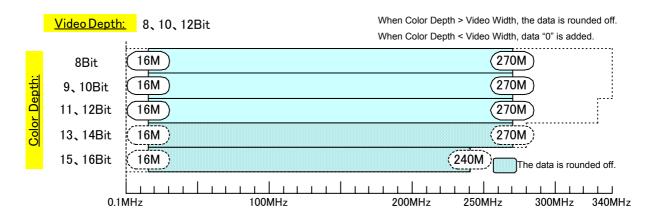
#### Restriction on parallel (Dual Link) dot clock frequency

For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

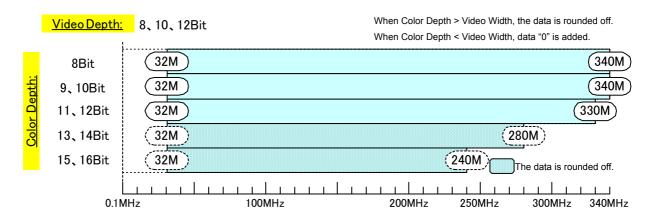
For details on the output video bit length (Mode), refer to "4.6.2 Parallel data setting procedure."

#### 12.2.7 DisplayPort unit

The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figure below. Data skipping occurs when the output video bit length (Video Width) at this time is less than the pattern drawing bit length (Color Depth).



#### Restrictions on dot clock frequency for DP1, DP2 (Single mode)



#### Restrictions on dot clock frequency for DP1, DP2 (Dual/Split mode)

The maximum dot clock also depends on the DisplayPort Link Rate and other settings. For details, refer to "11.1.8 DP unit".

For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing".

For details on the output video bit length (Video Width), refer to "4.12.2 DisplayPort setting procedure".

#### Setting unit of horizontal timing

In Single Mode, H-timing should be set by 2-dot. In Dual/Split Mode, H-timing should be set by 4-dot.

#### Front porch setting value in interlace timings

Due to the restriction of the DisplayPort Transmitter, Horizontal front porch should be set by 32-dot or higher, or vertical front porch should be set by 1H or higher.

#### Maximum vertical scanning lines

Due to the restriction of the DisplayPort Transmitter, 2046 is the maximum.

#### H-disp value

Due to the restriction of the DisplayPort Transmitter, H-disp / 2 (divided by 2) should be even number. If it becomes odd number, the signal is not stable.

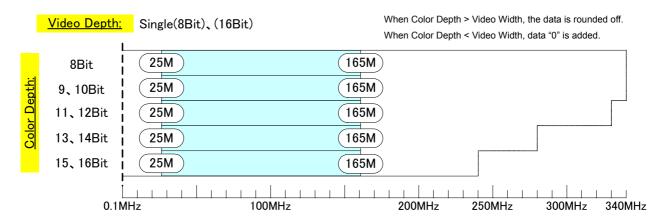
The below timing can not be output due to the restriction of DisplayPort transmitter and VG.

#### The timing that can not be output from DisplayPort unit

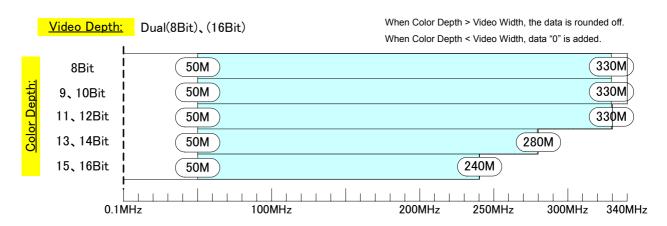
Program No.	Timing data name	Program No.	Timing data name
1324	HD-SDI 720@24p	1531	PAL TELETEXT
1325	HD-SDI 720@23.98p	1541	Mac NTSC-J DVD Type1
1401	NTSC PROG.	1542	Mac NTSC-J DVD Type2
1402	NTSC PROG. W	1543	Mac NTSC-J DVD Type3
1403	NTSC PROG. LB	1544	Mac PAL DVD
1410	NTSC-J 4:3	1551	SCART PAL VBS 4:3
1416	PAL PROG.	1552	SCART PAL Y/C 4:3
1417	PAL PROG. W	1553	SCART PAL RGB 4:3
1418	PAL PROG. LB	1554	SCART PAL VBS 16:9
1422	PAL 4:3	1555	SCART PAL TELETEXT
1438	1280x720@23.98p	1615	VESA1024x768@43
1439	1280x720@24p	1664	VESA1792x1344@120CVT
1451	1920x1035@59.94i	1666	VESA1856x1392@75
1452	1920x1035@60i	1671	VESA1920x1200@85
1453	SMPTE295Mi	1672	VESA1920x1200@120CVT
1454	SMPTE295Mp	1674	VESA1920x1440@75
1455	AUS 1152i	1677	VESA1366x768@60
1501	NTSC-J 4:3	1868	MDA
1502	NTSC-J 16:9	1869	CGA
1503	NTSC-J LB	1870	EGA
1504	PAL 4:3	1904	IBM 8514A
1505	PAL 16:9	1905	IBM 5080
1506	PAL LB	1906	IBM 5550
1507	SECAM 4:3	1908	NAVIGATION
1508	SECAM 16:9	1917	NEC PC9801XL
1509	SECAM LB	1944	Panasonic M550
1510	NTSC-M	1950	NTSC
1511	NTSC-443	1956	MEDICAL-1I
1512	PAL-M	1957	MEDICAL-1N
1513	PAL-60	1958	MEDICAL-2I
1514	PAL-N	1959	MEDICAL-2N
1515	PAL-Nc	1964	SECAM
1521	Closed Caption CC1	1968	NTSC
1522	Closed Caption CC2	1969	PAL
1523	Closed Caption Text1	1974	1035i
1524	Closed Caption Text2	1975	1035i
1525	V Chip MPAA G	1978	483P
1526	V Chip MPAA X	1979	PAL*2
1527	V Chip US TV-Y	1994	NTSC-M
1528	V Chip US TV-MA-VSL		

#### 12.2.8 4K2K (iTMDS) unit

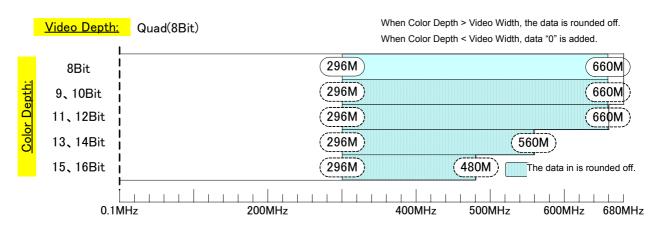
The dot clock frequency is restricted by the pattern drawing bit length (Color Depth) shown in the figure below. Data skipping occurs when the output video bit length (Video Width) at this time is less than the pattern drawing bit length (Color Depth).



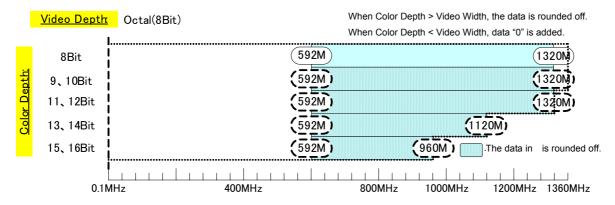
Restrictions on dot clock frequency for 4K2K(iTMDS) (DVI\_MODE/Single Link)



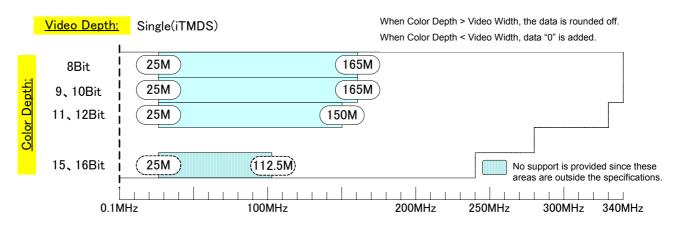
Restrictions on dot clock frequency for4K2K( iTMDS) (DVI MODE/Dual Link)



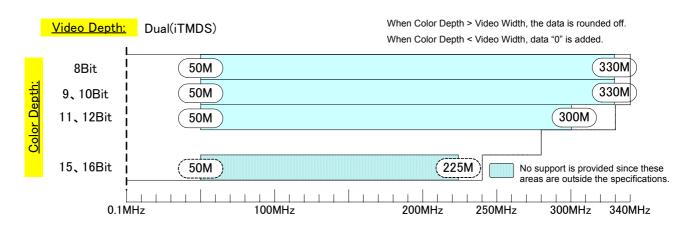
Restrictions on dot clock frequency for4K2K( iTMDS) (DVI MODE/Quad Link)



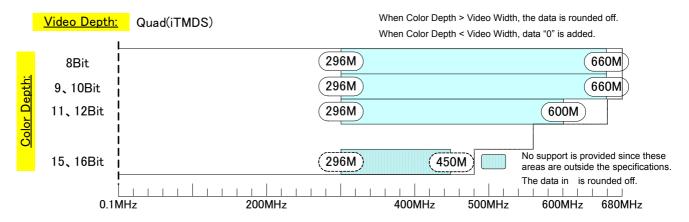
Restrictions on dot clock frequency for4K2K( iTMDS)(DVI MODE/Octal Link)



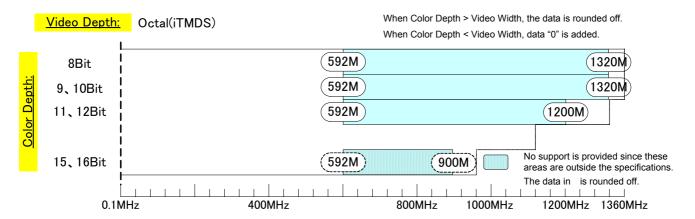
#### Restrictions on dot clock frequency for iTMDS(iTMDS MODE/Single Link)



#### Restrictions on dot clock frequency for iTMDS(iTMDS MODE/Dual Link)



Restrictions on dot clock frequency for iTMDS(iTMDS\_MODE/Quad Link)



#### Restrictions on dot clock frequency for iTMDS(iTMDS MODE/Octal Link)

For details on the pattern drawing bit length (Color Depth), refer to "4.1.5 Setting the bit length (gray scale) for pattern drawing."

For details on the output image bit length (Mode), refer to "4.4.2 iTMDS (4K×2K) unit setting procedure."

# 12.2.9 V-by-One HS unit

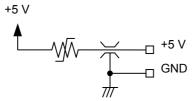
With the V-by-One HS unit, the dot clock frequencies are not restricted regardless of the pattern drawing bit length (Color Depth).

# 12.3 Concerning the maximum current consumption of the DDC (DP\_PWR) power supply

DDC power (DP PWR in case of DisplayPort output) is supplied to the outputs of the VG-870B/871B.

The maximum currents supplied by the DDC power supply are as listed below.

- HDMI output: 0.050 A for each channels
- DVI output: 0.5 A total for 2 channels
- LVDS 4-channel output: 0.5 A total for channels 1 to 4, and max. 0.5 A per channel
- Parallel output (2 channels): 0.5 A total for channels 1 and 2, and max. 0.5 A per channel
- TV encoder output: 0.5 A total for all channels
- PC analog output: 0.5 A total for all channels
- DisplayPort output: 0.5 A total for all channels
- 1) The DDC supply voltage is output as shown in the figure below.



DDC power supply output circuit

- 2) The supply voltage differs depending on the output connector.
  - · HDMI output: Fixed at 5 V.
  - · DVI output: Fixed at 5 V.
  - LVDS output: Can be switched between 5 V and 3.3 V using a rear panel switch.
  - Parallel output: Can be switched between 5 V, 3.3 V, 2.5 V and 1.8 V using a rear panel switch.
  - TV encoder output: Fixed at 5 V.
  - · PC analog output: Fixed at 5 V.
  - · DisplayPort output: Fixed at 3.3 V.



- The DDC power supply incorporates an overcurrent protection device, but do not use a current which exceeds the rating.
- Do NOT supply power to the DDC power supply from the device connected to the VG. If such the voltage of such a power supply is connected, both the VG and the connected device may fail.



# 13 LIST OF ERROR MESSAGES

#### **Media-related error** 13.1

Code (HEX)	Error message	Description
217	Flash ROM(User) Full	There is not enough free space in the internal memory.
228	No CF-Card	The CF card has not been inserted.
229	CF-Card Unformatted	The CF card has not been formatted.
22A	CF-Card Full	There is not enough free space on the CF card.
22C	OPT Data File Error	Error in the optional pattern data.
22F	Image Data File Error	Error in the image data.
233	Audio Flash Data Already Exist	The audio data has already been registered.
235	Audio Flash File Error	Error in the audio data.
236	Audio Flash Data Full	The maximum amount of audio data which can be registered has been exceeded.

# 13.2 General error

Code (HEX)	Error message	Description
302	'H-Timing DotClock' Over Limit	Dot clock in the horizontal timing data is outside the setting range.
303	'H-Timing Frontp' Over Limit	Frontp in the horizontal timing data is outside the setting range.
305	'H-Timing HD' Over Limit	HDstart+HDwidth in the horizontal timing data is outside the setting range.
307	'H-Timing Period' Over Limit	Period in the horizontal timing data is outside the setting range.
308	'H-Timing Disp' Over Limit	Disp in the horizontal timing data is outside the setting range.
309	'H-Timing Sync' Over Limit	Sync in the horizontal timing data is outside the setting range.
30A	'H-Timing Backp' Over Limit	Backp in the horizontal timing data is outside the setting range.
30B	'H-Timing Blanking' Over Limit	Blanking in the horizontal timing data is outside the setting range.
30C	H-Frequency Over Limit	The horizontal sync frequency in the horizontal timing data is outside the setting range.
30D	'H-Timing' Data Error	Error other than those described above in the horizontal timing data.
310	'Output' Data Error"	Error in the output condition data.
311	'Character' Data Error"	Error in the character pattern data.
312	'Cross Hatch' Data Error"	Error in the crosshatch pattern data.
313	'Dot' Data Error"	Error in the dot pattern data.
314	'Circle' Data Error"	Error in the circle pattern data.

Code (HEX)	Error message	Description
315	'Burst' Data Error"	Error in the burst pattern data.
316	'Window' Data Error"	Error in the window pattern data.
317	'Color Bar' Data Error"	Error in the color bar pattern data.
318	TERMINAL) Parameter Error	Error in a parameter in the terminal mode.
319	TERMINAL) Data Error	Error in the data in the terminal mode.
31B	'Video/Setup/Sync Level' Error	The video level (Video), setup level (Setup) and sync signal level (Sync) are outside the setting range. (Setting range: [Video ≥ Setup] and [Video ≥ Sync] and [Video ≥ (Setup + Sync)])
31E	TERMINAL) Communication Timeout	Time-out has occurred in the data during communication in the terminal mode.
31F	TERMINAL) Undefined Command	An undefined command was received in the terminal mode.
321	'Program No.' Error"	Error in the program number.
322	'Group No.' Error"	Error in the group number.
323	'Character Code' Error"	Error in a user character code.
32B	'OPT No.' Error"	Error in the optional pattern number.
32D	OPT Data File Not Found"	The optional pattern has not been registered.
32E	'Image No.' Error"	Error in the image pattern number.
330	Image Data File Not Found"	The image pattern has not been registered.
333	CURSOR Not Selected	The cursor pattern has not been selected (when SP-8870 CurTool is used).
334	EDID Read Port Not Found	The EDID read port is not found. (The unit has not been installed.)
338	'Gray Scale' Data Error	Error in the gray scale pattern data.
339	'OPT/Image' Data Error"	Error in the optional pattern or image pattern data.
33B	'Cursor' Data Error	Error in the cursor pattern data.
33C	'Program Name' Data Error	Error in the program name data.
33D	'□×[ABC] Color' Data Error	Error in the □ × [ABC] color data.
33E	'Action' Data Error"	Error in the action data.
340	'V-Timing Total' Over Limit	Total in the vertical timing data is outside the setting range.
341	'V-Timing Disp' Over Limit"	Disp in the vertical timing data is outside the setting range.
342	'V-Timing Sync' Over Limit	Sync in the vertical timing data is outside the setting range.
343	'V-Timing Backp' Over Limit	Backp in the vertical timing data is outside the setting range.
344	'V-Timing Frontp' Over Limit	Frontp in the vertical timing data is outside the setting range.
345	'V-Timing Blanking' Over Limit	Blanking in the vertical timing data is outside the setting range.
346	V-Frequency Over Limit	The vertical sync frequency in the vertical timing data is outside the setting range.
347	'V-Timing VD' Over Limit	VDstart+VDwidth in the vertical timing data is outside the setting range.

Code (HEX)	Error message	Description
348	'V-Timing EQP-Fp' Over Limit	EQP-FP in the vertical timing data is outside the setting range.
349	'V-Timing EQP-Bp' Over Limit	EQP-BP in the vertical timing data is outside the setting range.
34A	'V-Timing' Data Error	Error other than those described above in the vertical timing data.
34E	DDC2 Line Error	ACK was not received in DDC2.
350	Macrovision Not Supported	An IC supporting Macrovision has not been installed in the unit.
352	EDID Header Error	Error in the EDID header.
353	EDID Check Sum Error	EDID checksum error.
354	EDID Header & Check Sum Error	Errors in both the EDID header and checksum.
355	User YPbPr Coefficient Error	Error in the color difference coefficients.
358	Audio Data No. Error	Error in the audio data number.
35A	Audio Data File Not Found	The audio data has not been registered.
35D	Lip Sync Invalid EDID Latency	Error in the EDID at the connection destination (when Mode:EDID has been selected with LipSync).
35F	Lip Sync 'EDID Port' Error	The HDMI unit is not installed (when Mode:EDID has been selected with LipSync).
360	Image License Error	The image data license has not been supplied.
361	Data File Not Found	The data (other than the optional pattern and image data) cannot be found.
362	Copy Condition Error	<ul> <li>The copy source data and copy destination data are identical.</li> <li>The number of copy source data and number of copy destination data are different.</li> </ul>
363	Image RAM Full	There is not enough free space in the image memory.  Set the high-speed drawing mode in 9.1.16 to OFF or reduce the number of data specified.

# 13.3 HDCP-related error

Code (HEX)	Error message	Description
403	HDCP) Transmitter KSV Error	KSV of the transmitter does not contain twenty '0's and '1's.
404	HDCP) Receiver KSV Error	KSV of the receiver does not contain twenty '0's and '1's.
405	HDCP) Link Check Error	During initial validation, the values did not match (R0 ≠ R0').
406	HDCP) Encryption Error	Encryption was not completed.
407	HDCP) Hot Plug Error	The device to be connected is not connected.
408	HDCP) Ri Ready Error	The ready bit of the receiver was not set high.
412	HDCP) I2C Line Error	The I2C line is not working properly.
414	HDCP) Receiver Not HDMIMode	The connected device (receiver) was not set to the HDMI mode when the HDCP version was identified as 1.1 as a result of HDCP version:1.1 or HDCP version: EDID check.
415	HDCP) Ri NG	The values of Ri and Ri' do not match.
416	HDCP) FIFO Ready Time-out	FIFO Ready fails to occur within restricted time limit.
417	HDCP) DEPTH Error	The depth number has exceeded '7'.
418	HDCP) DEVICE_COUNT Error	The count number has exceeded '127'.
419	HDCP) List Error (V'!=V)	The values of V and V' do not match.

# 13.4 User-generated optional pattern-related error

Code (HEX)	Error message	Description
501	OPT Program Not Found	The user-generated optional pattern is not found.
502	Variables Stack Error	Variable stack error.
503	Register Stack Error	Register stack error.
504	Call Stack Error	Function stack error.
505	Illegal Instruction Code	Illegal instruction code.
506	Divide by Zero	An attempt was made to divide a number by zero.
539	OPT-USER License Error	The user-generated optional pattern license has not been supplied.



## VG-870B/871B/873/874

## **Instruction Manual**

## **NOTICE**

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